

TWENTY-SEVENTH ANNUAL



TestConX™

Archive

DoubleTree by Hilton
Mesa, Arizona
March 1-4, 2026

Enhanced Test Socket Reliability:

Through Multi-Modal Failure Analysis and Design Optimization for
High-Volume Semiconductor Manufacturing

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Tektronix Component Solutions



Mesa, Arizona • March 1–4, 2026



Background Ramped Production

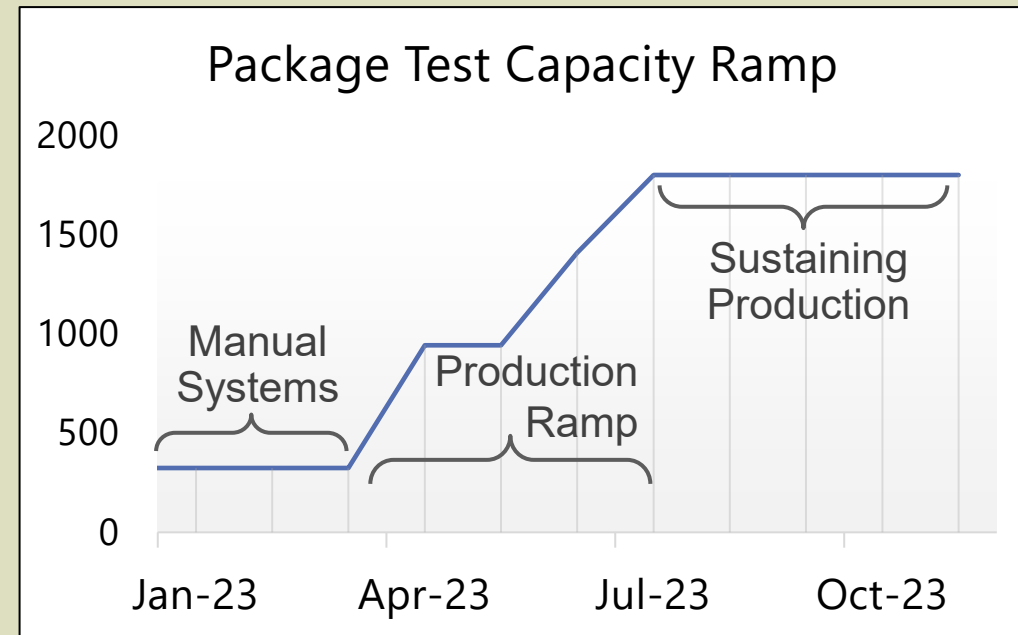
New Program Demand:

Execute **fivefold** increase in BGA package test output within a compressed timeline to meet critical program deliveries

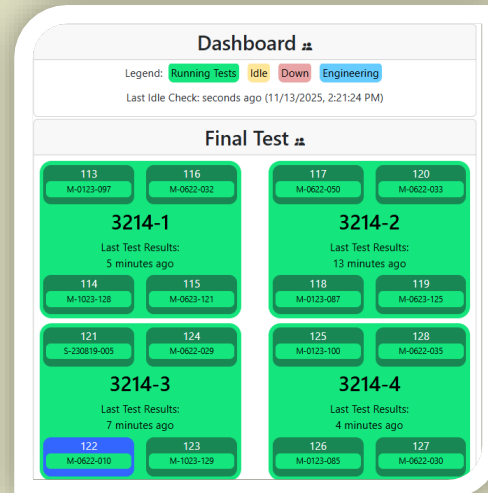
Our Solution:

- ✓ Introduce automated handling test equipment
- ✓ Workforce expansion
- ✓ Factory visibility and data capture

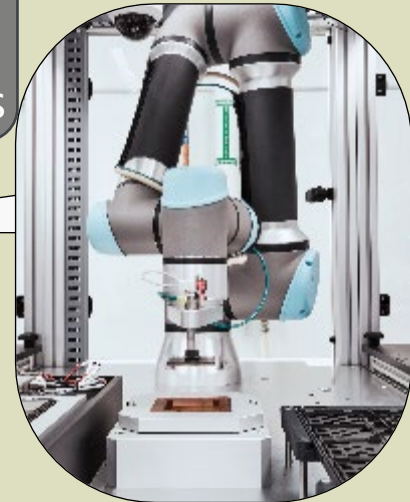
Qty. 6 Manual Single Site Test Systems



Real Time Dashboard



Additional Qty. 4 Quad-Site Robot Handler Test Systems



Background Test Application

★ **Test Highly Sensitive to CRES**



Tri-Temp



3x 30min test

Performance must be ensured at hot, cold and ambient temperatures



At-Speed

Part is tested at speed conditions as it would be used in its end application



System Level Test (SLT)

On Board peripherals to test transmission and control of external resources



IO Characterization

Instead of a simple IO check, Inputs and Outputs are characterized



Multiple Communication Methods

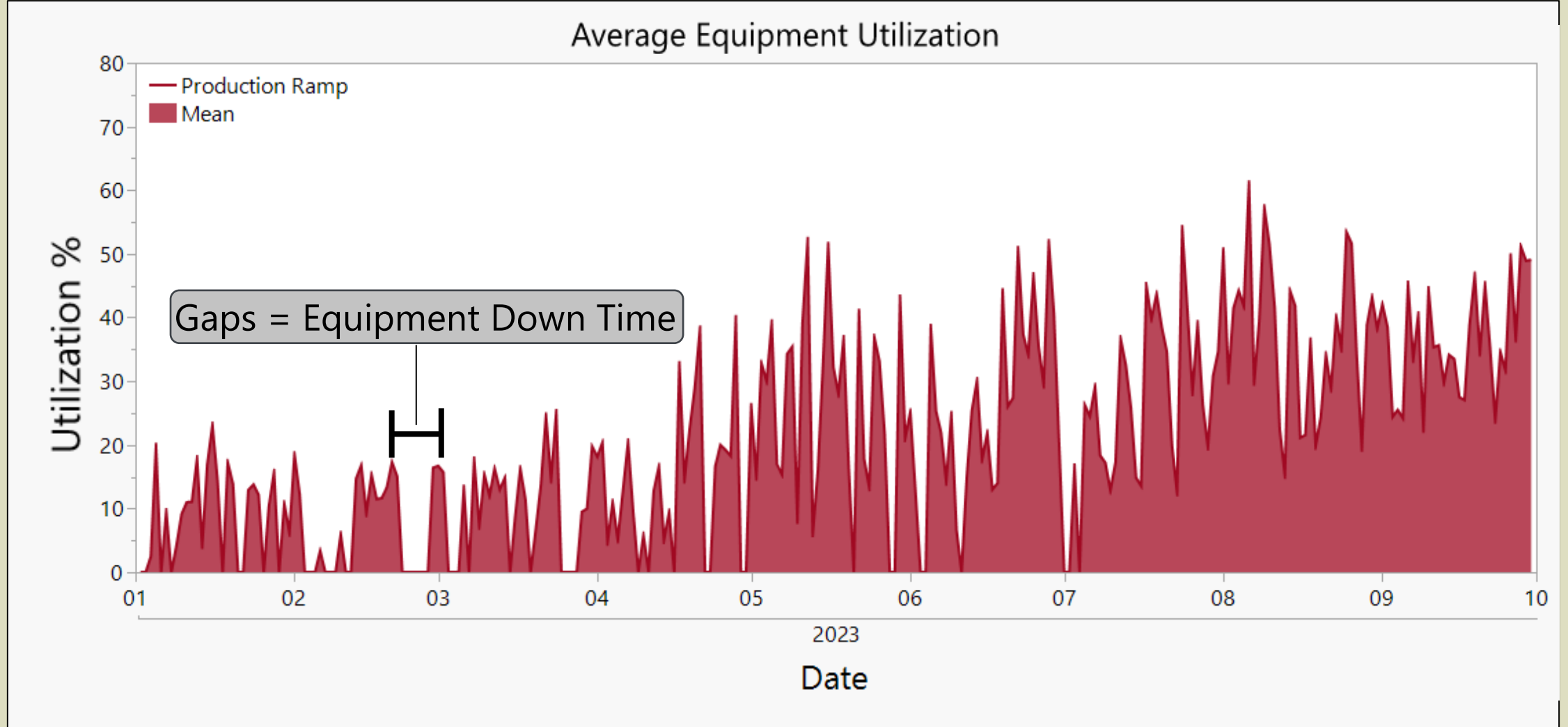
Subtests are executed to check different forms and different lines of communication between on board peripherals



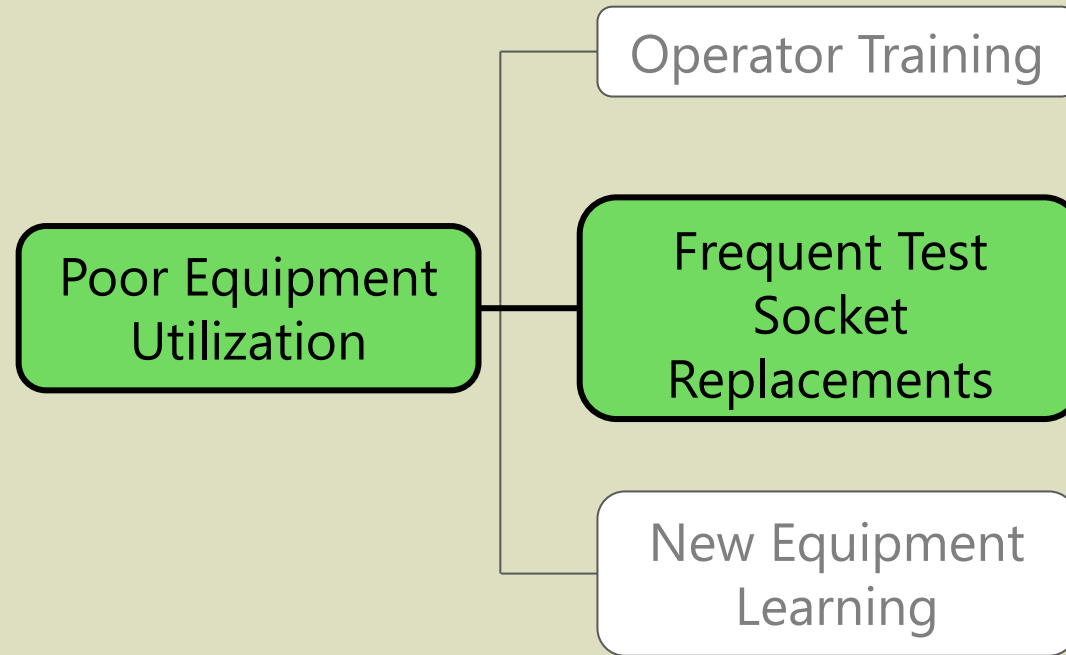
State Performance

There is extra complexity added as the chip is tested in its different operating states

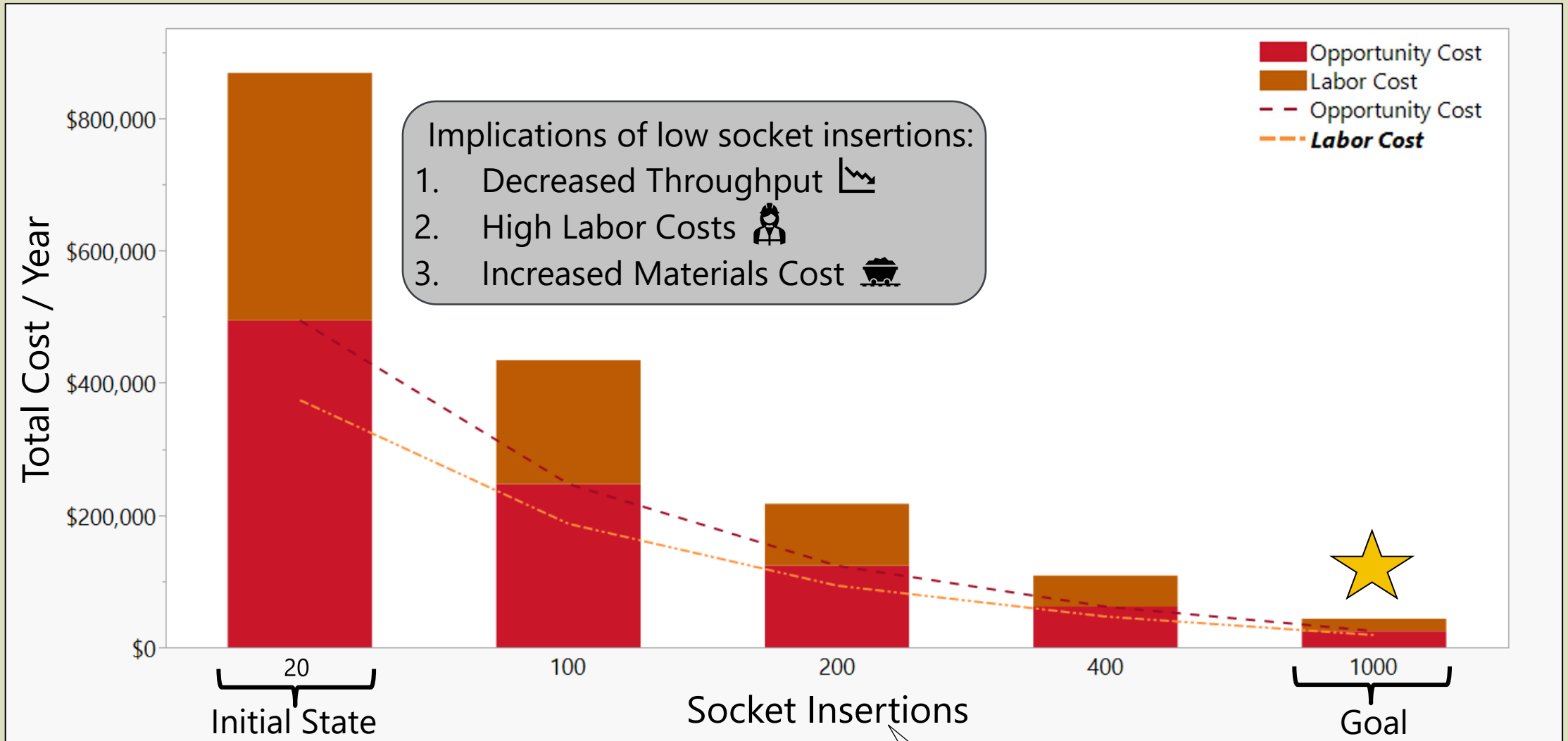
The Problem



Problem Solving Process



Motivation



The Tools

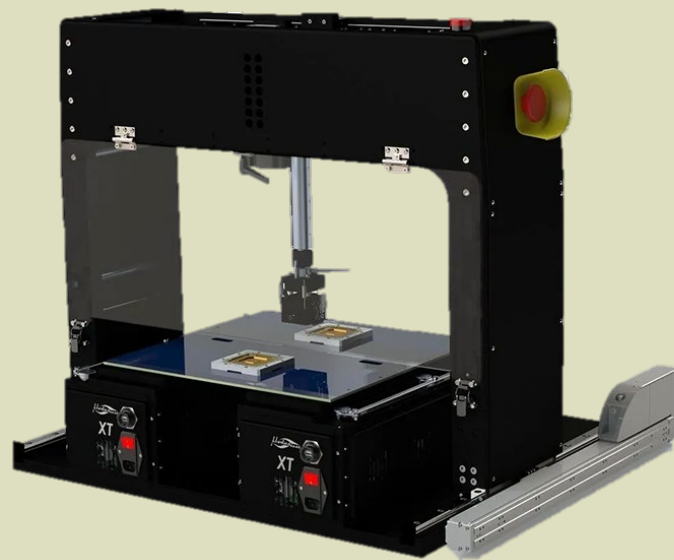
Real-Time Dashboard



Keyence VR-5000
Profilometer



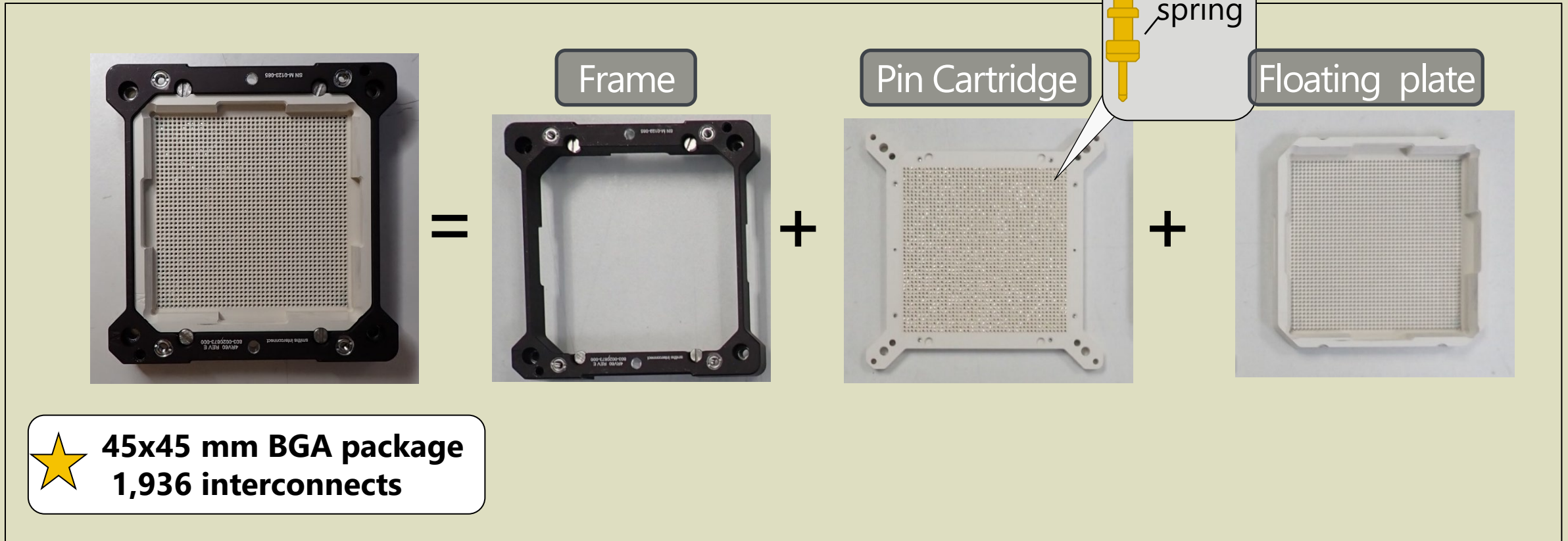
ModusTest Contact
Resistance (CRES) Tester



Hitachi TM4000
SEM

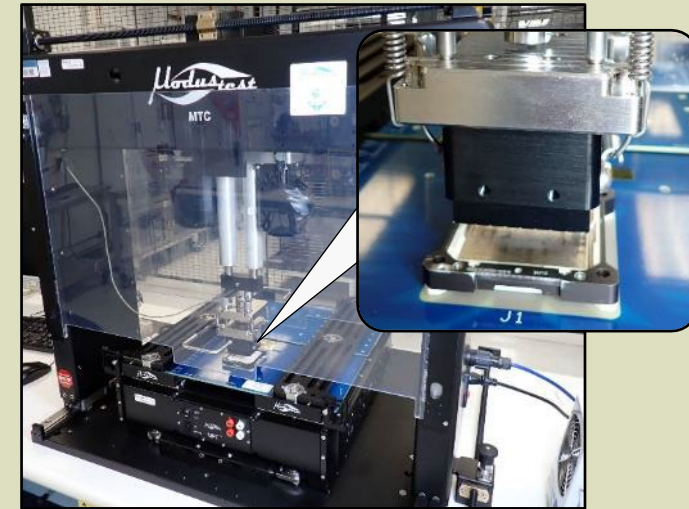
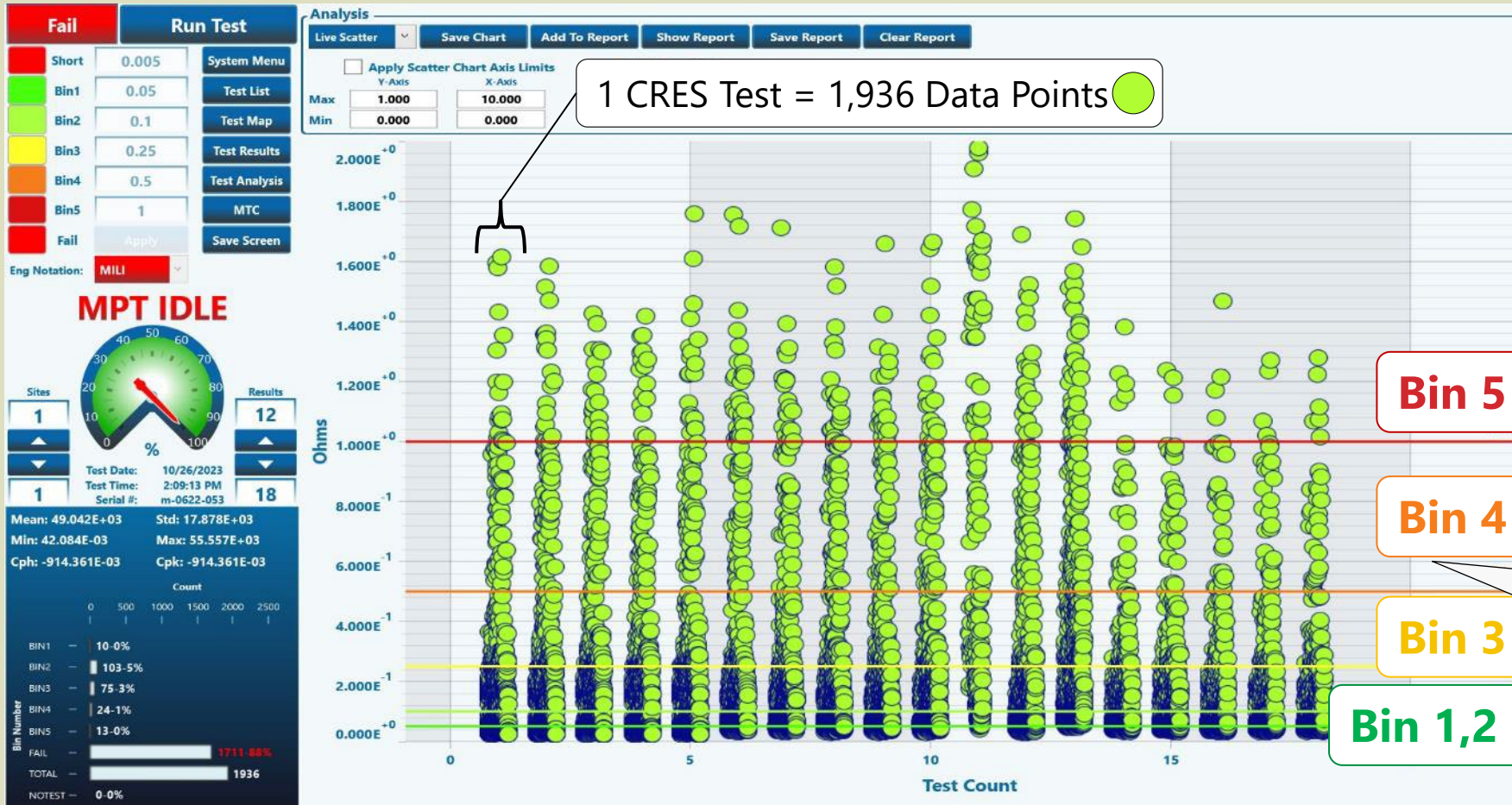


BGA Socket Introduction

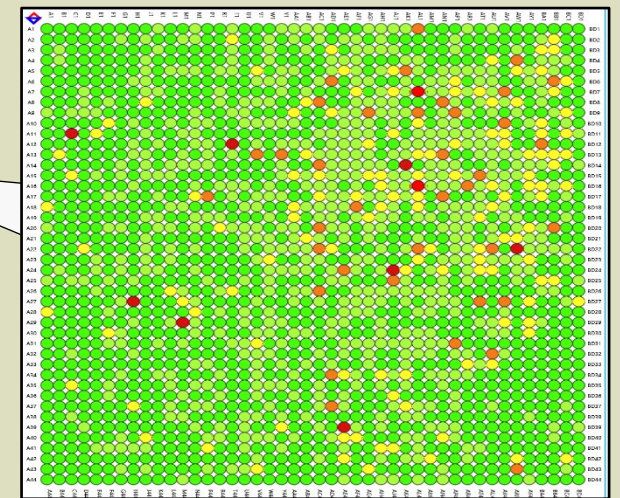


CRES Testing

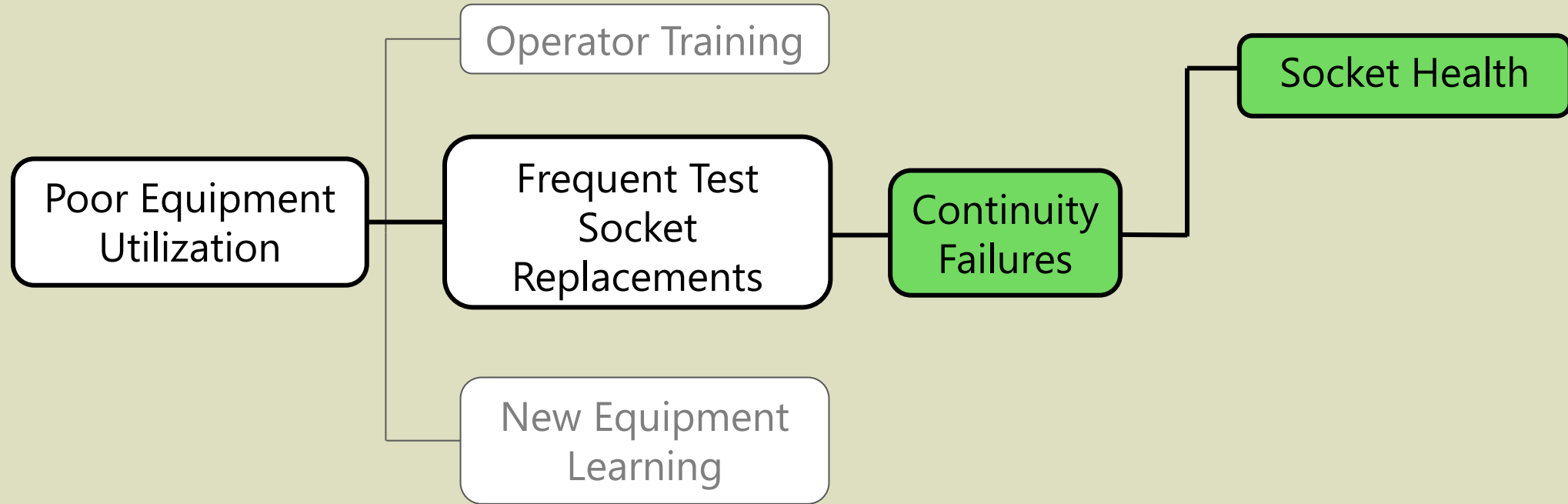
Example Scatter Plot



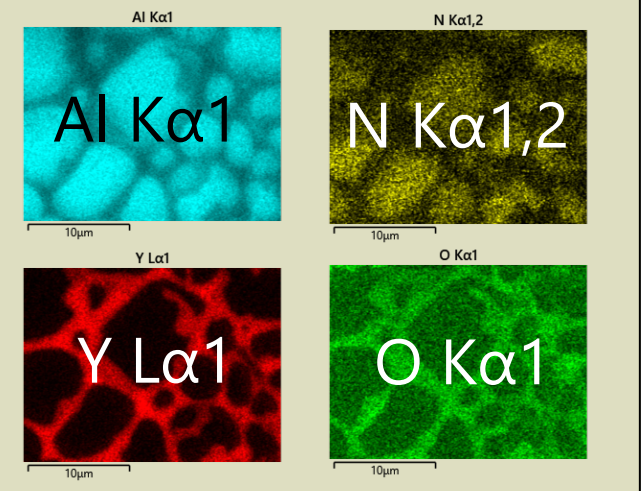
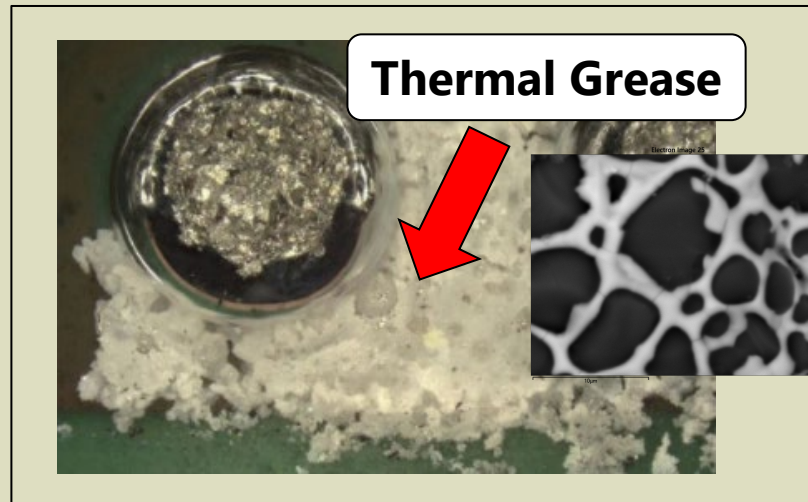
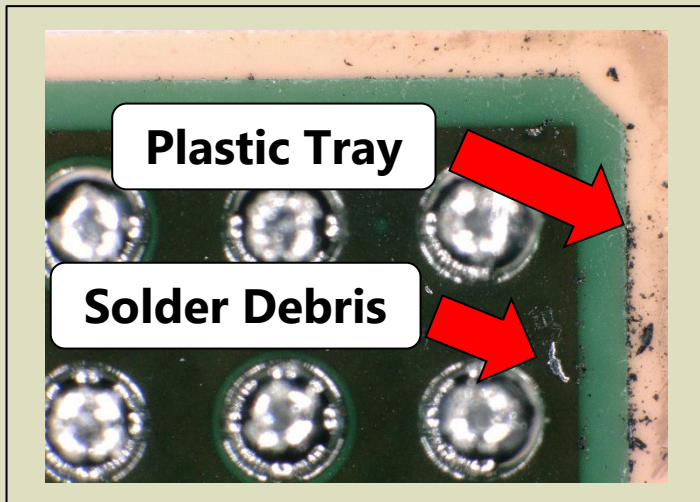
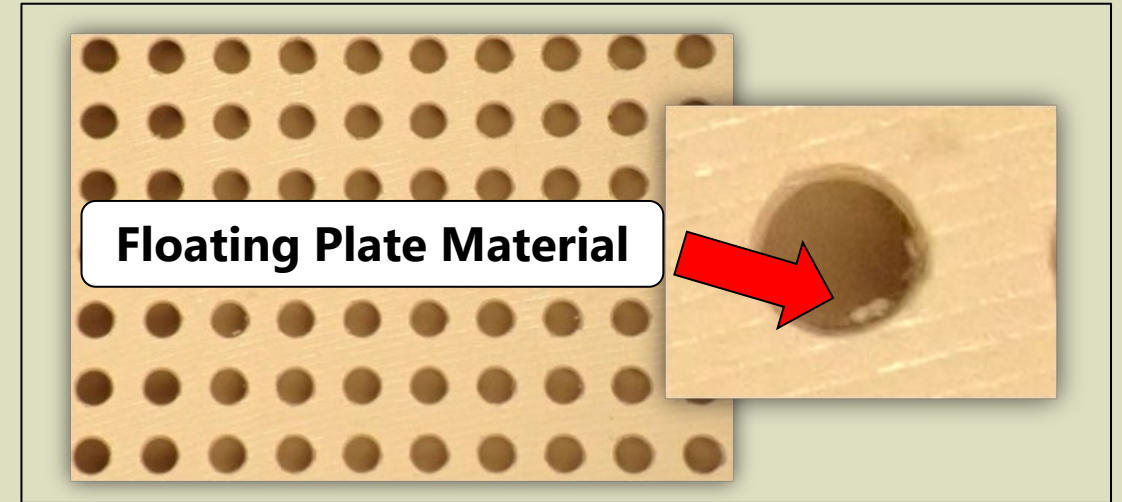
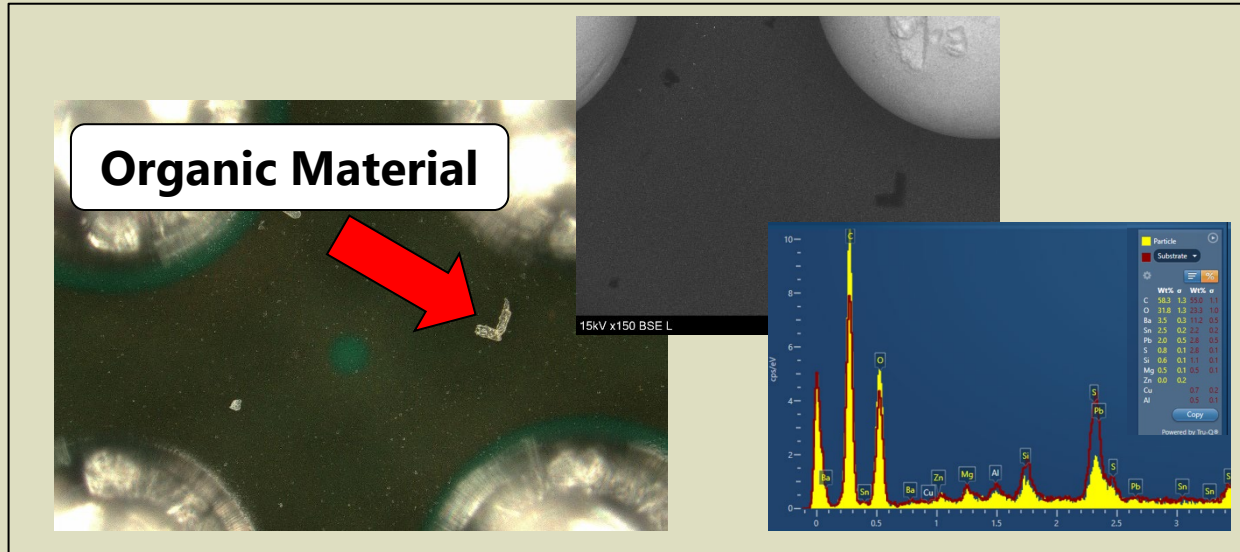
Mean Map/Measles Chart



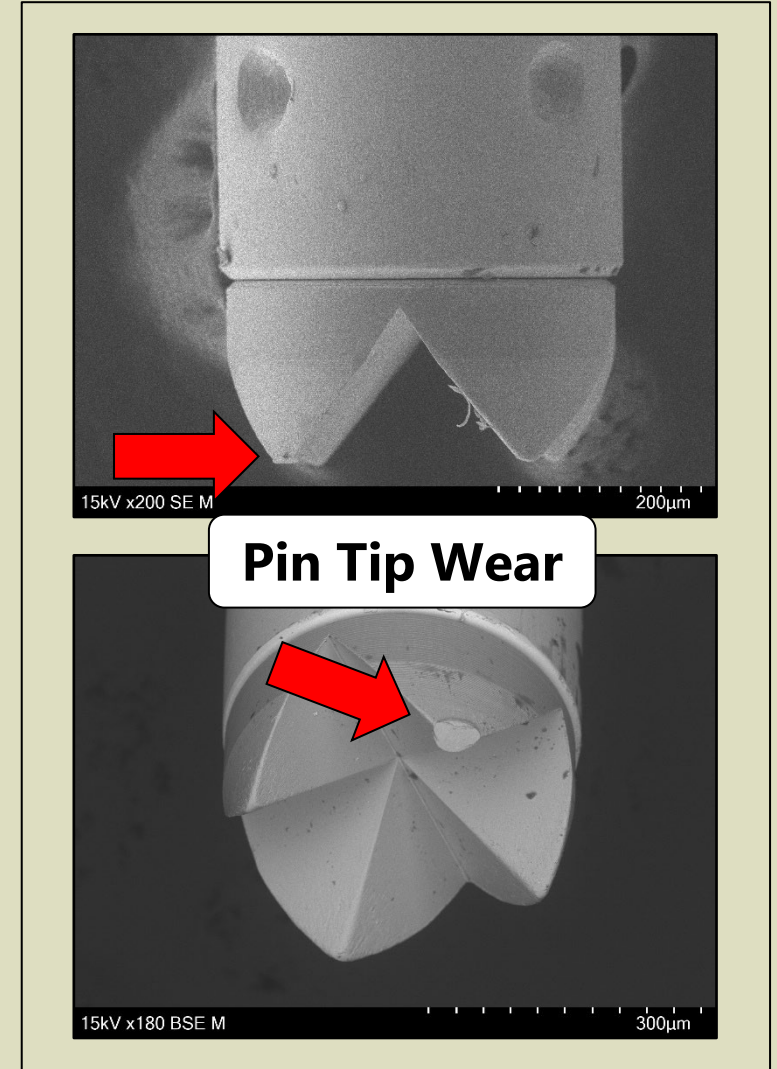
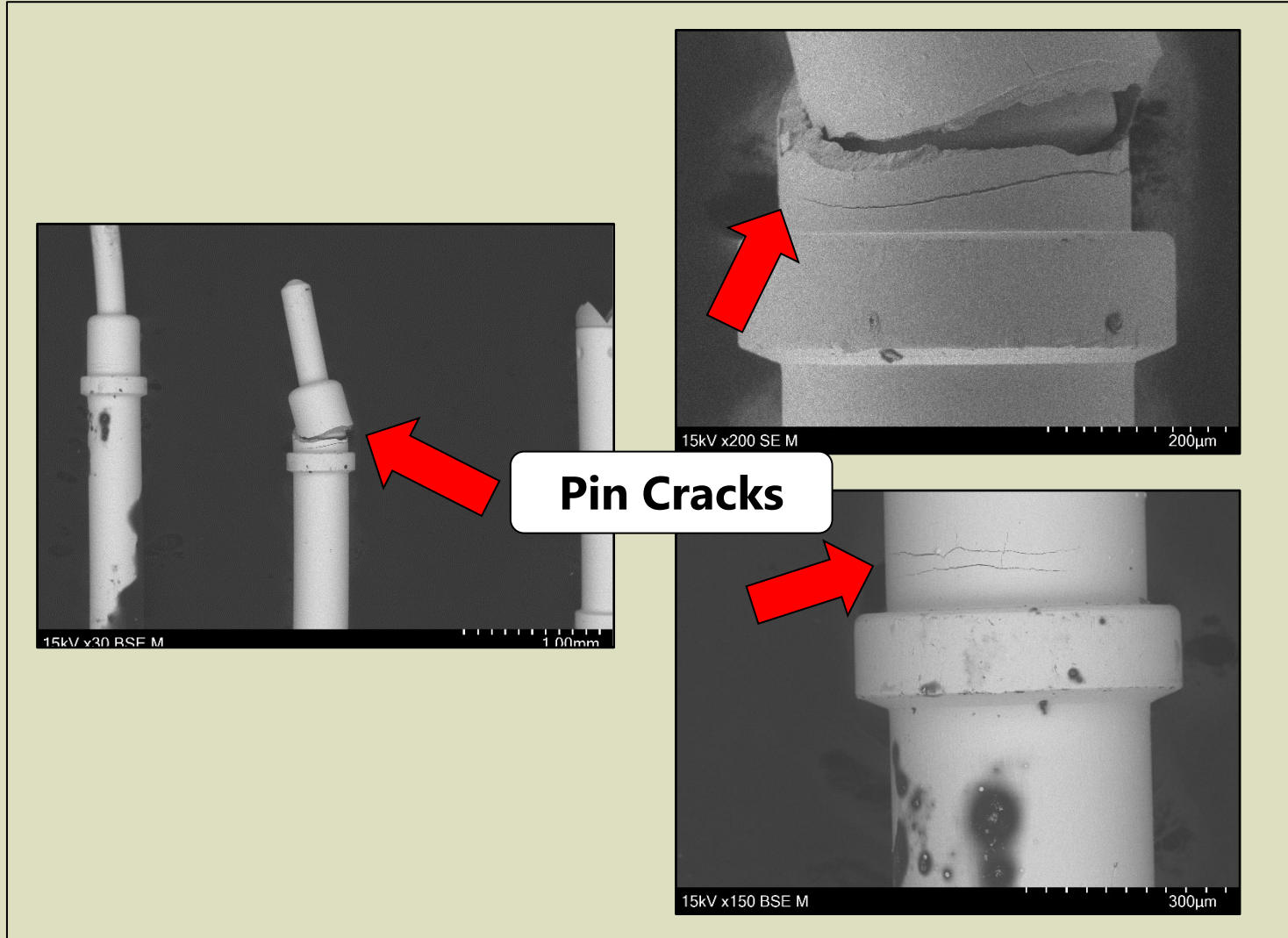
Problem Solving Process



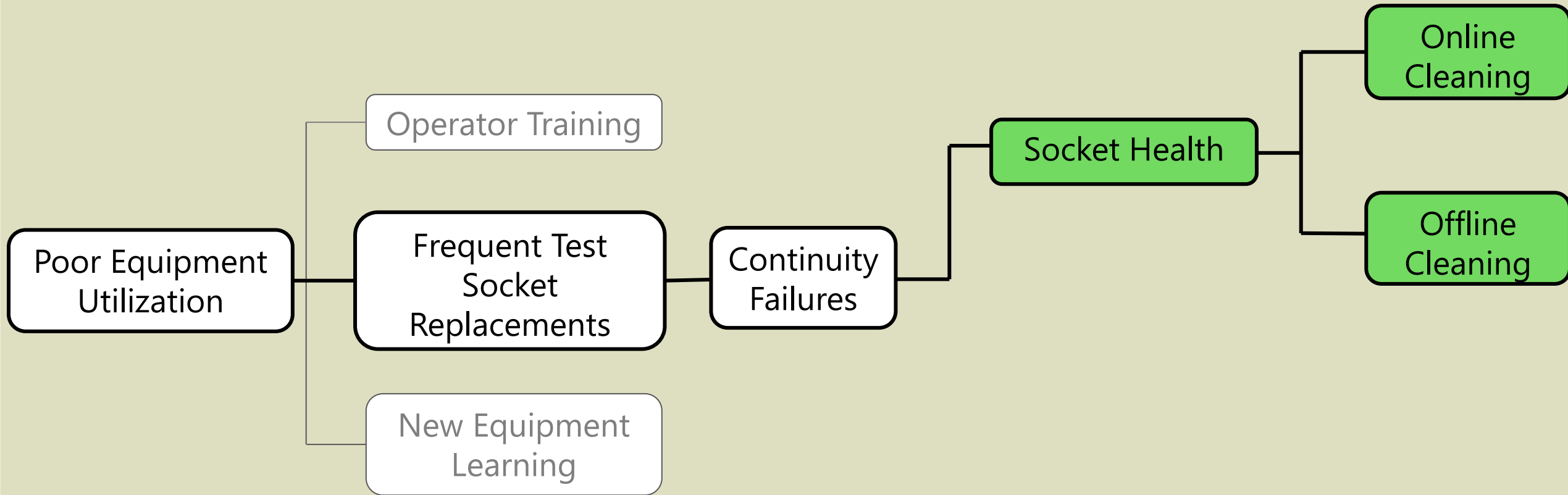
Multiple Sources of Foreign Object Debris



Pin Failure Modes



Problem Solving Process



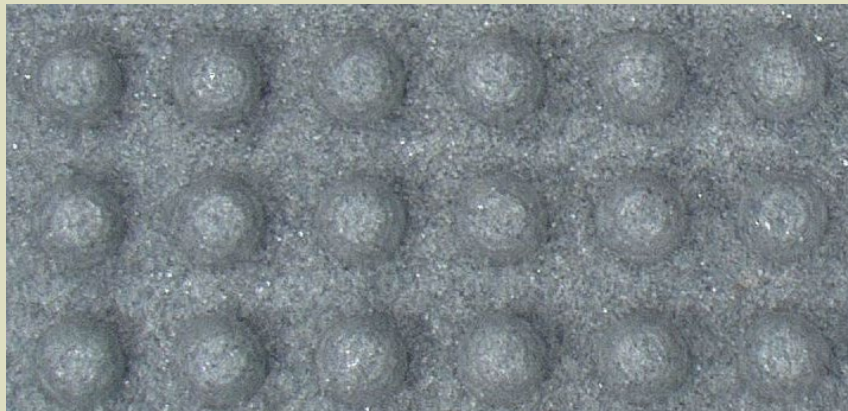
Online Cleaning

Used Cleaning Pad

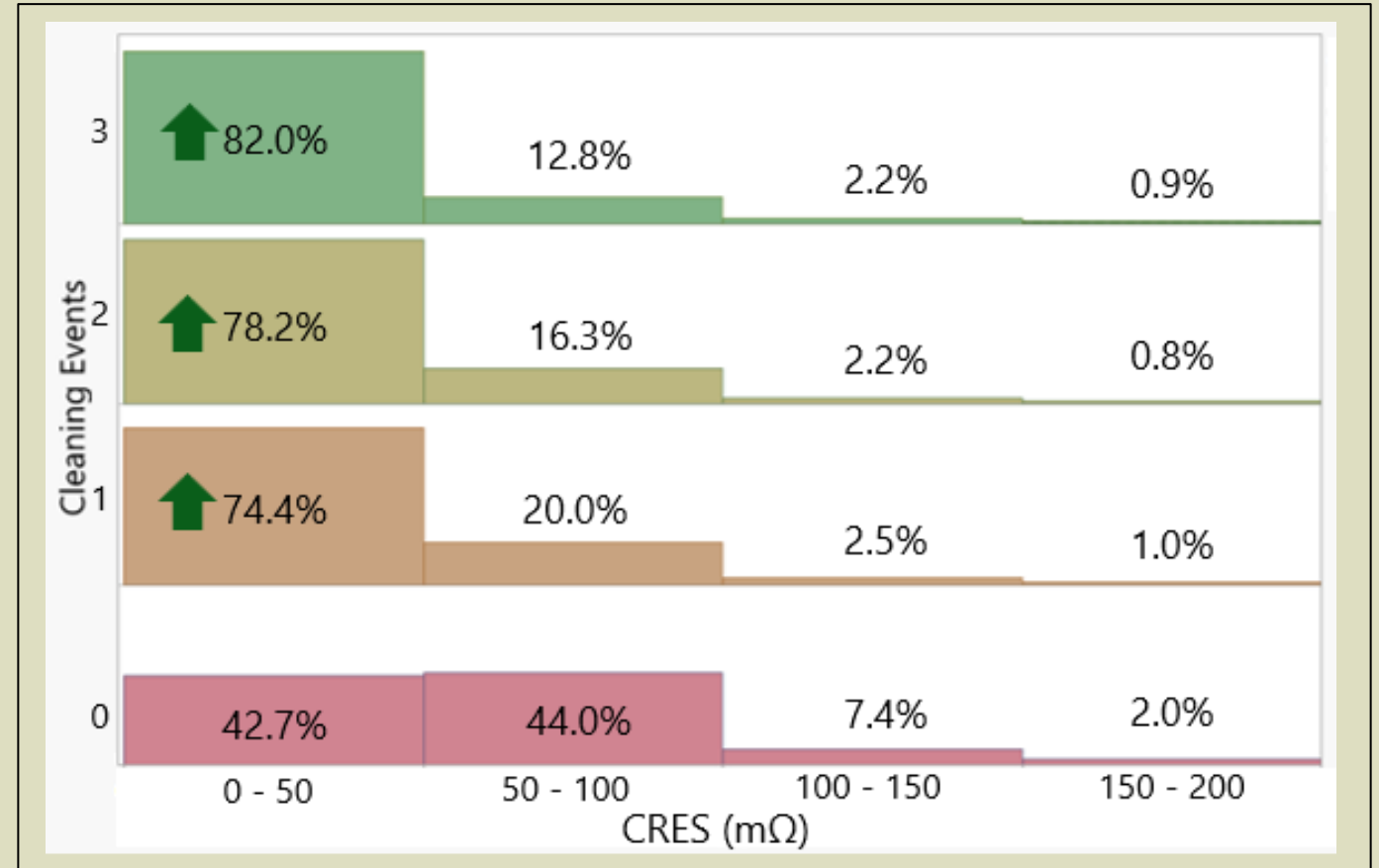


FOD from Test Sockets

New Cleaning Pad

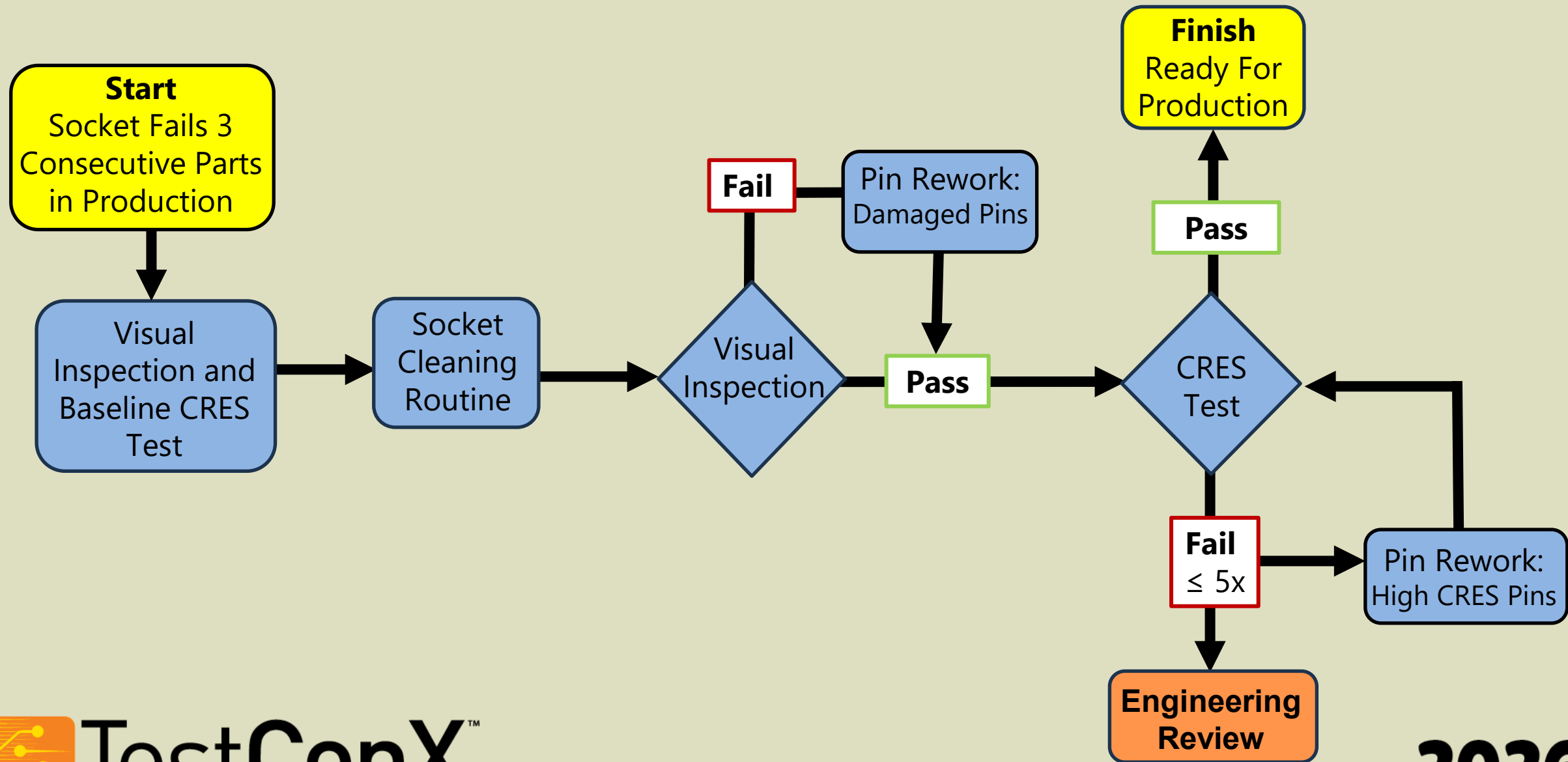


Socket Cycled with Cleaning Pad Between CRES Testing

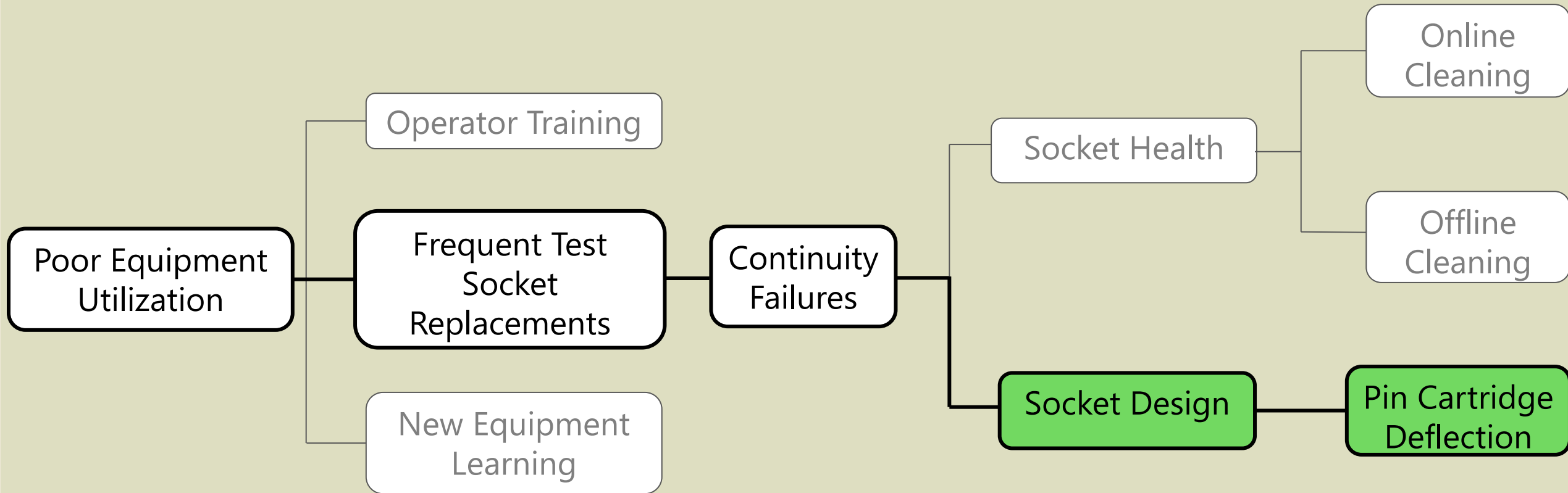


★ % High Conductivity Pins Increases with Iterative Cleaning Pad Usage

Offline Cleaning: Socket Maintenance Routine

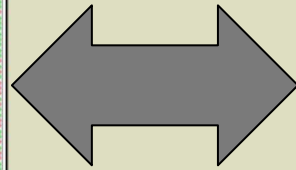
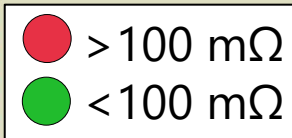
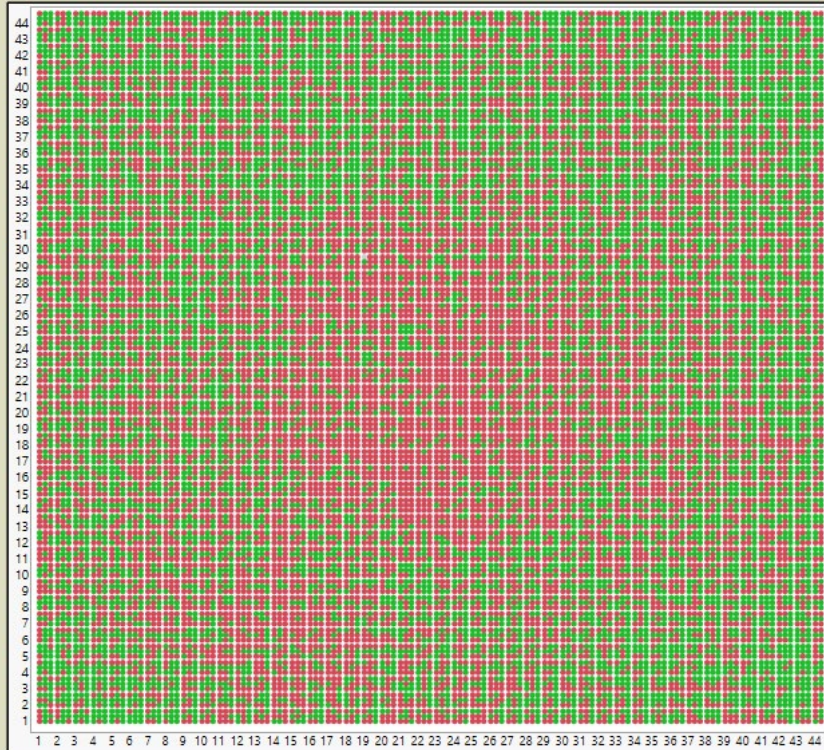


Problem Solving Process

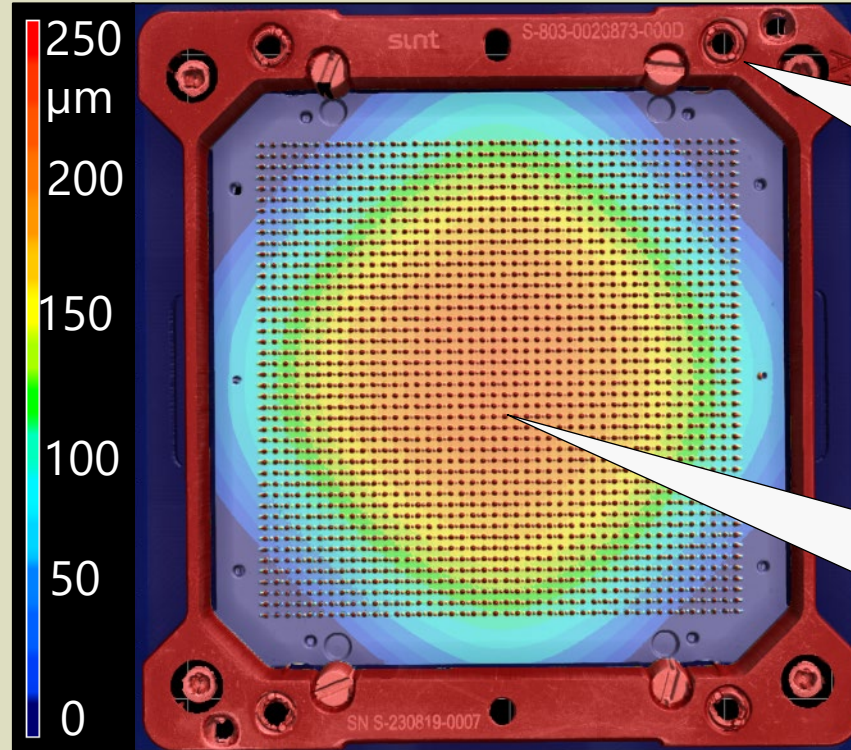


Cartridge Deflection

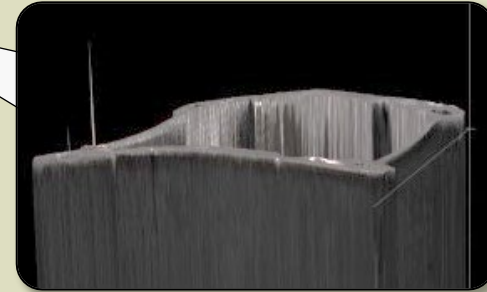
10 Socket CRES Aggregate



Topographical Heat Map



Frame Behavior



Arched Bridge Analogy

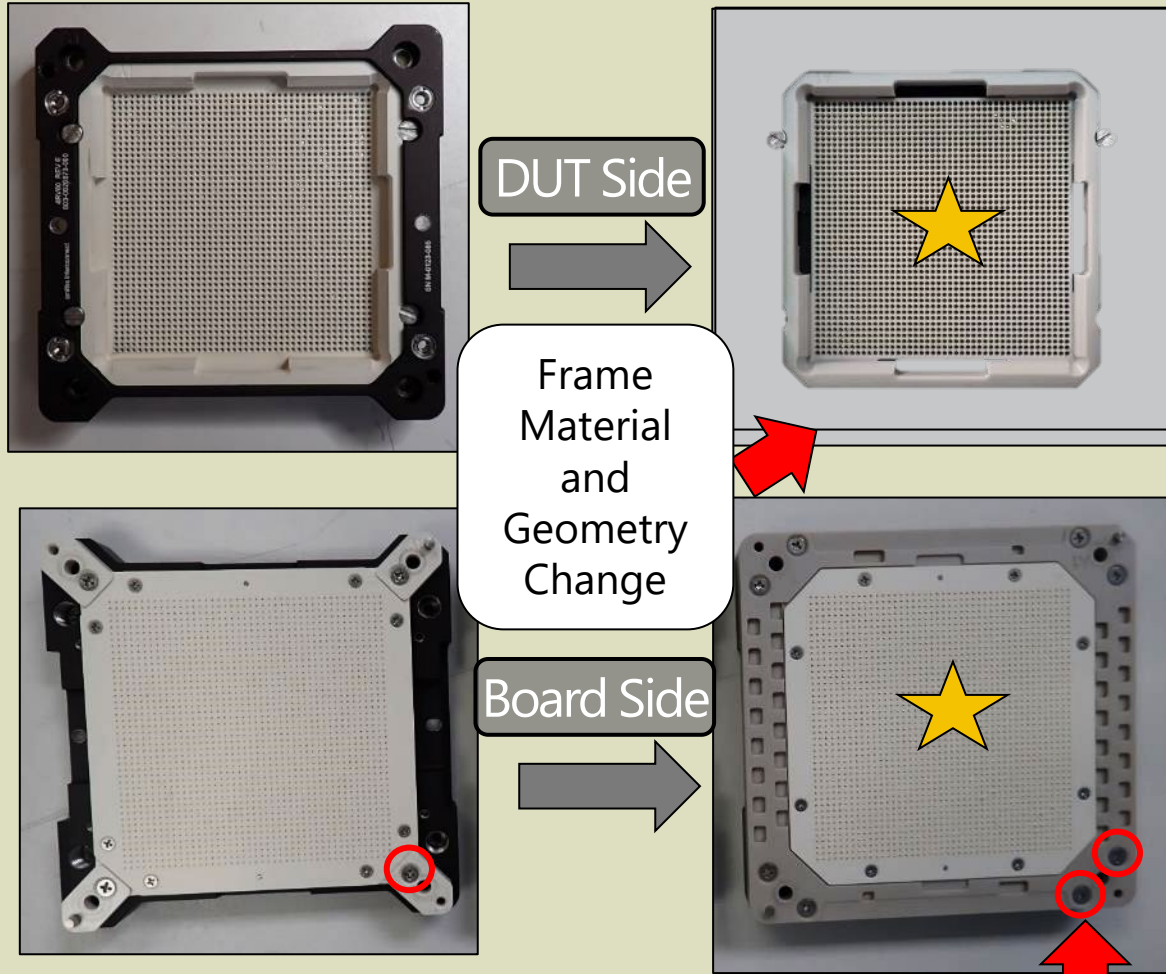


10 different socket CRES tests overlaid

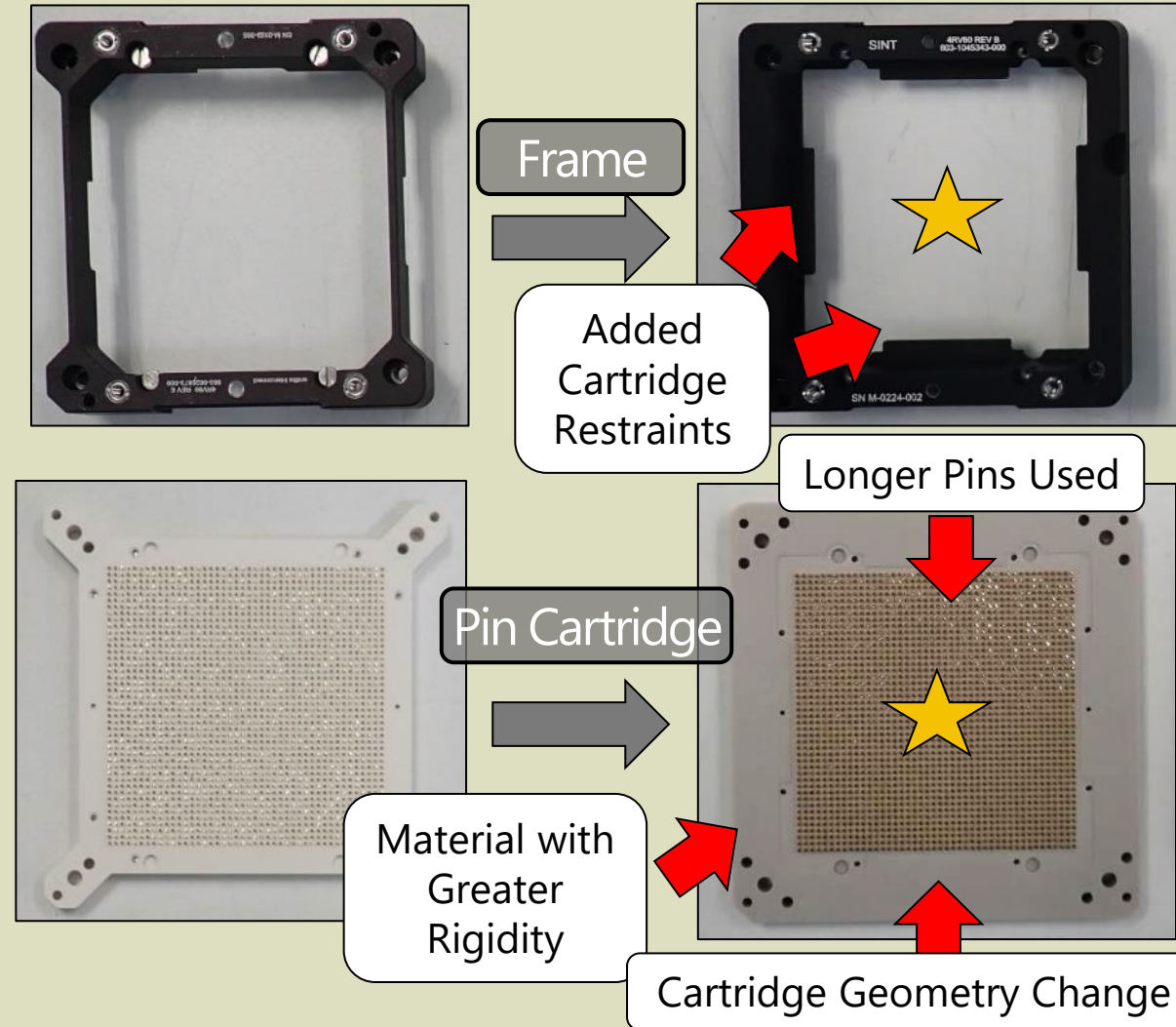
- ✓ Socket screwed to test board with 0.25 Nm torque wrench
- ✓ Floating plate removed for profilometry

Side-by-Side: Socket Design Improvement

Fully Assembled

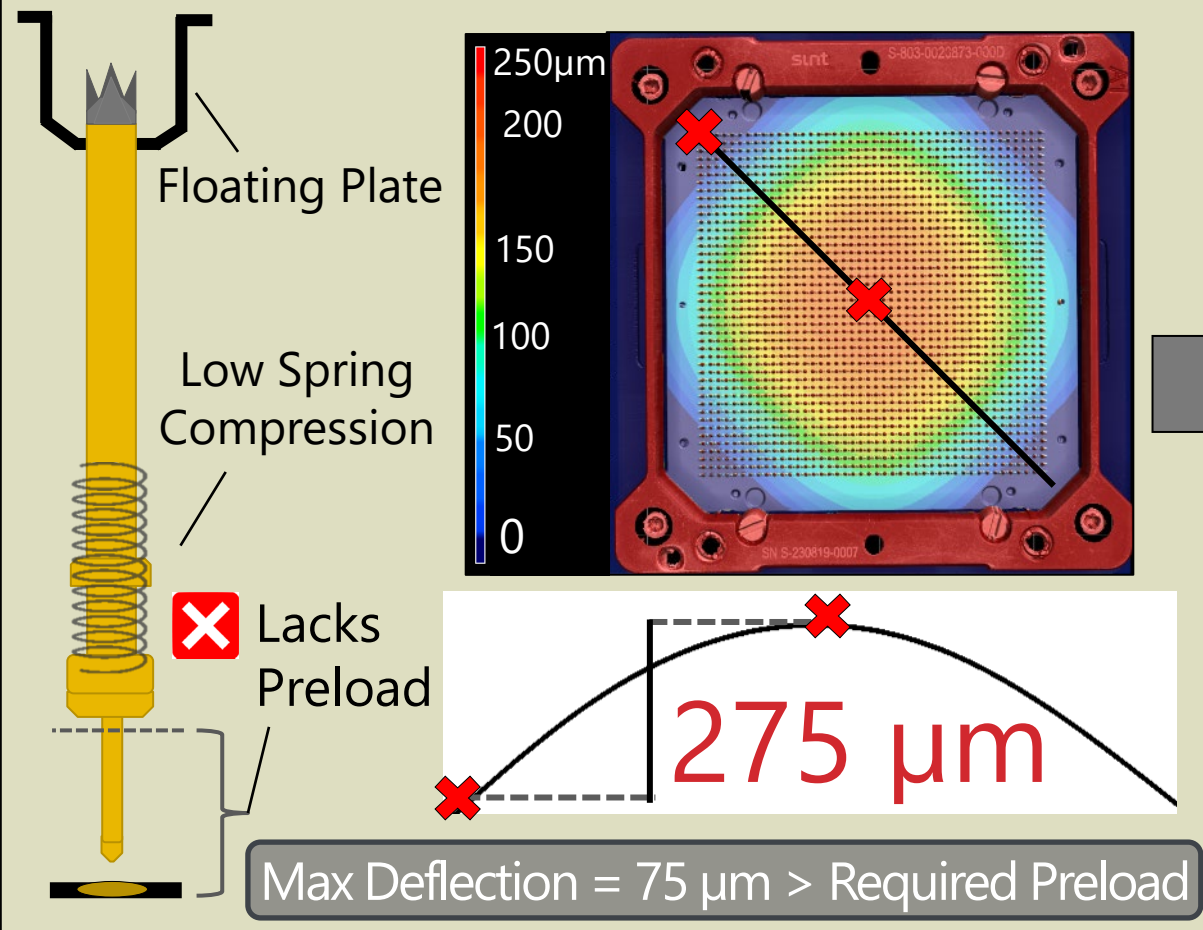


Disassembled Components

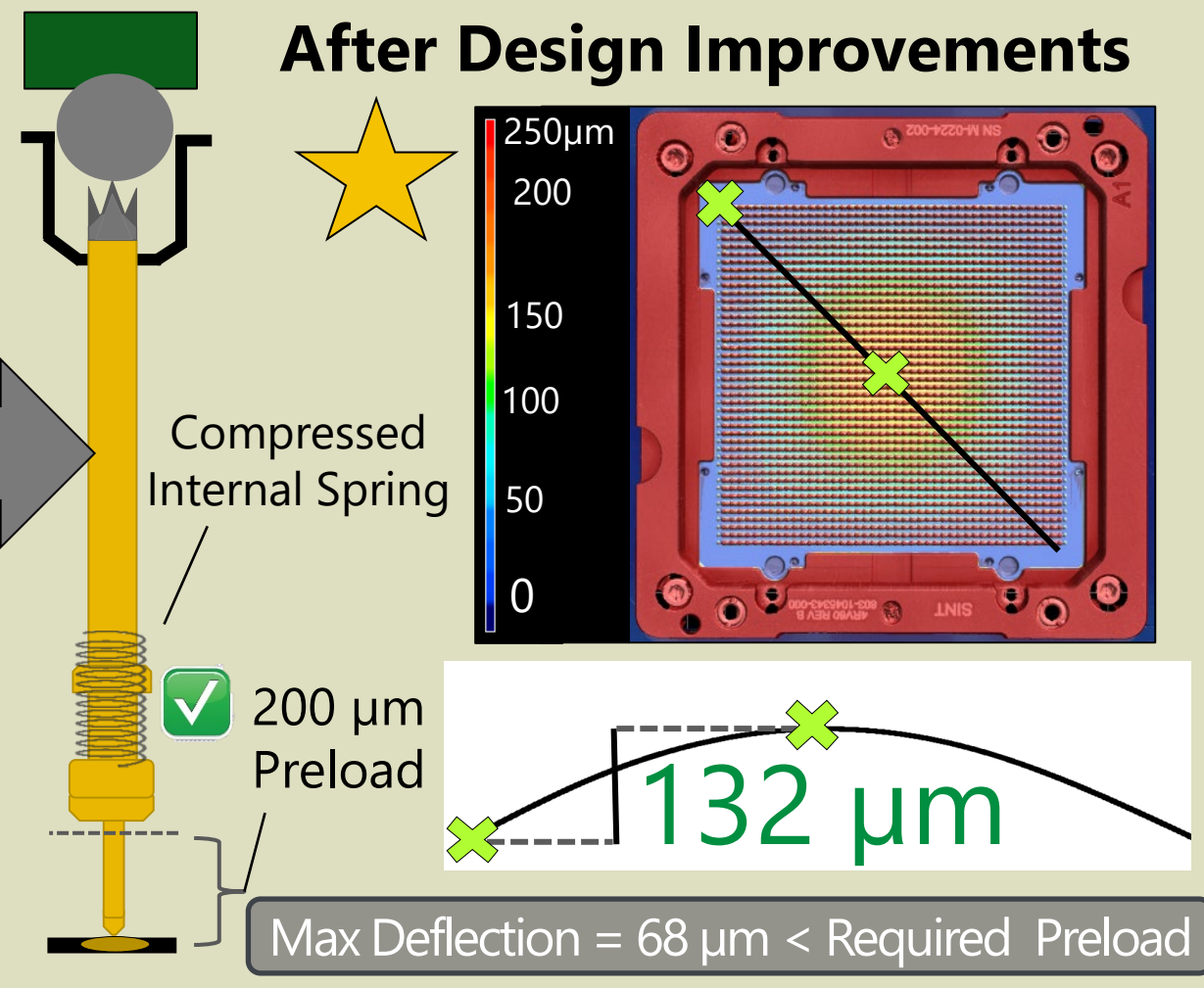


Socket Design Improvements

Original Design

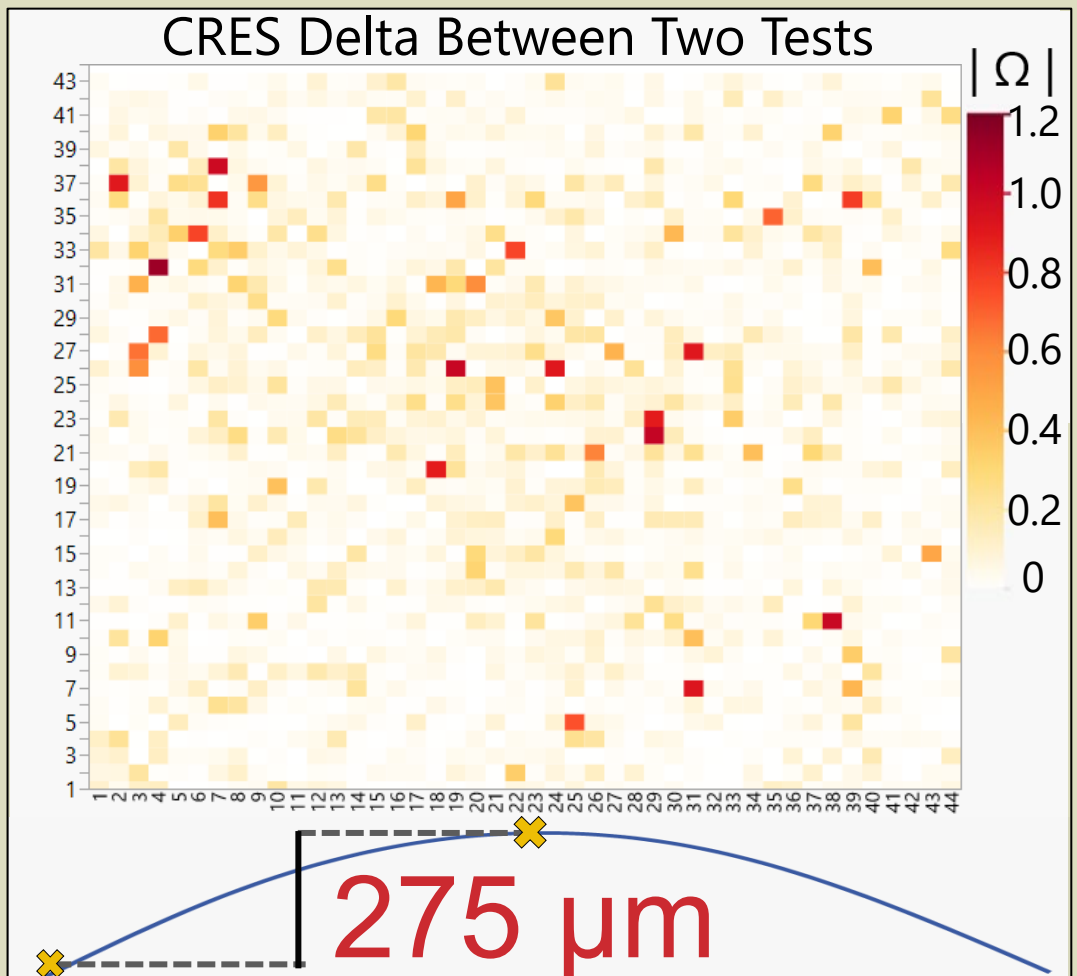


After Design Improvements

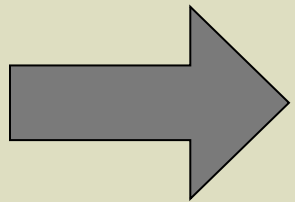
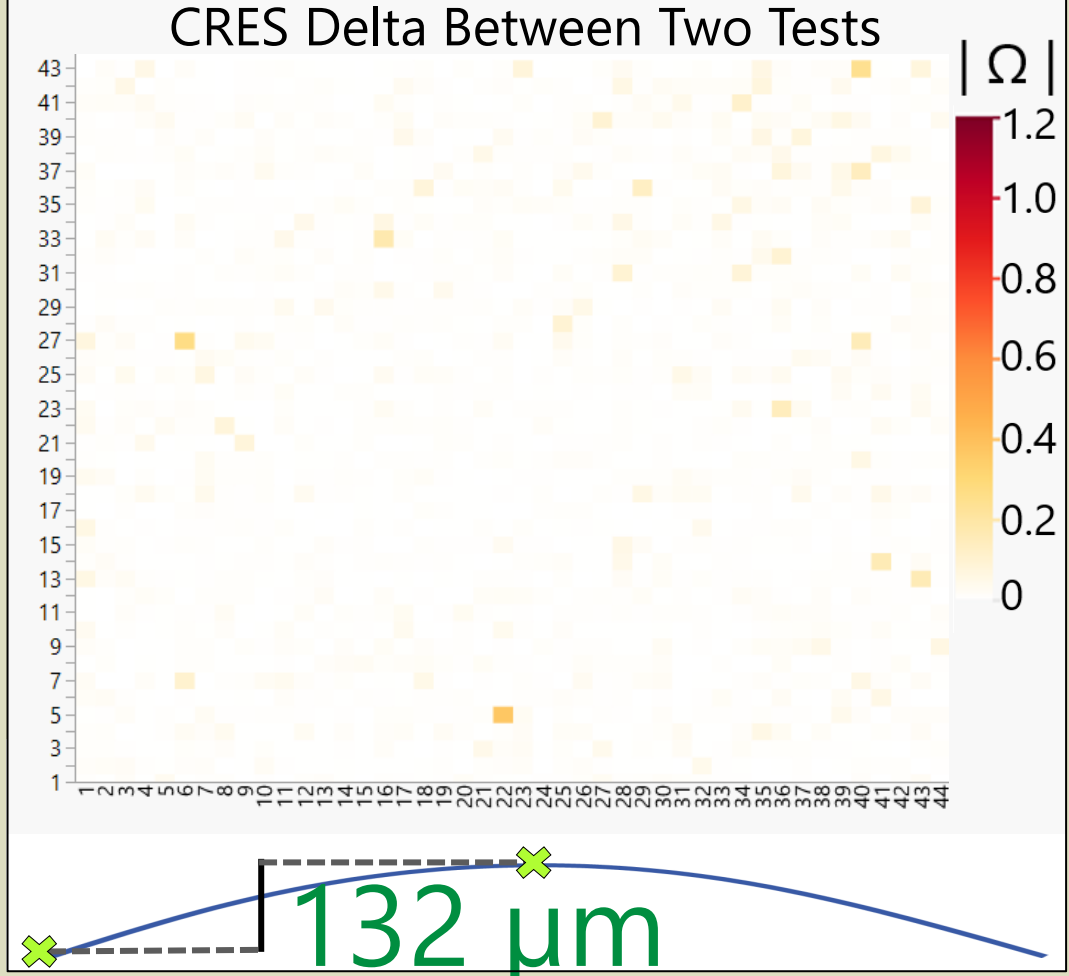


Socket Deflection and CRES Repeatability

Original Hardware



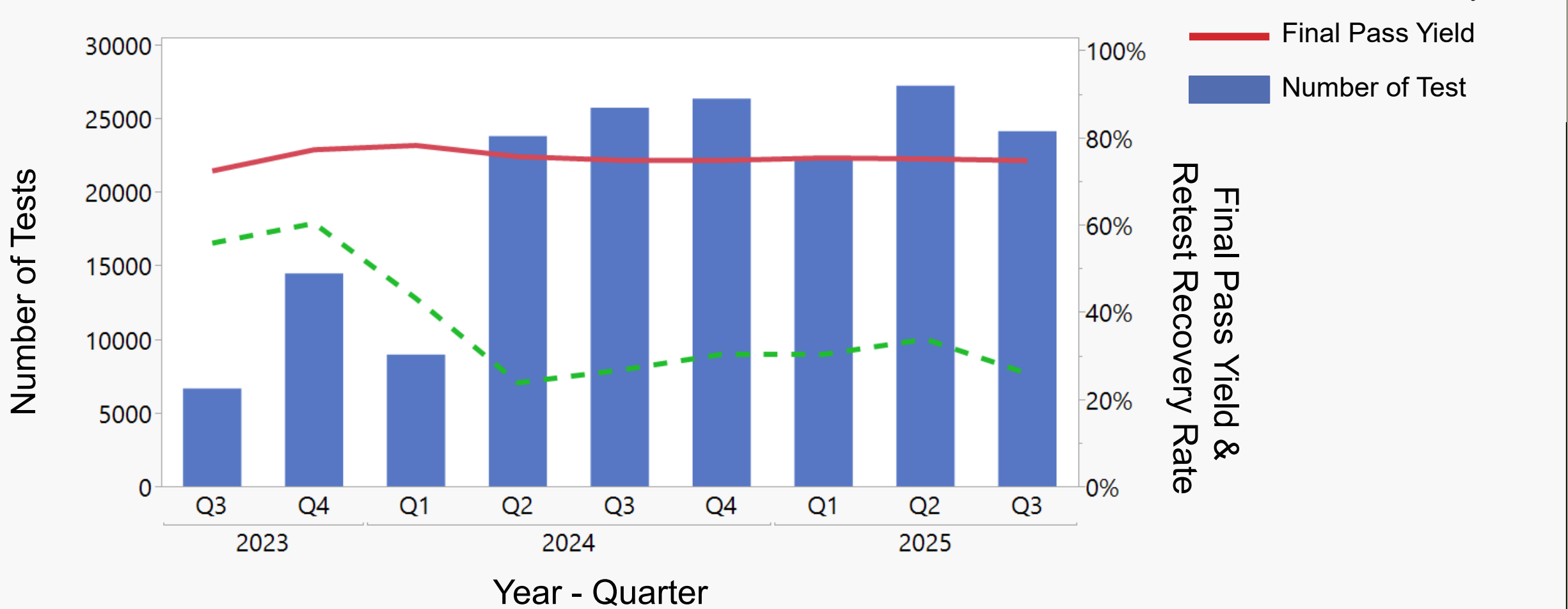
Final Socket Design



↓ Pin Cartridge Deflection = ↑ Repeatability

Yield Impact of Socket Reliability

Retest Recovery Rate and Final Yield – Production Ramp



$$\text{Retest Recovery Rate} = \frac{\text{Parts Recovered}}{\text{Number of Retests}}$$

Enhanced Test Socket Reliability

Results

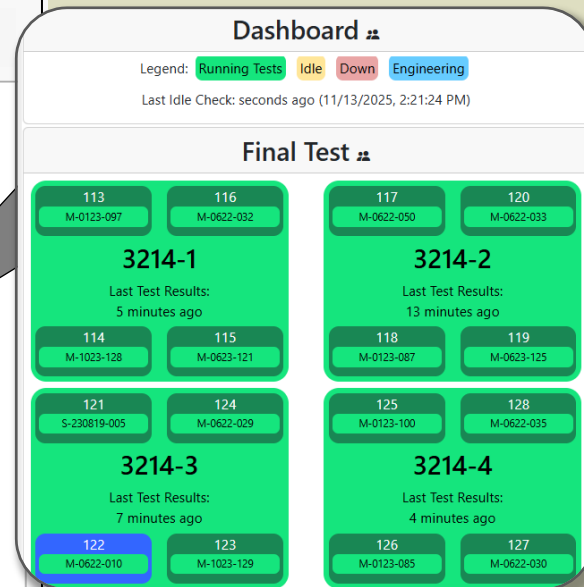
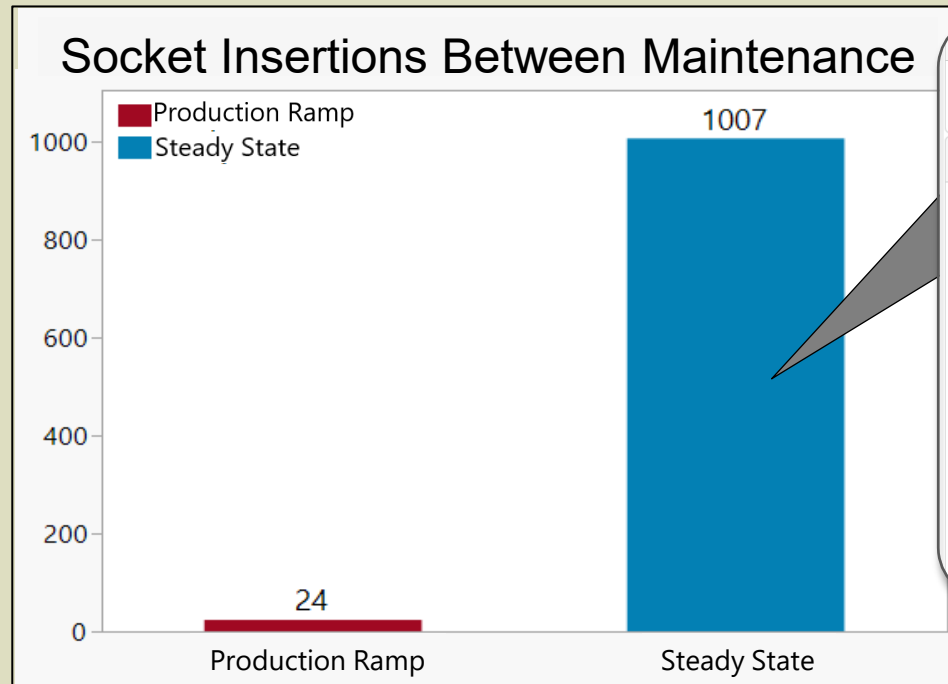
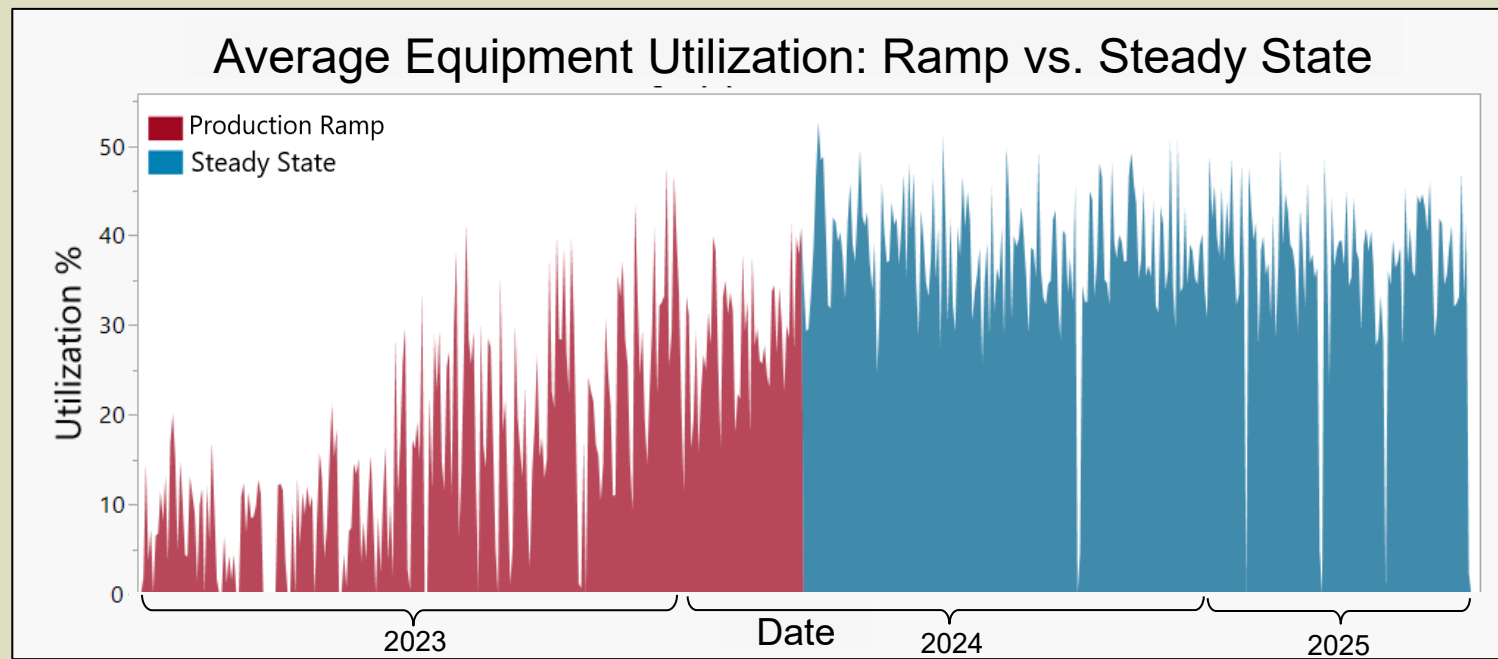


Our Goal:

Support program ramp by increasing our average number of socket insertions 50x from ~20 to over 1000

Our Solution:

- ✓ Online Cleaning Pads
- ✓ Offline Socket Maintenance
- ✓ First Pass Yield Monitoring
- ✓ Socket Design BKM
- ✓ Equipment PM for FOD



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