

## Marketplace Report Past & Future Learning

Ira Feldman Feldman Engineering Corp.





#### **Outline**

- The Last 25 years
- The Next 25 years
- Action needed

Socket Market





#### **The Last 25 Years**





#### Remember 2000?



WarnerMedia Final logo, used from 2019 to 2022











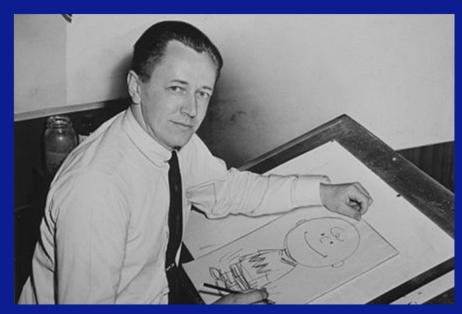


George W. Bush

Al Gore



#### **Notable Passings**



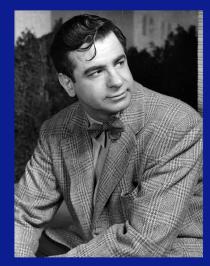
**Charles Schultz** 



Doug Henning



**Hedy Lamarr** 





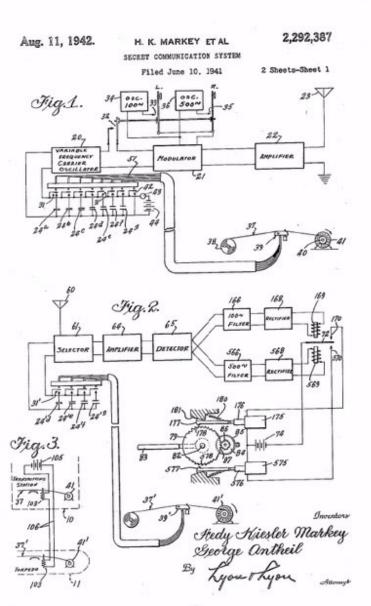
Steve Allen

Walter Matthau

Picture credits: wikipedia







#### **Hedy Lamarr & George Antheil**

#### STATES PATENT OFFICE

#### SECRET COMMUNICATION SYSTEM

Hedy Kiesler Markey, Los Angeles, and George Antheil, Manhattan Beach, Calif.

Application June 10, 1941, Serial No. 397,412

6 Claims. (Cl. 250-2)

This invention relates broadly to secret communication systems involving the use of carrier waves of different frequencies, and is especially useful in the remote control of dirigible craft, such as torpedoes.

An object of the invention is to provide a method of secret communication which is relatively simple and reliable in operation, but at the same time is difficult to discover or decipher.







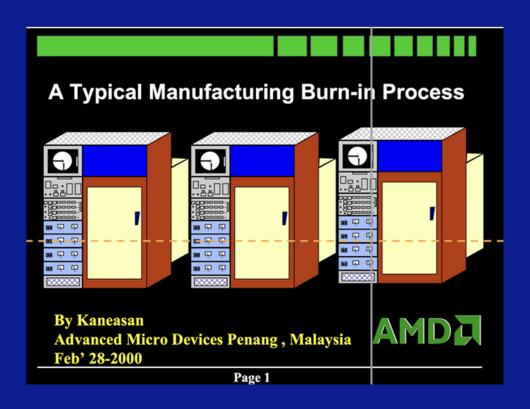




Thermal Management & The Parameters That Affect Heat Dissipation During Burn-in

Feb 2000 Bits Workshop

Presented by Erik Orwoll Engineering Manager WELLS-CTI







#### Interface Considerations for High Performance Contactors in Automated Test Environments

Dan Maccoux
Product Engineering & Support Manager
Pamela Lohr
Applications Engineer
Johnstech International
February 27-29, 1999



Johnstech International

## Package Tolerance of VFQON: Effect on Socket Design

2000 Burn-in and Test Sockets Workshop February 27-29th, 2000

Marc Abelanet and David Pfaff 2601 Texas Drive Irving Texas 75062 972-258-2580; david@locknest.com







BiTS Feb 2000

**Aspect of Socket Designs and its Trends** 

by
LP Goh
Intel Technology Sdn. Bhd
Intel Test Tooling Operation (ITTO)
Malaysia

LP Goh Feb 2000

Intel Itto Penang



**Interconnect Solutions for ATE** 

Iraj Barabi / Mehdi Attaran
OZ Technologies Inc.
February 26th 2000





A Method for Measuring and Evaluating Contact
Resistance in Burn-in and
Test Sockets

2000 Burn-in and Test Sockets Workshop



**Angelo Giaimo** 

**IBM** Corporation

Methodology for Characterizing RF Response of Sockets and Test Contactors

Theory & Basic Techniques
Using Readily Available
Tools



Valts Treibergs







### Test and Burn-in Socket Evaluation for PBGA Devices

2000 Burn-in and Test Socket Workshop



Zen Podpora

IBM Microelectronics
Contacting Systems Engineering

02/27/2000

Characterization of high performance contactors for production RDRAM chip-scale package test.





2000 Burn-in and Test Sockets Workshop

Francois Billaut Hewlett Packard Geary Chew Hewlett Packard Ken Karklin Agilent Technologies







### Burn-In Board Design For Manufacturing

2000 Burn-in and Test Sockets Workshop



Aamir Jamil aamir@pycon.com



Burn-In Board Design consideration for High Speed & High Power Devices

2000 Burn-in and Test Sockets Workshop



Aamir Jamil aamir@pycon.com





## **Burn-In Board Design Considerations**

2000 Burn-in and Test Sockets Workshop



Tony Valente
PCB Design Manager

unisys

# Evaluation of Test Socket for TSOPII Package

2000 Burn-in and Test Sockets Workshop



HO, Peng Ching Micron Semiconductor Asia Singapore







#### Critical Performance Characteristics for High Frequency Test Contactors

John W. O'Sullivan

**Product Manager** 

Johnstech International

February 27-29, 2000



### Design Characteristics of Test Contactor & ESD Concerns

By
JC Tan & TW Fong
Intel Technology Sdn. Bhd
Intel Test Tooling Operation (ITTO)
Malaysia

JC Tan Feb 2000

Intel Itto Penang



Johnstech International





### EFFECTS OF GOLD PLATED CONTACTS IN BGA BURN-IN SOCKETS

By Ariane Loranger and Alfred Sugarman

Presented At The Burn-In & Test Socket Workshop (BiTS), February 27-29, 2000, Mesa, AZ











### Using MicroSpring<sup>TM</sup> Contacts as Second Level Interconnect

BiTS Workshop Mesa, Az. 28 February 00

John Novitsky VP Business Development 925-456-3850 jnovitsky@formfactor.com





### A NEW BURN-IN SOCKET FOR FINE PITCH BGAs

Yuji Wada , Akio Hasebe , Kenichiro Morinaga and Hideo Arima

Assembly Technology Development Operation Semiconductor Integrated Circuits, Hitachi Ltd.

And

Hiroyuki Mogi , Hokuto Kanesashi Tomoaki Soshi

**Semiconductor Peripherals Div., Enplas Corporation** 

02/29/2000

# Novel Contacting Technology for Fine Pitch Leaded & Area Array Devices

Frank Bumb
Product Development
Manager

Phoenix, AZ

Ron Revell
Laboratory Manager
Austin, TX

Electronic Handling & Protection Division

BiTS 2000 Revell.ppt







#### Recurring "Themes"

- Thermal Management
  - Burn-in → Final Test
- Contactors
- Package Tolerances
- Socket Characterization
- PCB Design

High PerformanceHigh Frequency

High Power

 Low Contact Resistance

Fine Pitch

→ Same fundamentals with harder requirements

**FOR** 





#### **The Next 25 Years**





#### Digitization vs Electronification

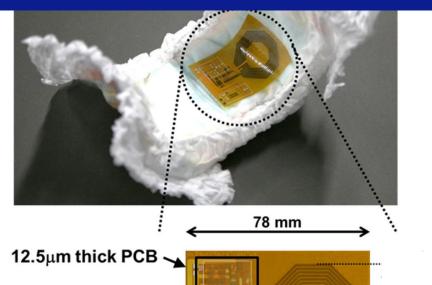




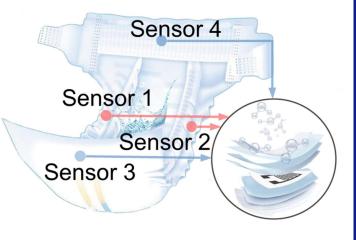




#### **Electronics Everywhere**



University of Tokyo 2013 via CNET



Pampers <u>Lumi</u> 2019 (discontinued)



Reusable urine sensor

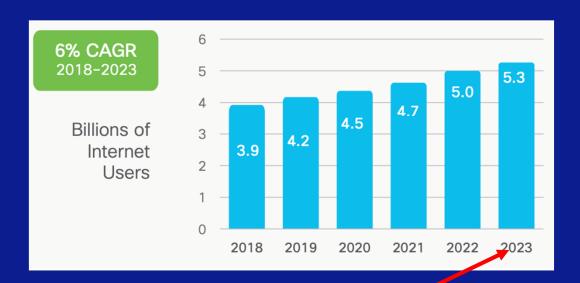
**Smartphone App** 

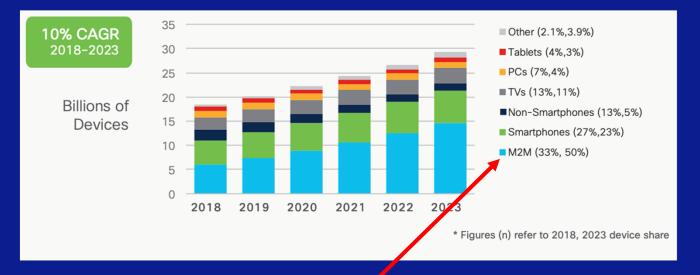
Video Monitor



Penn State 2023

#### **Internet Usage & Connections**





66% of population

19% CAGR

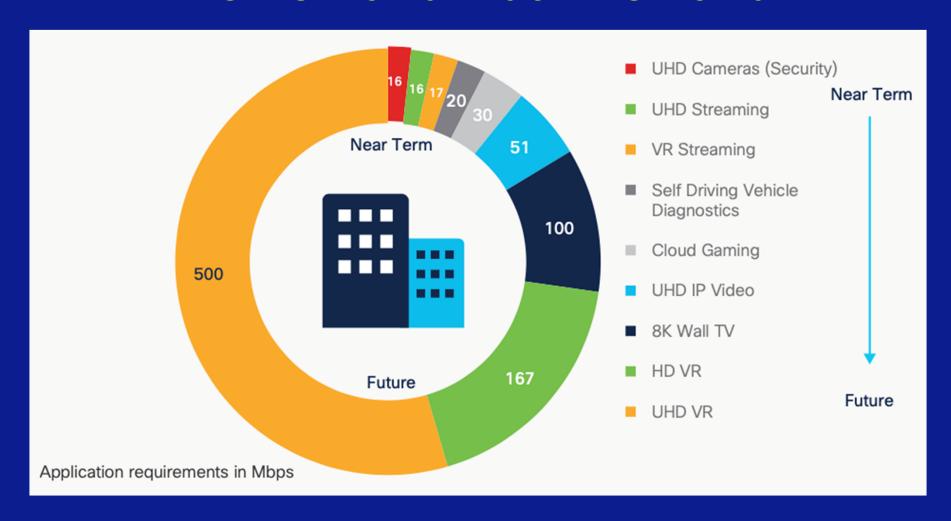
Mostly consumer applications

Cisco Annual Internet Report, 2018–2023





#### **Home Bandwidth Demand**



Cisco Annual Internet Report, 2018–2023



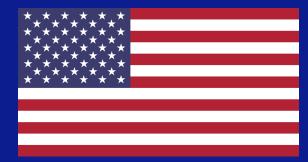


#### **Electronics**





#### **Strategic Government Initiatives**



CHIPS Act \$54B



EU Chips Act €43B

#### Plus...













#### **How to Build Chips**

IC Design \$.5B @ 3 nm

Packaging Engineering

Test Engineering Build fab \$5-20B

Wafer Fab Packaging
Internal or
OSAT

Wafer Sort

Skilled Labor

Skilled Labor :

Test
Package,
SLT,
Burn-in,
System, FA,
Validation, ...

Skilled Labor

Skilled Labor





#### **Increased Production Requires**

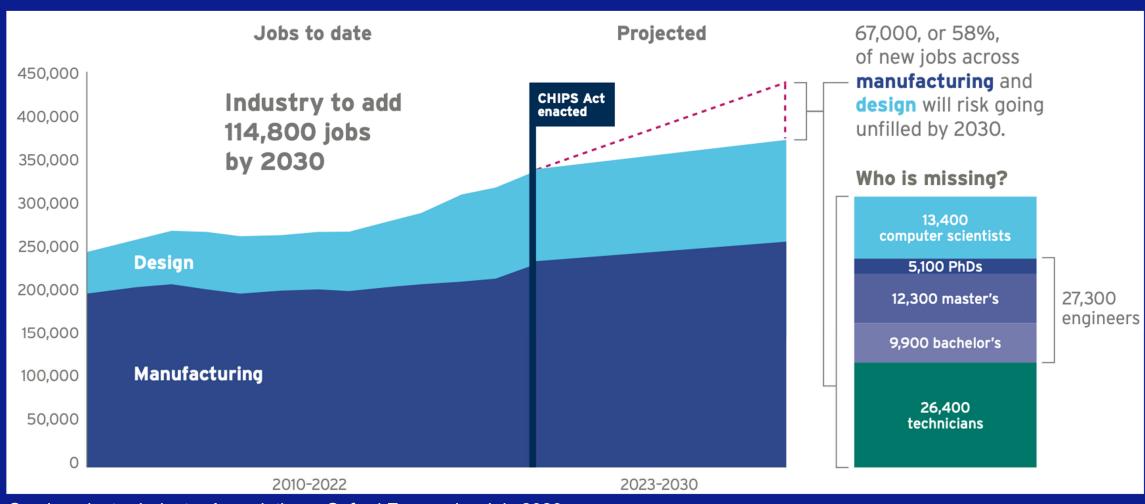


Adjust the balance and add more of both...





#### **US CHIPS Act Job Demand**

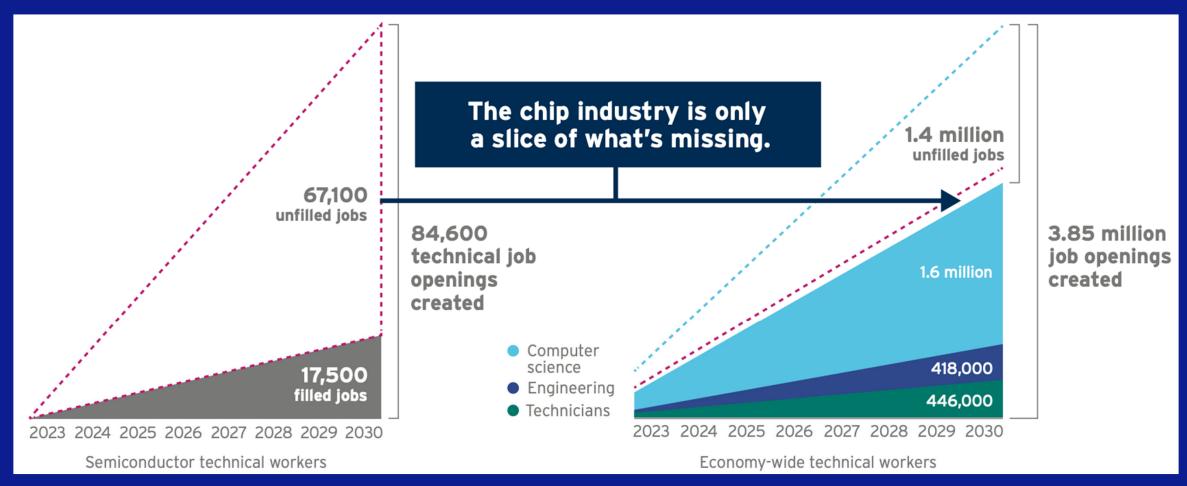


Semiconductor Industry Association – Oxford Economics July 2023





#### **US Technical Labor Shortage**



Semiconductor Industry Association – Oxford Economics July 2023





#### **Labor Supply Challenges**

#### In particular for semiconductor test

- Interest in STEM and test (student demand)
- Limited educational programs (capacity)
- Unclear knowledge & skill requirements (specification)





shutterstock / tassel78



**Educational Pipeline Challenges** 

- Low interest in entering STEM
  - Perceived as "hard"
  - Long delay from primary schools to industry
  - Lack of societal support
    - collectively we are outliers [Nerds DON'T rule]
- STEM industry retention rates





# Rare Example

#### San José State University

- EE 182 Analog and Mixed-Signal IC Test Development
  - Introduction to analog and mixed-signal IC and component measurements and automated testing. Test specifications, methods, techniques, and interfaces. Measurement accuracy, correction, and calibration. Automated test equipment (ATE) hardware and software. DAC/ADC testing and DSP-based testing. Analog and mixed-signal design for test (DFT). Laboratory exercises and mini projects.



Best practices in Test methods and techniques; ATE Test
hardware/software, Device characterization; Multi-system reliability;
Device interface board design, building, debug; ATE development
and Execution.









#### **Labor Shortage**

"In college the concept of test was mainly covered as how to test E-test structures (transistor IV curves & interpretation) which is a teeny tiny part of the overall test landscape.

What I know about real world test has organically come from on-thejob training and real-world learning - discovering what works and what doesn't."

#### -- Abram Detofsky

Principal Engineer, Tester Architecture | Foundry Technology Development Intel Foundry





#### **Multiple Solution Paths**

- Degree programs
- Credential / certification programs
- On the job training programs
- Apprenticeships
- Self directed learning
- ???





























## Rosenberger Apprenticeship Program

- 2 to 3.5 years
- Paid employment
- No long-term commitment
- Fully self-funded







#### **Solution Elements**

- Clear & articulated vision from industrial side
  - Design of curriculum
  - What is the course content and knowledge
  - Instructional methods and skill development
- Support from academia
  - Formal programs (imprimatur)
  - Learning delivery and performance measurement
- Government funding and incentives
  - e.g. CHIPS and Science Act
  - Other programs





#### **Call to Action**

- TestConX will form a "working group" to further define the industry needs and vision
  - First (virtual) meeting: week of April 1

- Let me know if you wish to participate
- Recommend others who should join



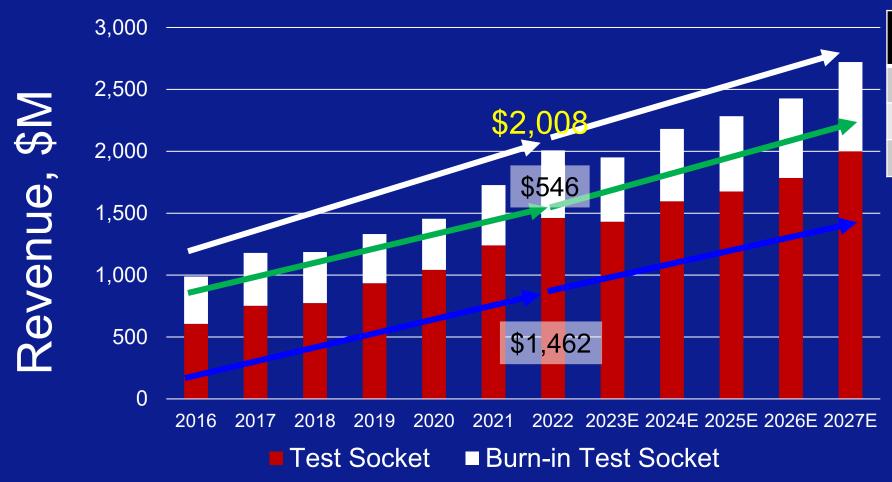


#### **Socket Market**





#### **Test and Burn-In Socket Market**



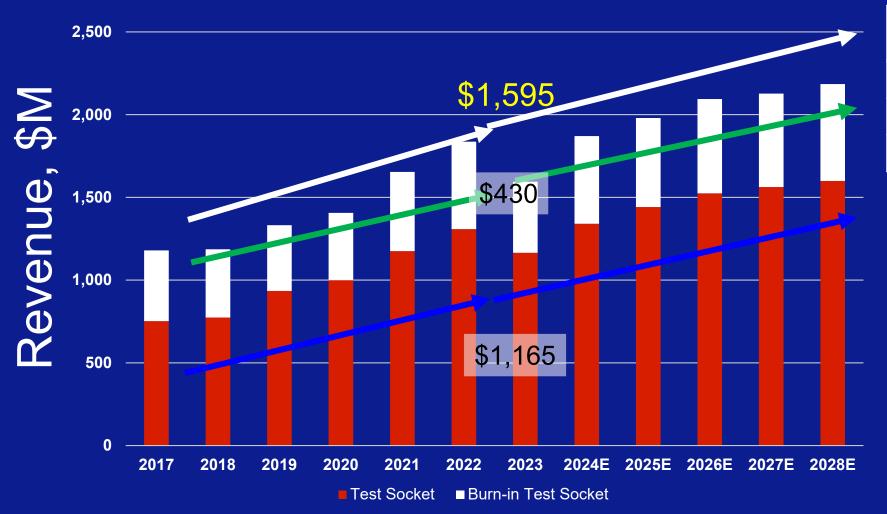
	2016-2022 CAGR	2023-2027 CAGR
Overall	12.5%	8.7%
Burn-in	6.1%	8.5%
Test	15.8%	8.7%

Forecast vs Actual For 2022:
Test +\$110M
Burn-in +\$9M





#### **Test and Burn-In Socket Market**



	2017-2022 CAGR	2023-2028 CAGR
Overall	11.7%	6.5%
Burn-in	4.3%	6.4%
Test	9.3%	6.5%

Forecast vs Actual For 2023: Test (\$266M) Burn-in (\$89M)

Yole Group 2023





#### Segment Revenue (\$M)

	2023	2028		2023 – 2028 CAGR
Test Socket	1,165	1,599		6.5%
Package Test	722	909		4.7%
System Level Test	339	568	10.9%	
Engineering Test Sockets	105	123		3.2%
Burn-in Test Socket	430	586		6.4%
Package Test	383	514		6.0%
System Level Test	21	40		5.4%
Engineering Test Sockets	25	32		3.2%
Total Sockets	1,595	2,185		6.5%







#### **Top Test & Burn-in Vendors 2023**

Rank	Overall	Test Socket	Burn-in Socket
1	LEENO	LEENO	Enplas
2	Cohu	Cohu	Boyd
3	Enplas	WinWay	Micro Contact Solution
4	Yamaichi	Yamaichi	Yamaichi
5	WinWay	Advantest	Okins





#### Acknowledgements

- Socket Market Data courtesy of Yole Group
  - Thank you John West!





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