# TestConX+E

# Virtual Archive

# October 26 – 29, 2021 Virtual Event

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Geopolitical forces are changing the test landscape. What steps should suppliers take to make sure they don't miss out?

# Lin Fu VLSIresearch Europe



China Virtual = October 26-29, 2021



TestConX China Workshop

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# **Overview**

• How will industry megatrends drive the market?

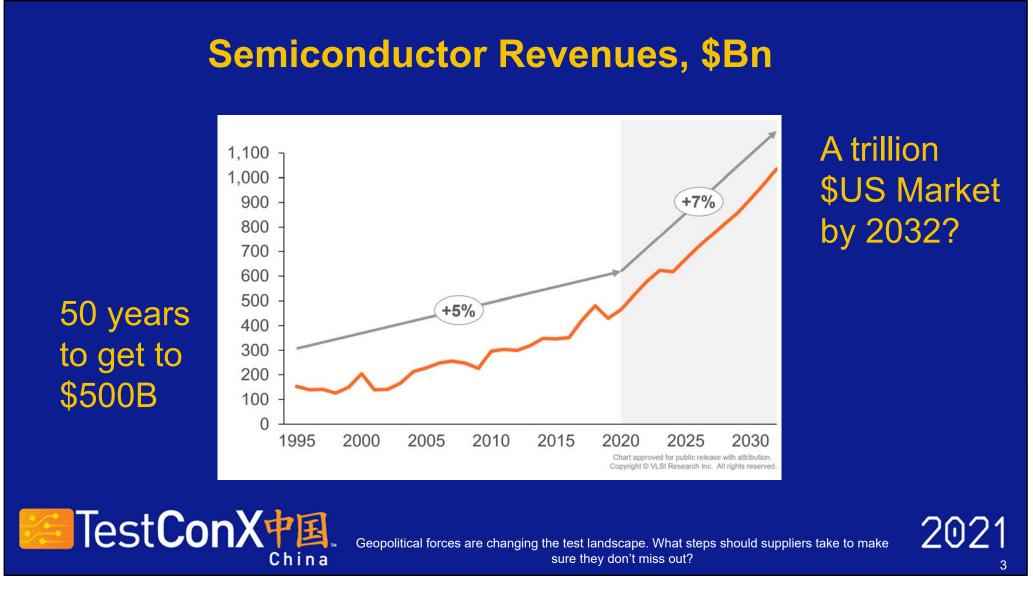
How geopolitical forces are changing the test landscape?

What does the test market look like?



Geopolitical forces are changing the test landscape. What steps should suppliers take to make sure they don't miss out?





**Industry Doubling** What's Changed to Supercharge the Industry? Digital Transformation – accelerated by the pandemic Cloud, 5G & Infrastructure, AI, Intelligent Edge, Gaming and Visualization It's a data centric world – no longer driven by human consumption Silicon content – (cm<sup>2</sup>) of electronic devices is increasing A clear technology pathway – manufacturing at the 3nm node Test**ConX中国** 2021 Geopolitical forces are changing the test landscape. What steps should suppliers take to make

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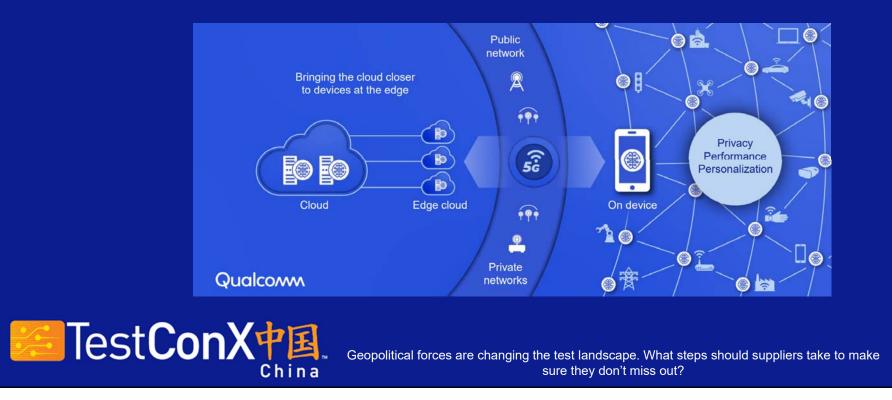
sure they don't miss out?

2021

# TestConX China 2021

# Industry doubling Distributed Computing Enabled by 5G

#### Bringing the cloud closer to the outside world





# How geopolitical forces

# are changing the test landscape?



Geopolitical forces are changing the test landscape. What steps should suppliers take to make sure they don't miss out?



# Geopolitics – Industry is on the move

- Countries will protect technological sovereignty
  - Restriction of technology transfer
  - Rebuild domestic capabilities: pandemic result in supply chain not working, many countries realize too much dependency on Taiwan
- Reconfiguration of supply chains
  - Localization
  - Multiple source strategies

#### The risk vs. cost equation has changed

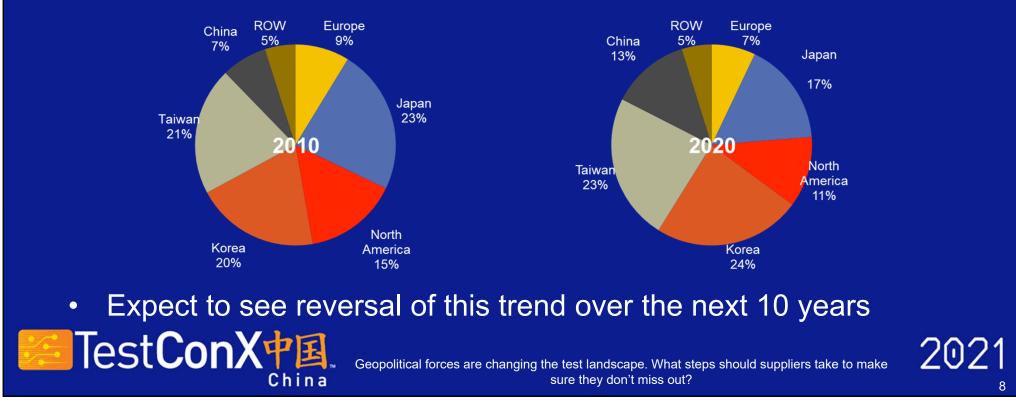


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# **Chipmaking Capacity by Region**

- China, South Korea and Taiwan have grown
- Europe, Japan and North America have declined





#### US - China dispute -----> US thinking more control of supply chain

reshoring their semiconductor manufacturing Foreign foundries build new fabs in US Profitable or not

Local fabless go back to IDM Realistic or not

Take time

Extra Questions: Labor

Labour shortage and cost

Impact on existing local supply chain



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# **Geopolitical forces – EU**

#### Problems of EU —> Technology Behind & Finding Customers

- Most chips from the EU's leading semiconductor companies like Infineon, ST Microelectronics and NXP are using trailing-edge technology
- A lot of small fabless companies design chips such as processors for data centres, highperformance computing and artificial intelligence, which are in leading-edge (10nm,7nm and 5nm). However, the number of leading-edge wafer requirement is not enough to fill a fab



- Technology behind Taiwan, South Korea and the US
- If Europe were able to set up an advanced fab
  - what will they make
  - who to sell to





# **Geopolitical forces – Japan**

#### 



Memory : Most memory manufactures in Japan currently are owned by foreigners, such as previous Elpida and Toshiba



Non-memory: leading-edge non-memory capability is far behind (Japan's most advanced semiconductor plant is a 40 nm chip factory operated by Renesas Electronics)



- Technology far behind Taiwan, South Korea and the US
- Under the decline of Japan's semiconductor industry
  Where will the (local) investment come from?



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# **Different Stage – US, EU and Japan**



- Each region already has their own existing strength in semiconductor manufacturing supply chain
- The leading manufacturing equipment suppliers, Applied material, Lam research, ASML and Tokyo electron are all from Japan, the US, and the EU

More thinking & planning in US, EU and Japan. Government incentives will change but takes time.





# **Different Stage - China**



- Government & Capital invest heavily
- International talent & technology introduce largely
- Tech giants partner with local suppliers as much as they can
- Cover as many sub-industries as they can to establish
  own ecosystem





# China faces different but equally painful problems, these need to be solved to avoid bubble



• Fill Technology gap – combination of money, time, innovation and "know-how" experience

- Seek breakthrough point of complex semiconductor manufacturing supply chain
- Reduce the impact of the US, the EU and Japan reshoring





# China faces different but equally painful problems, these need to be solved to avoid bubble



- Reduce impact of outflow to the southeast of Asia like Malaysia, Vietnam and Philippines due to increasing labour cost in China and spreading risk
- Maintain continuous investment on research and development facing long payback period



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# How geopolitical forces

# are changing the test landscape?



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# **Changes of Test Landscape**

- New suppliers coming out / small suppliers growing even if there is a trend of more consolidated globally, some suppliers bound with specific customers locally can still find space to compete
- Top suppliers adjusting market strategy frequently (The pandemic contribute it as well) – Except solid domination of Teradyne and Advantest in ATE market, top suppliers in the rest of test market did not grow in step with each other per year
- Diversity of customer increasing Leadership of data era (speed of data transferring and computing, security of data storage) make previous chip users like Google and Huawei are becoming more integrated. Suppliers are keeping their eyes open for new buyers entering the market and decision-marker replacing existing clients





# What does the test market

look like?



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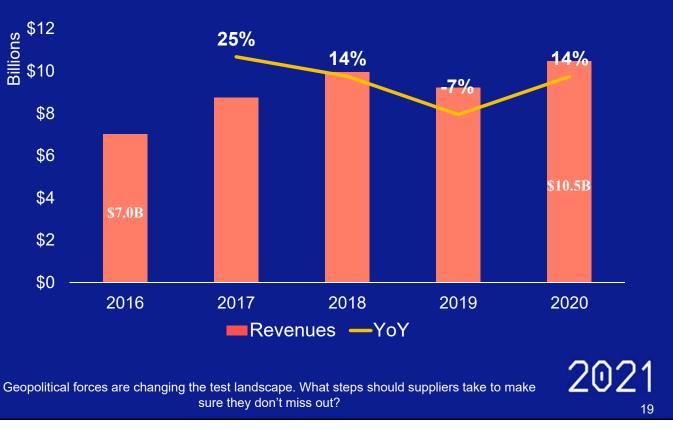
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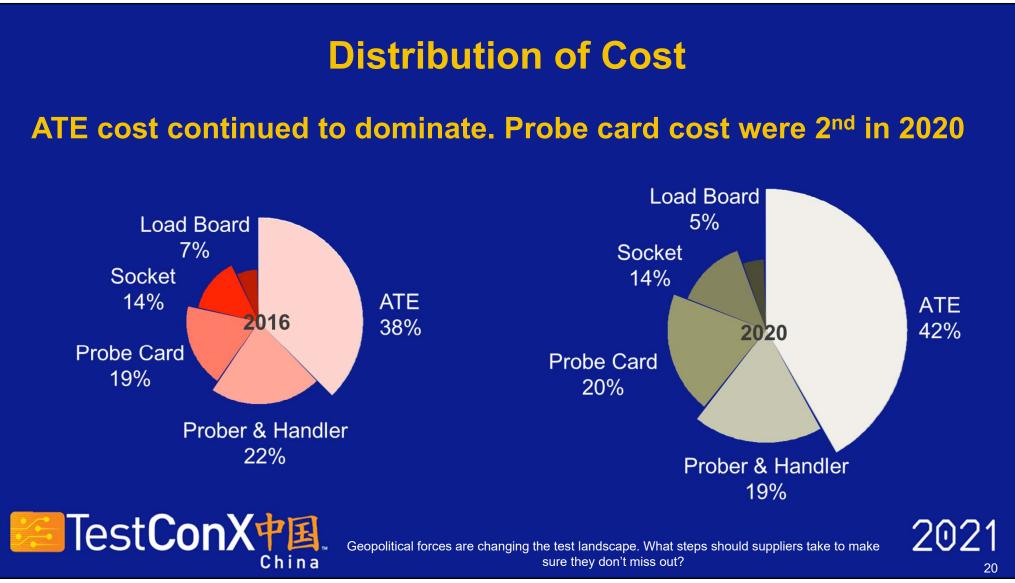
# **Semiconductor Test Cost**

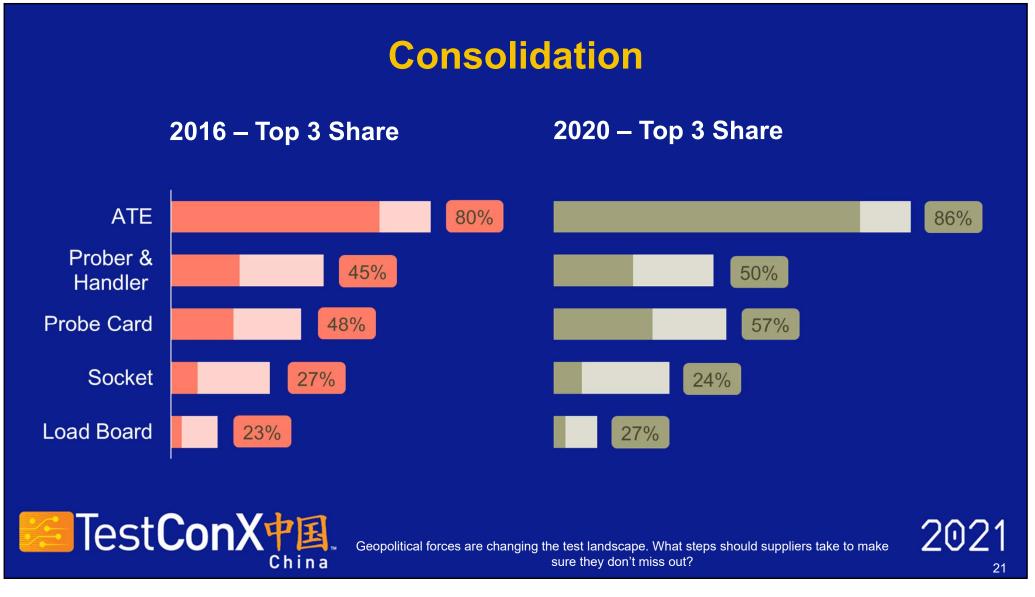
#### Cost \$10.5B on test, increased by 50% compared to 2016

- ATE
- Prober & Handler
- Probe Card
- Socket
- Load Board









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What will drive the semiconductor test market?

Number of chips will increase

Complexity of test increase resulting in more testing steps involved

- More wafer level burn-in
- More system level test

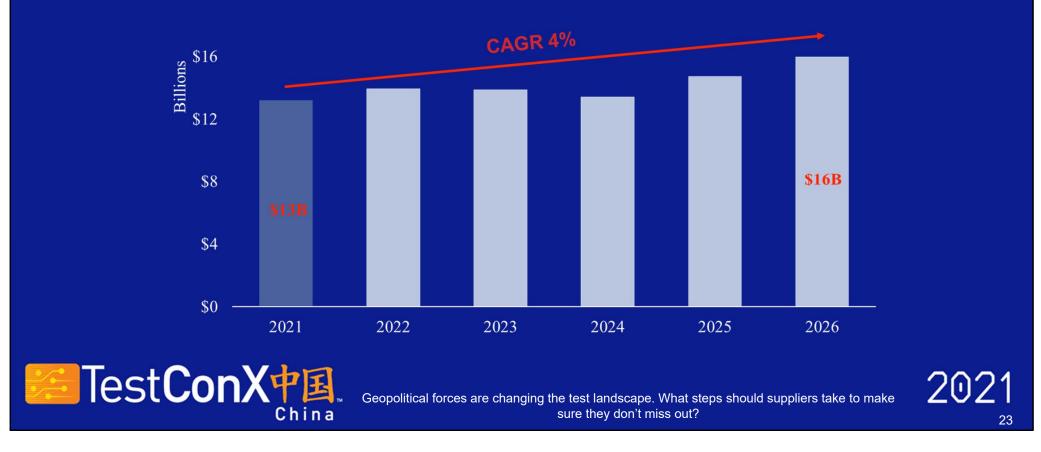
Price of products – for high-end applications increase

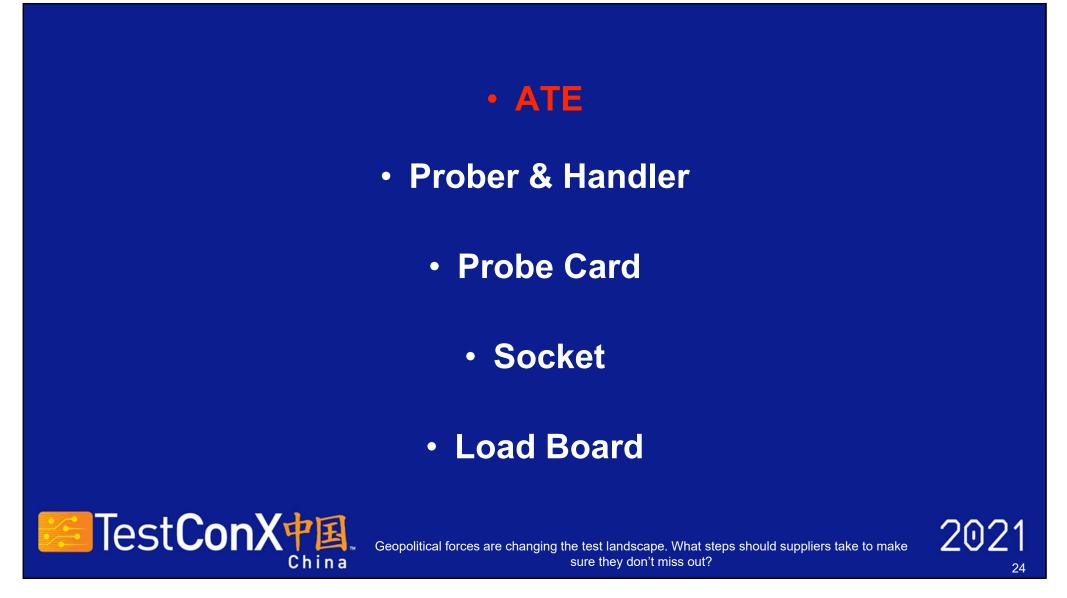
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## **Forecast – Semiconductor Test Cost**

#### **Overall trend – continue to increase**





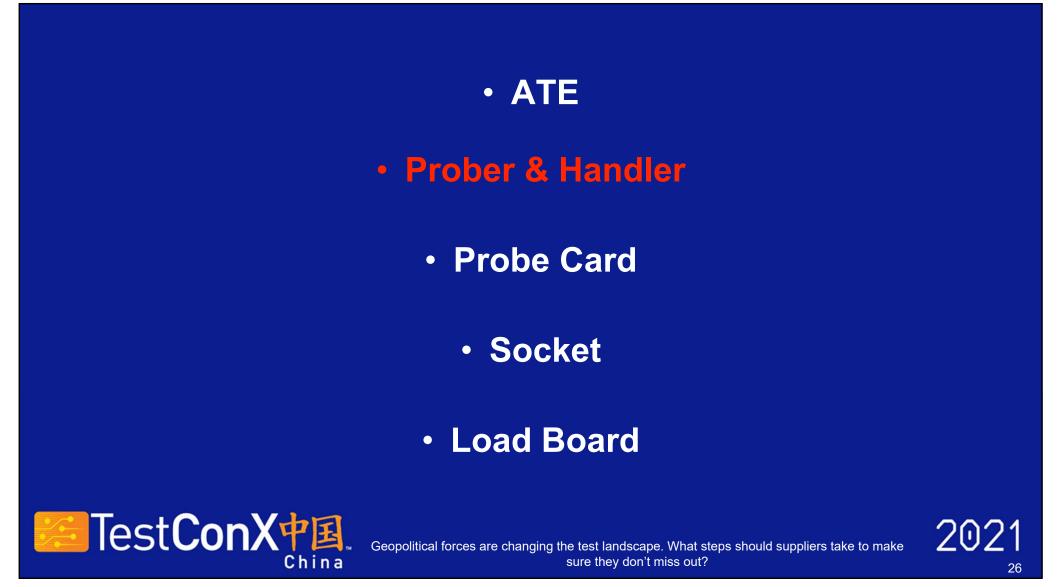
# **ATE Market**

- In the middle of a replacement cycle. New machines are much more efficient with more channels
- Grow in line with semiconductor equipment overall over the next five years
- Oligopoly of Teradyne and Advantest will be difficult to break, US and Japan will continue to dominate this market
- Other 50 suppliers battle for the rest market (<15%, \$600M)



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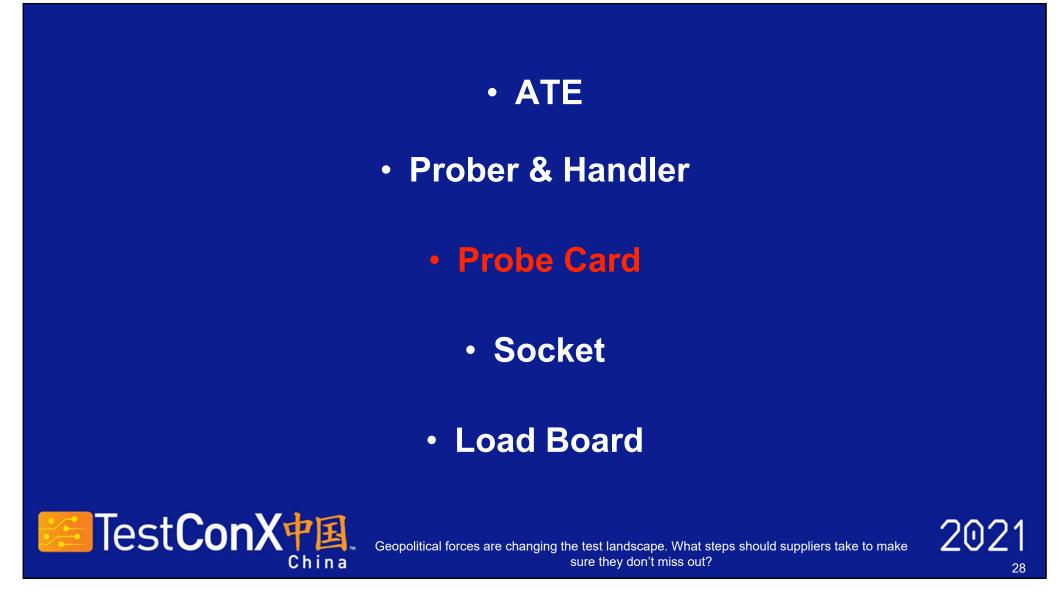


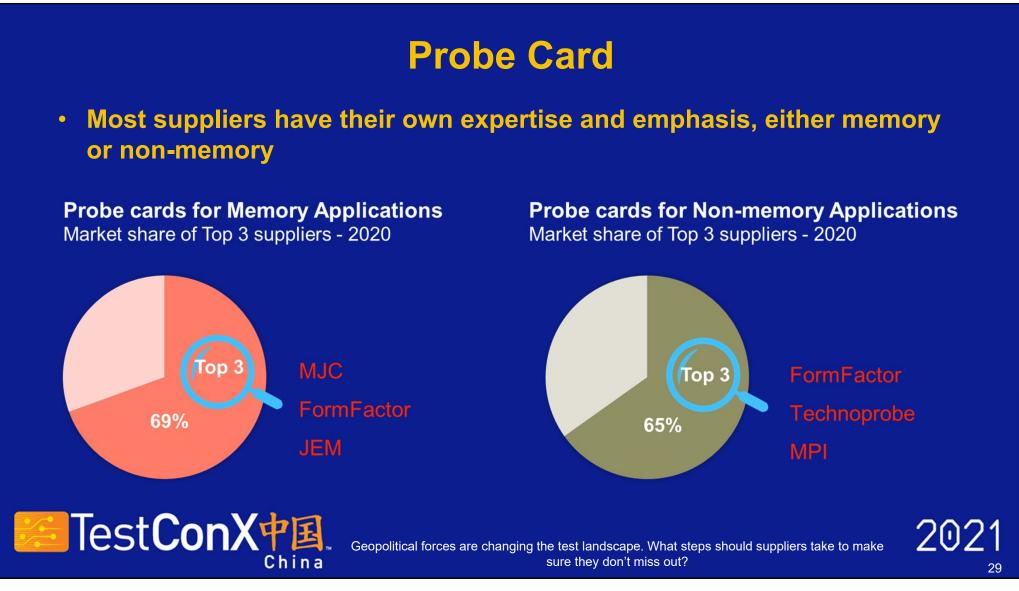
# **Prober and Handler**

- Grow in line with semiconductor equipment overall over the next five years
- Stiff competition among top 3 suppliers (Tokyo Electron, Tokyo Seimitsu and Cohu), no obvious leader
- Japan and US still dominate, but South Korea, Taiwan and Mainland China are not far behind, and gap is tightening











 Regionalization is not obvious. The US, Japan, the EU, South Korea and Taiwan all have notable suppliers



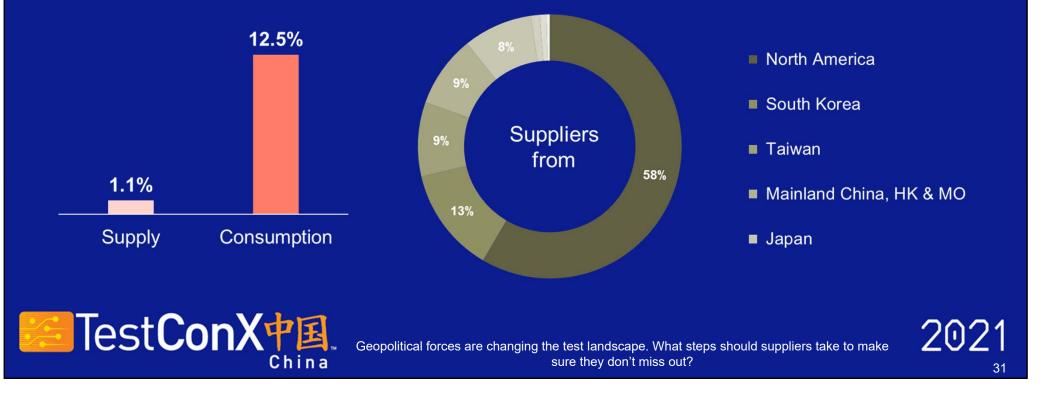
Barrier of MEMS probe cards is high and even limit growth of some top suppliers in the past several years



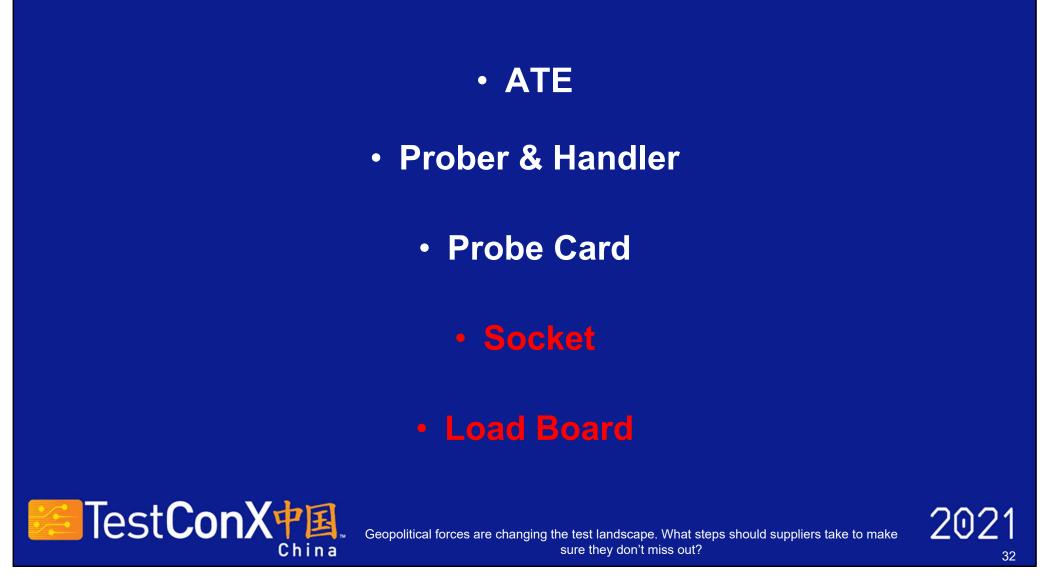


# **Probe Card – Chinese Market**

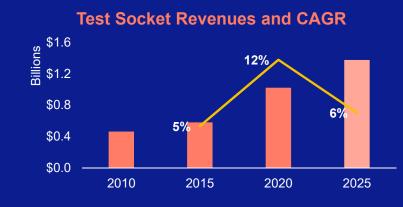
 The market share of Chinese suppliers was 1.1% in 2020, mainly focusing on cantilever and vertical type (more price matter). Started to make some progress to enter MEMS card market. Either develop own MEMS pins or purchase pins from others



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# **Test Socket and Load Board**





- Test socket sales were quite strong from 2015, with a CAGR of 12%; broke through the \$1Bn barrier in 2020. 2021 has gotten off to a strong start. The current upturn will continue until a cyclical downturn in semiconductors
- Test socket shipments is currently running at a CAGR of 5.1%. This is slightly lower than the CAGR for IC unit shipments (7.1%), as the lifetime of test sockets continues to grow
- The predicted growth in the load board market is due to a combination of several factors: increasing complexity which is driving up the prices for design and manufacture of boards for leading-edge applications, the growing number of devices to be tested, and a higher number of boards being consumed per chip design

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This is a highly competitive and vibrant marketplace where no single supplier dominates. Each has its own distinctive character and technological edge

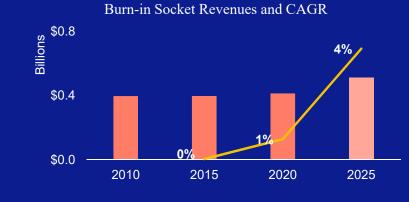
- Cohu 11.3% LEENO 9.3% Other Yokowo 2020 9.2% ISC 8.5% Winway 7.6% Test**ConX中国** China
  - Cohu from North America is the market leader in 2020 and specializes in test sockets for high end non-memory applications
  - LEENO from South Korea, who also supply test socket pins to their competitors, is in second place and is slowly gaining ground on Cohu
  - In third place, Yokowo from Japan has been on a surge due to sales of sockets for mobile and 5G applications
  - ISC from South Korea are leaders in elastomer test sockets
  - Winway from Taiwan have a focus on serving the Taiwanese market

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# **Burn-in Socket**

The overall market for burn-in sockets has been growing at a much lower rate than for semiconductors. However, closer inspection reveals a bifurcated market.



One part is for commodity products characterized as high volume, low price, with falling prices

2010 – 4.8M shipments 2015 – 5.5M shipments 2020 – 5.7M shipments

The other part is for high-performance products, which present continuous opportunities for new products with higher prices, and the volumes are growing to the point where they more than offset the price reductions elsewhere

The change in product mix and pricing drives the overall market above historical trends making this a much more exciting space for suppliers



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35

# **Burn-in Socket Supplier**



- The top 3 suppliers are Enplas (Japan), Sensata (EU), and Yamaichi (Japan). These suppliers have strong positions in all memory and non-memory segments. The difference in revenues between first and third place is only a few million US dollars in any given year, so the rankings typically change from year to year. Currently, Enplas have the edge and have been in the top spot for the past two years
- The market shares of Micro Contact Solution (South Korea) and Plastronics (US) were just over 5%, a big gap with top 3
- Burn-in socket suppliers (about 30 active players) are much less than test socket suppliers

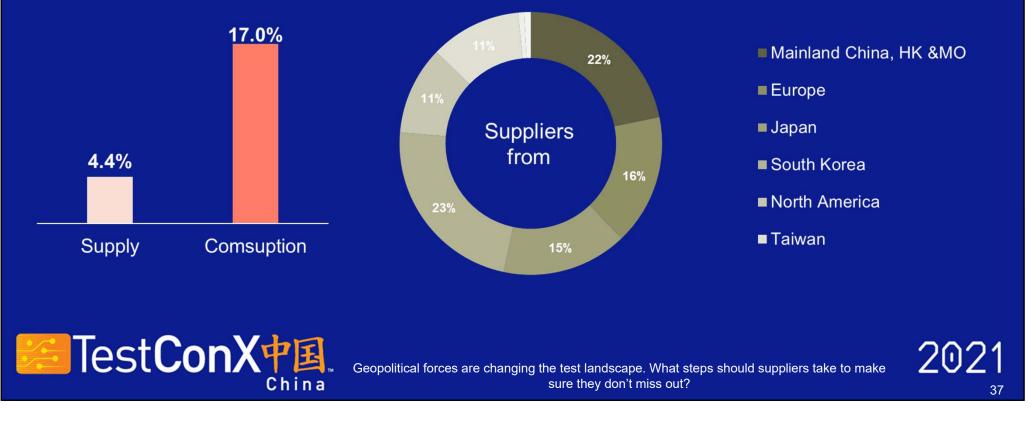
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36



• The market share of Chinese suppliers was 4.4% in 2020. Consumption was 17.0%



# Data and the stories behind data help you understand

- How will industry megatrends drive the market?
- How geopolitical forces are changing the test landscape?

#### Where is industry going to be in the future?



Geopolitical forces are changing the test landscape. What steps should suppliers take to make sure they don't miss out?





What does the test market look like?

#### Where are you now?



Geopolitical forces are changing the test landscape. What steps should suppliers take to make sure they don't miss out?





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