#### TestConX 2020

New Spins - Printed Circuit Boards (PCBs)

# A Deep Dive Into Your Load Board

#### Tom Bresnan R&D Altanova



Virtual Event • May 11-13, 2020



TestConX Workshop

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New Spins - Printed Circuit Boards (PCBs)

#### **Some Things to Think About**

- Aspect ratio's
- Flute length
- Flip drilling
- Drill wander
- Layer alignment
- Via plating
- "Sweet spot"
- Board thickness limitations

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Unless otherwise noted, pictures and videos are provided by R&D Altanova

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#### Aspect Ratio – what is it and why do I care?

- It's the ratio of board thickness / drill diameter
- In device test, some typical aspect ratio's:
  - 1.0mm, 13:1
  - 0.8mm, 16:1
  - 0.5mm, 31:1
  - 0.4mm, 38:1
  - <u>– 0.35mm, 40:1</u>

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#### Calculations based on a 5.0mm thick load board

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202

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![](_page_5_Figure_4.jpeg)

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Session 3 Presentation 1

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#### **Drill Machines**

- Drill side one
- Flip
- Drill side two

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![](_page_6_Picture_7.jpeg)

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![](_page_7_Figure_3.jpeg)

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![](_page_12_Figure_3.jpeg)

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	1.0mm (0.0394)	0.8mm (0.0315)	0.5mm (0.0197)	0.4mm (0.0157)
Pad	0.76mm	0.66mm	0.35mm	0.3mm
	(0.0299)	(0.026)	(0.0138)	(0.0118)
Hole	0.37mm	0.3mm	0.15mm	0.1mm
	(0.0146)	(0.0118)	(0.0059)	(0.0039)
Line	200µ	200µ/125µ	200µ/75µ	200µ/50µ
	(0.008)	(0.008/0.005)	(0.008/0.003)	0.008/0.002)
Hole 2 Copper	0.25mm	0.18mm	0.12mm	0.1mm
	(0.010)	(0.007)	(0.005)	(0.004)
Aspect Ratio	Low	Medium	High	Extreme

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#### **Via Plating**

- Cathode bar
   For the board
- Anode bar(s)
  - Facing each side
- Plating solution
  - Copper sulfate
    - Think rock salt
  - Water
  - Other ingredients

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![](_page_14_Picture_13.jpeg)

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## Via Plating

- High Aspect Ratio Challenges
- Fluid dynamics inhibit flow of chemistry through holes
  - Air bubbles or pockets form
- Electroplating wasn't designed for holes
  Think chrome bumper on your first car
- Reverse Pulse Plating or Dutch Reverse Pulse Plating
  - Enhances the throwing power into high aspect ratio holes
  - Minimizes copper buildup on the surface

![](_page_15_Picture_11.jpeg)

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![](_page_15_Figure_13.jpeg)

![](_page_15_Picture_14.jpeg)

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#### The Sweet Spot

- There is an area of the process panel in printed circuit manufacturing we like to call "The Sweet Spot"
- In the early days of test, it occupied the center of the panel and was maybe 40mm<sup>2</sup>

![](_page_16_Picture_6.jpeg)

![](_page_16_Picture_7.jpeg)

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#### **The Sweet Spot**

- Now we would say it consumes 300mm x 100mm or more
- It's important to know that the tolerances have not changed, only the area required for them has.

![](_page_17_Picture_6.jpeg)

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#### **Board Thickness Limitations – A Summary**

- Aspect ratio's
- Flute length
- Flip drilling
- Drill wander
- Layer alignment
- Via plating
- "Sweet spot"

![](_page_19_Picture_11.jpeg)

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![](_page_19_Picture_13.jpeg)

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![](_page_20_Picture_3.jpeg)

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