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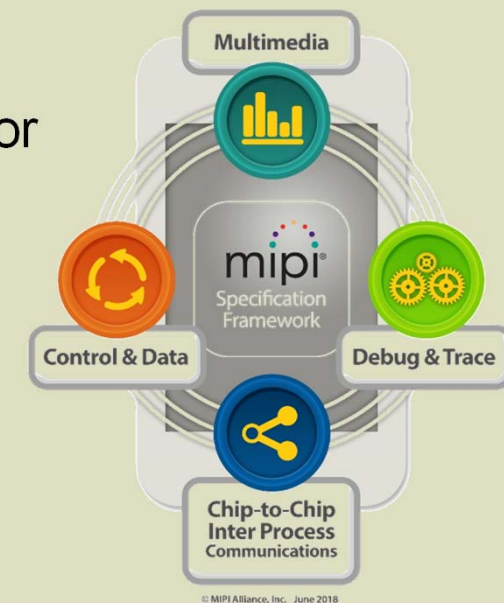
MIPI[®] Alliance Introduction

Enrico Carrieri
Debug WG Chair, MIPI Alliance



What is the MIPI Alliance?

- The MIPI Alliance is a collaborative global organization of over 300 member companies focused on hardware and software interfaces for the mobile and mobile-influenced ecosystems.
 - Their specifications serve six fundamental application areas: physical layer, multimedia, chip-to-chip or inter-processor communications (IPC), control/data, and debug/trace, and software.
 - The specifications are available as individual interfaces, enabling companies to adopt those that meet their particular needs.



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Leveraging MIPI Beyond Mobile



Characteristics of MIPI specifications

MIPI specifications are suitable for use in a variety of other applications because they've been optimized for:

- High bandwidth
- Low power consumption
- Low electromagnetic interference

MIPI Debug WG

The focus is to unify/define:

- Protocols that support debug/trace.
 - With a particular focus on highly integrated, fielded systems
 - Software layers to support and/or implement these protocols
- Configuration/control mechanisms required directly by debug/trace protocols.
- Reuse of functional interfaces and protocols for debug/trace.
- Mating connections and pin assignments.
- Electrical characteristics.



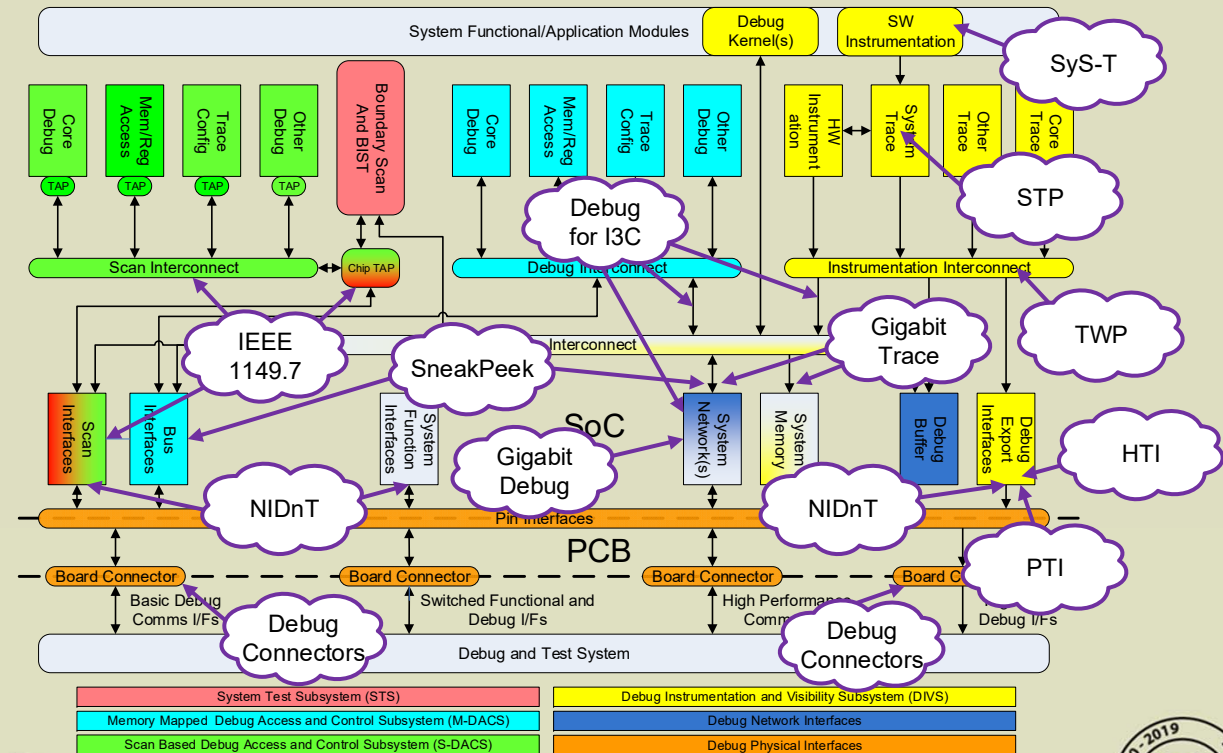
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Debug WG Specifications

- Have over 10 specifications adopted or in progress.
- Also produced several whitepapers and other documentation.



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For more information...

- MIPI Alliance Web Site:
<http://mipi.org>
- MIPI Debug WG Public Page:
<https://www.mipi.org/specifications/debug>
- MIPI Architecture Overview for Debug:
https://www.mipi.org/sites/default/files/mipi_Architecture-Overview-for-Debug_v1-2.pdf
- MIPI Debug on Wikipedia:
http://en.wikipedia.org/wiki/MIPI_Debug_Architecture



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