NINETEENTH ANNUAL Burn-in & Test Strategies Workshop

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Bits 2018

Session 2A Presentation 1

Touch Down! - Contact Technology

High Current Test Contactor – Divide and Conquer

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Burn-in & Test Strategies Workshop

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Session 2A Presentation 1

Touch Down! - Contact Technology

Overview

- Background
- Problem Statements
- Road To METS Test
- MET CCC Test
- Contactor Design for Reliability
- High Current Contactor Performance
- Does Current Equally Split?
- High Current Test Methodologies
- Summary

High Current Test Contactor – Divide and Conquer

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Background

- Semiconductor devices become smaller → smaller pitches and smaller pad/lead (land area) → need smaller contactor to test
- The application of the devices become more complex
 - Low RDSon and High current testing on the same device pads
 - Extreme Tri-temp testing
- Need contactors
 - Small enough to handle small pitch and pad sizes Small enough to handle small pitch and pad sizes
 - High Current Carrying Capability (CCC)
 - Low Contact Resistance (Cres)
 - Extreme Tri-Temp Capability
 - >1M Insertions Reliability
 - > High Multi-site \rightarrow low spring force requirement





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Problem Statements

- Mismatch between Contactor Spec. vs. Actual Test
 - Contactor Spec.: ISMI (International Sematech Manufacturing Initiative) and Temperature rise methods → use Continuous Current, New Pins, Room Temp,....
 - In test application: use various pulse current, tri-temp, degrade over time, foreign materials,....
- Inadequate hardware/circuit implementation
 - ➢ High continuity/FuncPara → Low Yield
 - Depressed pin, burnt pins, melting pin block,... High Maintenance





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stress test.

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Contactor Design for Reliability

- Develop Pin CCC calculation table using Temperature rise study and METS Tests data
 - Inputs
 - Max Current
 - On Time
 - Off Time
 - Test Temp
 - Outputs
 - # of Pins Needed
- Design right hardware during design stage

| Input Conditions | | | | | | |
|------------------|-----|-------|--|--|--|--|
| Pin count | 2 | pc(s) | | | | |
| Max Current | 30 | А | | | | |
| Current On time | 8 | ms | | | | |
| Index time | 300 | ms | | | | |
| Ambient | 25 | С | | | | |
| Need more pins | | | | | | |

| Input Conditions | | | | | |
|------------------|-----|-------|--|--|--|
| Pin count | 6 | pc(s) | | | |
| Max Current | 30 | А | | | |
| Current On time | 8 | ms | | | |
| Index time | 300 | ms | | | |
| Ambient | 25 | С | | | |
| Working | | | | | |



High Current Test Contactor – Divide and Conquer

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Does Current Equally Split?

- 15A through VIN to PH (2F & 1S)
- Theoretically 15A shares two VIN pins → 7.5A each
- The total resistance varies with one insertion to another due to alignment, biasing of the pin, foreign material, and so on... → Managing current/pin
- More current flows through lower resistance path and downgrades the pin → See-saw effect





| Assumed all other variables are constant. | | | | | |
|---|-------|-----------|------------------|-------------|--|
| Total I= 15A | | Cres (mΩ) | Total Resistance | Current (A) | |
| Case 1 | Pin 1 | 50.0 | 25.0 | 7.5 | |
| | Pin 2 | 50.0 | 25.0 | 7.5 | |
| Case 2 | Pin 1 | 75.0 | 30.0 | 6.0 | |
| | Pin 2 | 50.0 | 30.0 | 9.0 | |
| Case 3 | Pin 1 | 150.0 | 37.5 | 3.8 | |
| | Pin 2 | 50.0 | 37.5 | 11.3 | |

High Current Test Contactor - Divide and Conquer



the problem with a seesaw is you're always off balance.

Picture From: [https://www.google.com/search?q=seesaw&source=lnms&tbm=isch&sa=X&ved=0ahUKEwjL7cKGm_ vYAhVR7VMKHduFBusQ_AUICigB&biw=1396&bih=778#imgrc=TYidGKsNkgyokM:]

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High Current Test Methodologies

- Kelvin Check
- Managing max current clamping
- Balancing the current flows to all force pins
 - > Add resistor
 - Add relay (gang sense pin for use as force pin during non-kelvin test sequence)
 - Split current path



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Kelvin Check

- Debris, Foreign material on the device and contactor
 - ◆ Debris causes high contact Resistance between pin and device → Hot spot
 - ♦ The higher the Resistance, the higher the Power → the higher the Energy

$$\mathsf{P} = I^2 * R (W) \qquad \mathsf{W} = P * t(WSec \text{ or } I)$$

 Check the contact resistance between pin tip to the DUT pads before high current testing



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Managing Max Current Clamping

- Current or voltage surge could cause early failure of the contactor
- Choosing right clamping (program & hardware) current or voltage to prevent unwanted current spike



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Balancing The Current Flows

- Add series resistors in the high current path and split all Force PCB pads to eliminate the unbalance current effect due to small contact resistance variabilities.
- In case of having one Force and one Sense contact, add relay to short F & S for high current testing.



Contactor

Contactor

Add Resistors





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DUT Pad

100mΩ 150mΩ

50mΩ

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