# NINETEENTH ANNUAL Burn-in & Test Strategies Workshop

### March 4 - 7, 2018

Hilton Phoenix / Mesa Hotel Mesa, Arizona

Archive



# **COPYRIGHT NOTICE**

The presentation(s)/poster(s) in this publication comprise the Proceedings of the 2018 BiTS Workshop. The content reflects the opinion of the authors and their respective companies. They are reproduced here as they were presented at the 2018 BiTS Workshop. This version of the presentation or poster may differ from the version that was distributed in hardcopy & softcopy form at the 2018 BiTS Workshop. The inclusion of the presentations/posters in this publication does not constitute an endorsement by BiTS Workshop or the workshop's sponsors.

There is NO copyright protection claimed on the presentation/poster content by BiTS Workshop. However, each presentation/poster is the work of the authors and their respective companies: as such, it is strongly encouraged that any use reflect proper acknowledgement to the appropriate source. Any questions regarding the use of any materials presented should be directed to the author(s) or their companies.

The BiTS logo and 'Burn-in & Test Strategies Workshop' are trademarks of BiTS Workshop. All rights reserved.

# www.bitsworkshop.org

# **Bits 2018**

## **Poster Session**



# **BiTS 2018**

## **Poster Session**

#### DFM

•Items to Consider to Improve Manufacturability:

- Trace Routing (Ideally trace routing is around tight pitch pins instead of through them and is equidistant between adjacent pins.)
- Drill Structures Affecting outer layer (Can drill spans be increased or stacked lasers utilized?)
- Grouped GND Pins (Can grouped ground pins be used to reduce drill hits and open space for trace routing on inner layers?)



#### Functional Registration/Layer to Layer

•Definition: Worst case measurement between adjacent S-G or S-S layers within a PCB.



•Mis-registration between these layers can lead to the transmission line losing part or all of its adjacent reference.

•Design Related Contributing Factors:

- Material (B-Stage Resin Content, Glass Style & Type, Core Thickness, etc.)
- Manufacturing Stack-up (Hybrid Materials, Sequential Laminations)
- Material Scale Factors (vary significantly between standard FR4 and exotics)
- Copper %, Weight & Distribution by Layer





#### Burn-in & Test Strategies Workshop

# **Bits 2018**

# **Poster Session**



Burn-in & Test Strategies Workshop