

Burn-in & Test Strategies Workshop

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1

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Addressing Burn-In Challenges for High Temperature Products

Paolo F. Rodriguez Analog Devices



BiTS China Workshop Suzhou September 13, 2016



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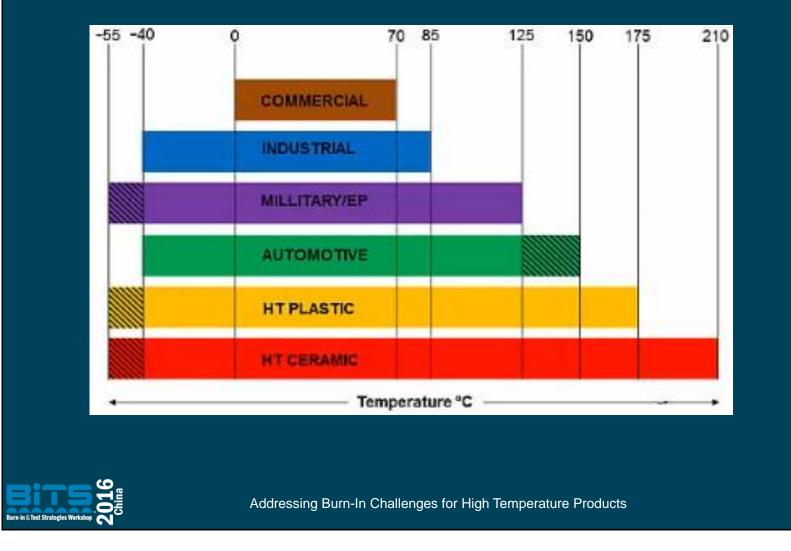
Presentation Outline

- I. Introduction
- II. Objective
- III. Challenges
- **IV. Material/Component Selection**
- V. Design
- **VI.** Qualification
- VII.Conclusion
- VIII.Recommendation
- IX. Acknowledgement



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High Temperature (HT) Electronics



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3

High Temperature (HT) Electronics

- Old systems depended on active or passive cooling
- Disadvantages:
 - Cooling adds cost and weight
 - System reliability
 - Impractical solution in harsh environments
- So, why NOT just operate the electronics HOT?





Addressing Burn-In Challenges for High Temperature Products

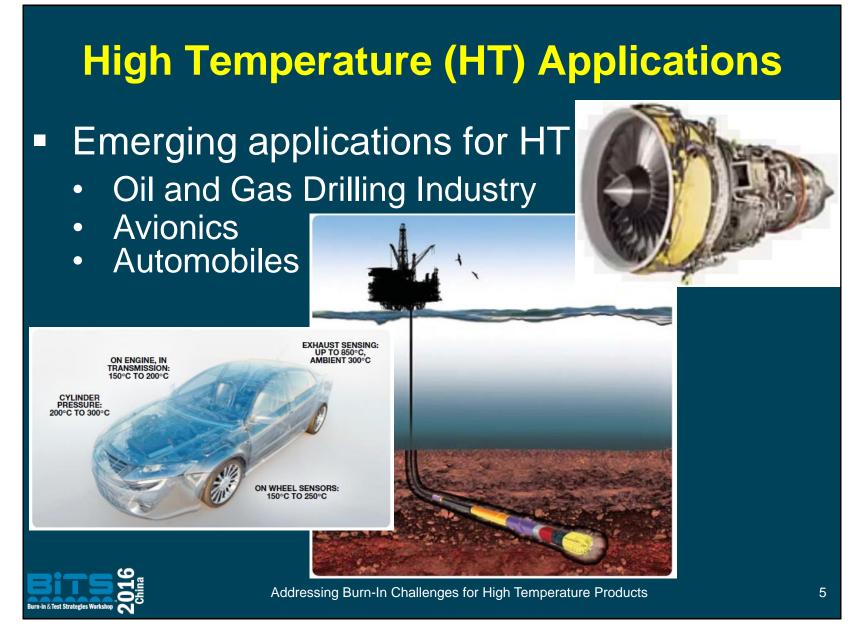


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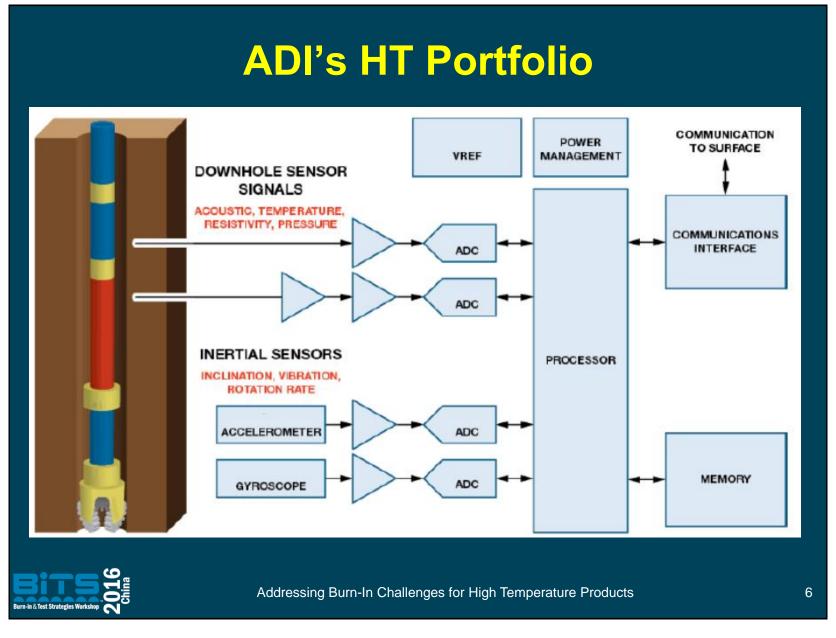


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Product Qualification...

Necessitates rigorous testing at temperature
High Temperature Operating Life (HTOL) is required for design or fabrication changes

Test Group	Test Code	Package	Description	Sample Plan (per lot) LTPD SS N	Number of Lots Sampled	Notes
9a	<u>1a</u> <u>1b</u> <u>36</u> <u>15</u> <u>1a</u> <u>1b</u>	C, P, H, B, O, L, M C, P, H, B, O, L, M	ELECTRICAL ENDURANCE - HIGH TEMPERATURE SEQUENCE Guardbanded Electrical Test or Data Log Electrical Test - (New die types) Solder Heat Resistance Sequence - Note 1 High Temperature Operating Life (HTOL) Guardbanded Electrical Test or Data Log Electrical Test - (New die types)	See <u>Table 5</u>	3	Delta Analysis of Key Parameters (t0 to Post- Reflow and Post HTOL) should be considered. Cpk≥1.33 recommended post-reflow and post- HTOL. Note 1 - CSAM not applicable (e.g., use Solder Preconditioning - Table 4 Test Code 20) for Hermetic/Cavity Packages



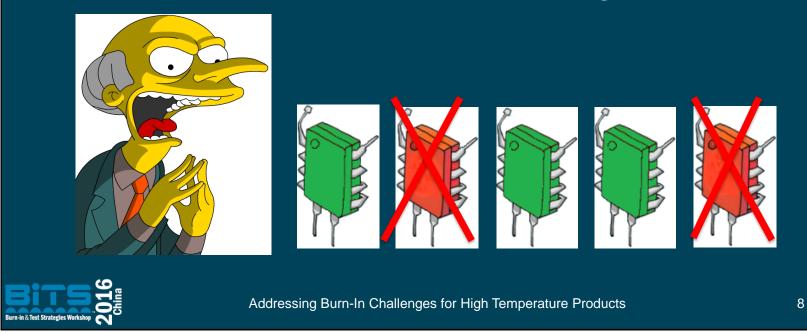
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Mr. BURN-IN...

Is a screen to weed out weak devices
Utilizes thermal stress and electrical excitation to maintain the device junction temperature within a certain range



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Burn-In Oven

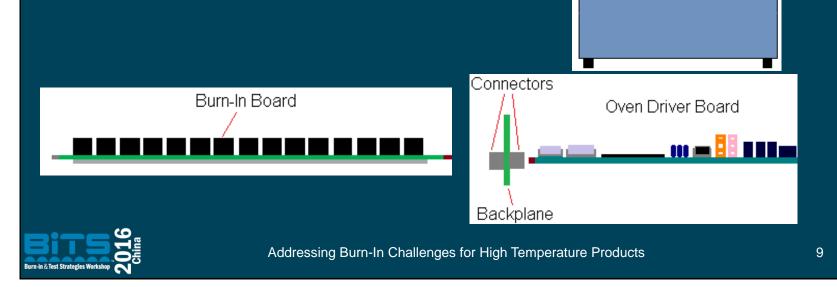
Board Rack Assembly

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Burn-IN Hardware

 Anything inside the "HOT BOX" must be able to withstand prolonged exposure to temperature



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Objective

 Document the *challenges* and *innovations* that led to the development of a 210°C dynamic <u>burn-in</u> solution for *high* temperature products



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10

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The Challenges

- How do we keep the burn-in boards from failing at high temperature?
- How do we prolong the life of the oven connectors which are costly and difficult to replace?
- How do we guarantee that the oven will pass high temperature calibration?



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11

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Materials

- Selection focused on high temperature characteristics
- Burn-In Board PCB Material used POLYIMIDE
 ✓ Tg > 250°C

 Chamber surfaces & assemblies used ALLOY STEEL
✓ Max. service temperature > 500°C



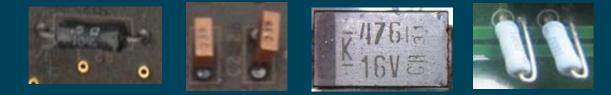
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12

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Components

 ✓ Selection focused on reliable datasheet performance at high temperature
METAL OXIDE FILM Resistors
✓ High endurance and stability at temperature
✓ Start to derate above 70°C



CERAMIC RADIAL MOLDED Capacitors
✓ Higher insulation resistance above 200°C

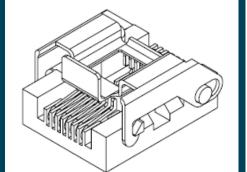


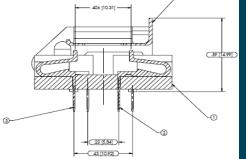
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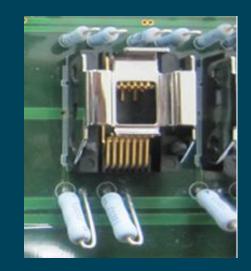
Burn-In Socket

- Effects of prolonged thermal exposure is the primary concern
 - ✓ minimum of 2,000 hours @ 210°C





ITEM NO.	QTY.	DESCRIPTION
1	1	BODY
2	7	CONTACT (INSIDE LEG)
3	7	CONTACT (OUTSIDE LEG)
4	1	LATCH





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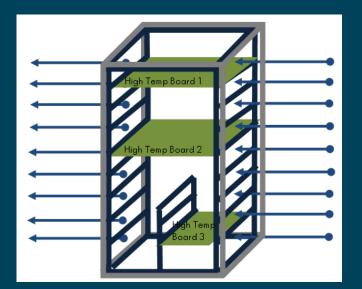
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Board Rack

- Tower board rack design has clearance from chamber walls
- Reduced chamber volume







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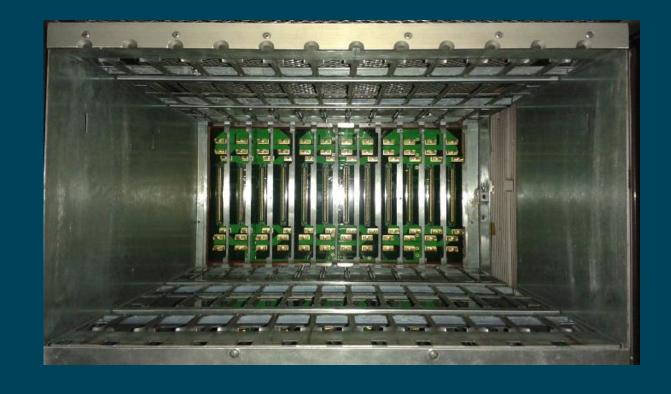
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Backplane Connectors





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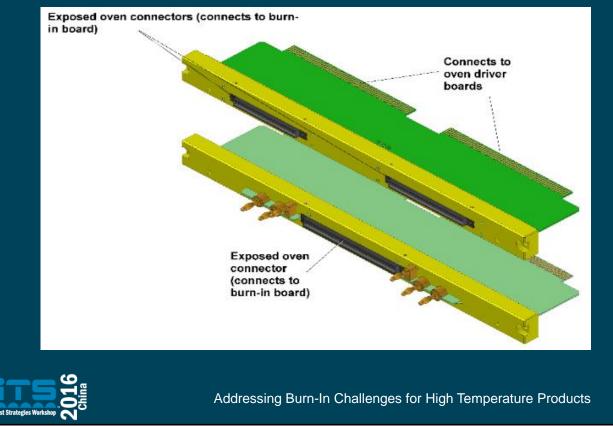
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Backplane Connectors

Mechanical shielding protect oven connectors from direct heat

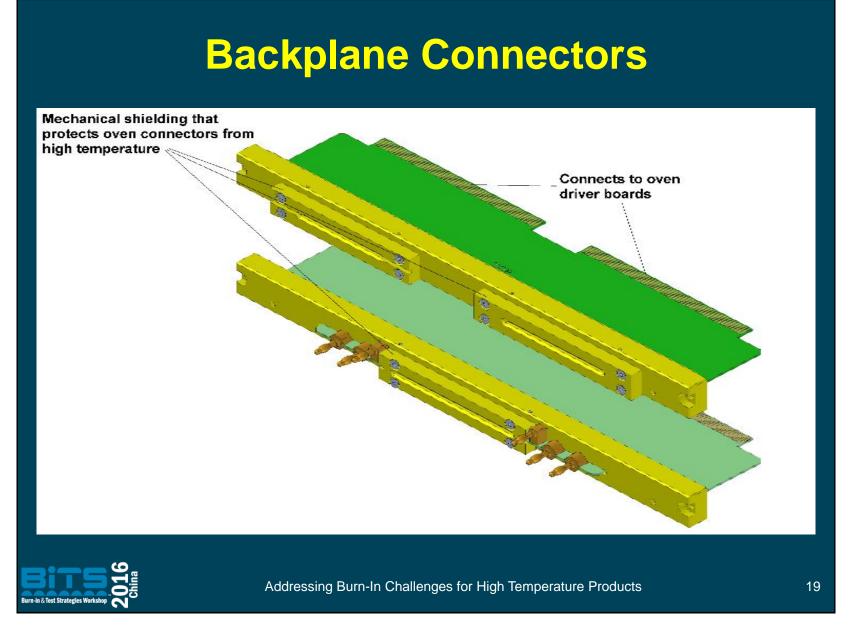


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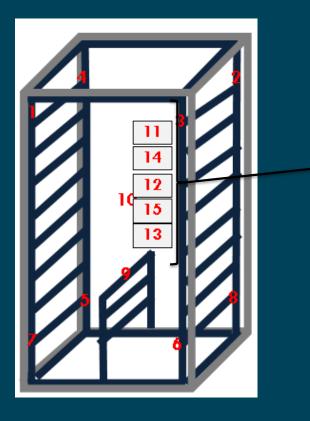
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Temperature Profiling

Thermocouple location points



Thermocouples embedded on the connectors (behind mechanical shielding)



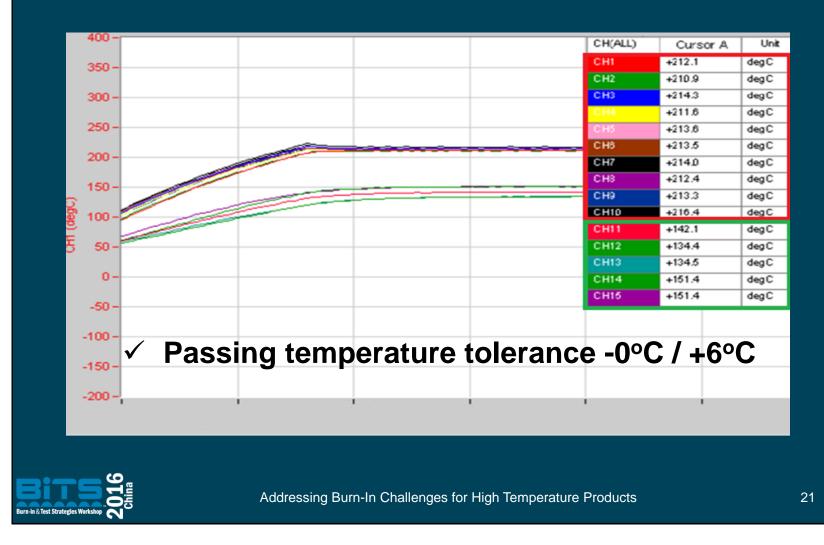
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20

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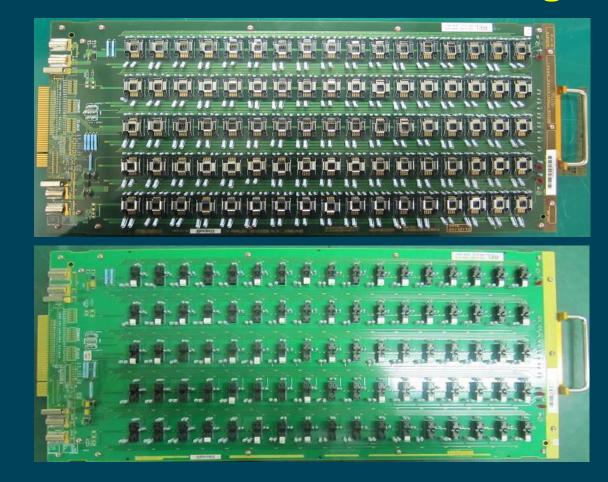
Temperature Profiling



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Burn-In Hardware Testing

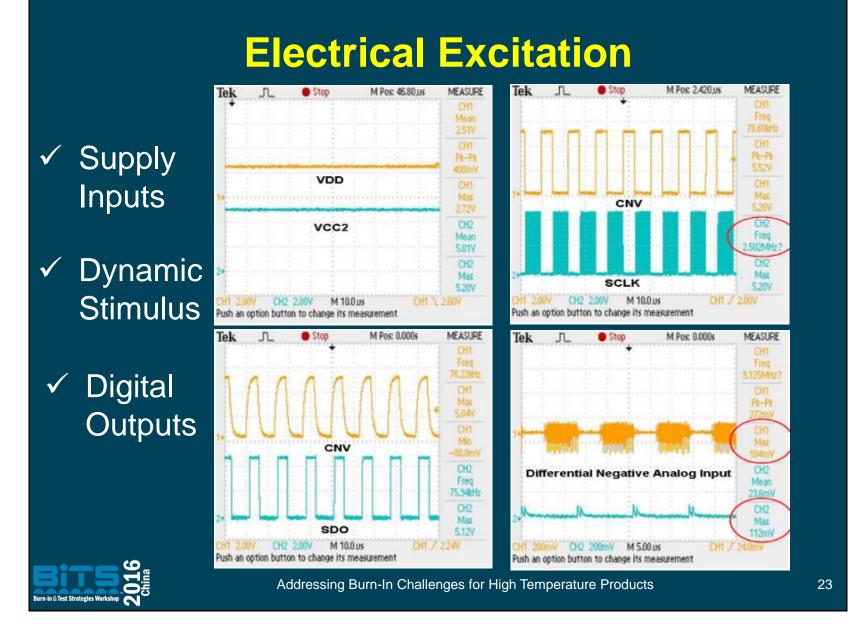




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Qualification Checklist

- BI Boards were checked and found compliant to board build specifications
- Device excitation was checked and found compliant to bias specification
- High temperature oven was qualified; documented according to equipment template



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24

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HT Qualification Results

 Plastic Micro Small Outline Package (MSOP) variant released to market;
2,000h HTOL @ 175°C

 Ceramic Flat Pack variant is scheduled to start HTOL @ 210°C



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Conclusion

This burn-in system passed all equipment qualification requirements and together with customized high temperature burn-in boards can support the qualification requirements of ADI's high temperature product portfolio.





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Recommendation

 Deploy mechanical shielding solution to other EDA chambers so it would be possible to run 175°C and 210°C HTOL qualifications in parallel.



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27

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- Manny Malaki, John Reyes and Rochyll Amarille of AD Philippines Test & Product Engineering
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28