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Burn-in & Test Strategies Workshop

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March 6-9, 2016

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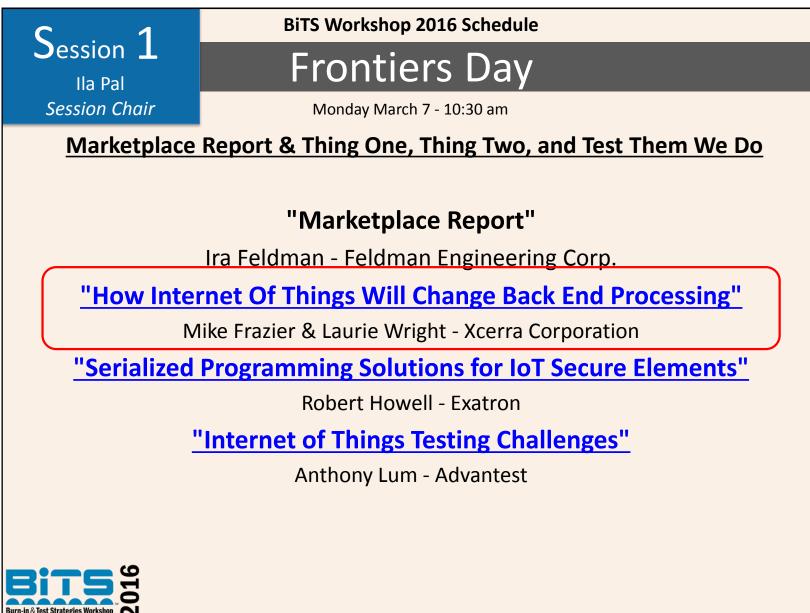
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**Burn-in & Test Strategies Workshop** 

Thing One, Thing Two, and Test Them We Do - Internet of Things

# HOW INTERNET OF THINGS WILL CHANGE BACK END PROCESSING

Mike Frazier Laurie Wright Xcerra Corporation



2016 BiTS Workshop March 6 - 9, 2016



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Thing One, Thing Two, and Test Them We Do - Internet of Things

# Agenda

- Technology requirements and business challenges of the Internet of Things market
- How IoT is changing back end processing – Handling

– Test

 Summary key technologies and capabilities for an Internet of Things test cell



HOW INTERNET OF THINGS WILL CHANGE BACK END PROCESSING

Thing One, Thing Two, and Test Them We Do - Internet of Things

## **IOT Overview**

- IOT Markets
  - Fitness and Wellness
  - Healthcare
  - Home and Office
  - Automotive
  - City
- Attributes
  - Smart (stand alone)
  - Low Power
  - Connected
  - Secure



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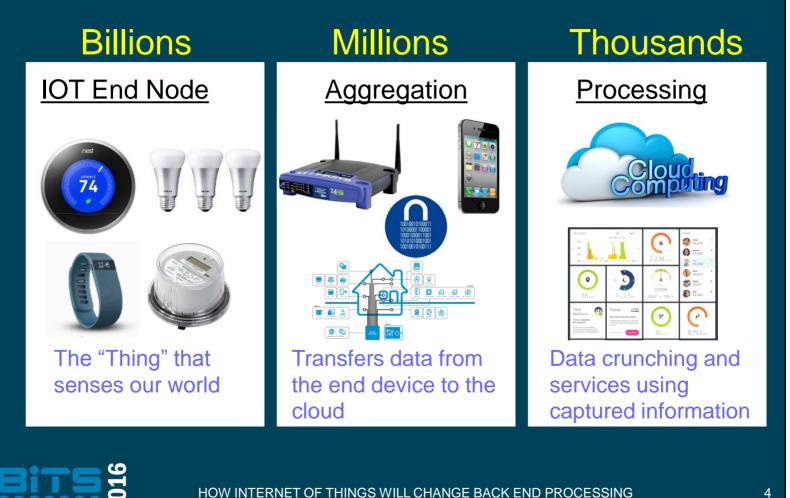




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# The Connected World





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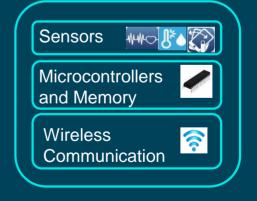
# **Internet of Things End Nodes**

#### • Sensors

- Enable us to connect our physical world to technology
- Many different sensors need physical stimulus

#### Processing capability

- Microcontroller have ultra-low power requirements, precision mixed signal integration, integrated memory in a small and inexpensive package
- Wireless communication
  - Transfer data to the cloud or smart device
  - Secure and low-power
  - Multiple standards competing in the market

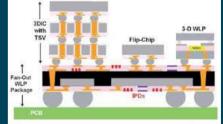




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# **Industry Challenges of IoT Market**

- Evolving industry
  - New applications development as standards emerge
  - High mix of devices with range of volumes behind each device
  - Fast development time
- Technology integration and packaging
  - Integration of different process technologies in a small packages
  - Sensor integration
- Cost pressures
  - Falling ASP for sensors, microcontrollers and radio







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# **IOT Effects on Device Test**

- Must meet technical requirements of the diverse IoT market
  - Digital, mixed signal, RF, embedded memory, power/automotive
  - Transaction based test patterns (protocol aware) test methodology
  - High quality test
- Highly scalable and reconfigurable tester
  - Address high mix of IoT devices
  - Ability for customization



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# **IOT Effects on Device Test**

- Microcontroller
  - Instrumentation optimized for MCU testing
    - Protocol aware
      - "Talk" to device in it native language (SPI, JTAG, CAN/LIN....)
    - Site dependant data
      - Ability to efficiently write different data to many sites (MAC address, Calibration data...)
    - Concurrent Test
      - Test different features of device at same time (Memory, Scan/BIST, Analog)
  - Mixed-Signal
    - High accuracy and high speed ADC/DAC testing
      - 8 bit going to 12 to 16 bit or more



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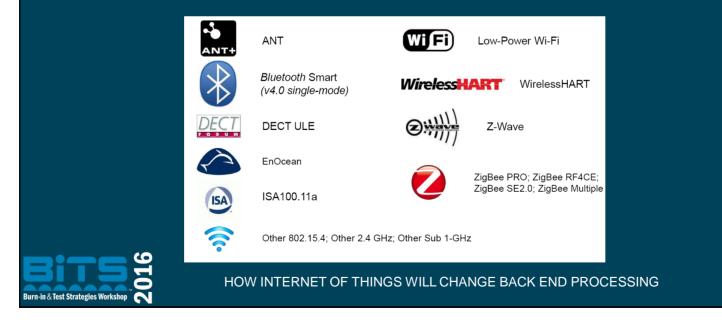
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# **IOT Effects on Device Test**

- Wireless connectivity
  - Multiple standards vying for adoption
  - Lower end RF is enough
    - Lowest power consumption will win
    - RF Test cost must be minimal



Nighthawk



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# **IOT Effects on Device Test**

- Low Cost of Test
  - High Utilization
    - Re-configurable system to match device requirements
  - Ability to cost effectively test low multi-site/high mix through very high multi-site (>256 sites)
  - High multi-site efficiency
  - Low CAPEX and operating costs



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# **IOT Effects on Device Handling**

More than just presenting device to the tester...

- Must provide physical stimulus to the sensor
  - Inertia, pressure, gas, temperature, light, sound, touch...
  - Integrated sensors (10DOF, environmental sensors) with demanding test challenges, minimize number test insertions
  - Higher performance (high SNR)
- Wide variety package types and small sizes
  - Increase WLCSP for low cost and small size
  - WLCSP, package, strip
  - Less than 1mm x 1mm
  - Fine pitch devices





Source: Gila Tool



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# **IOT Effects on Device Handling**

- High mix IoT devices and applications
  - Flexible
    - Reconfigurable and reusable for a wide product mix
    - Simple and fast change kit conversion
    - Equipment modularity for high test cell utilization
    - Tri-temperature for all markets
  - Adaptable
    - Cover all emerging new applications and sensors

#### • Faster time to high volume

- Scalability from engineering to high volume production
- Integrated and optimized test cell for highest performance



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# **IOT Effects on Device Handling**

- Cost Pressures
  - Focus on achieving lowest Cost of Test
    - Number of devices tested for 1 cent
  - Low operating costs (resources, floor space)
    - Minimize test cells and different platforms
  - High parallelism and high UPH
- Efficient high volume retest, re-programming or returned material (RMA) to minimize scrap or shelving of inventory
  - Changing customer demand
  - Tape and reel back into high volume test









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# **Key Features for an IoT Test Cell**

- Diverse technical capability
  - Flexible test solution that covers wide range of technology
  - Sensors, WLCSP, tri-temp, small devices...



Xcerra Test Cell

- Efficient Test
  - Time to Market
  - Ability to scale volume
- Low Cost of Test



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# Summary

- IoT will be a significant driver for semiconductor revenue over the next 5 years.
- The evolving IoT industry will drive change in the way we approach back end processing.
- Handling and test equipment will need to be flexible and adapt to the technical and business challenges of the dynamic IoT market.





InStrip/InMEMS



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