

SIXTEENTH ANNUAL

**BiTS**™

**Burn-in & Test Strategies Workshop**

March 15 - 18, 2015

Hilton Phoenix / Mesa Hotel  
Mesa, Arizona



**Archive – Session 2**

## Session 2

Mike Noel  
*Session Chair*

BiTS Workshop 2015 Schedule

## Frontiers Day

Monday March 16 1:30 pm

### Spanning the Socket Rainbow

#### "Contacting Solutions for High Power Bare Die Testing (IGBT MOS-FET and Diodes)"

Markus Wagner - Cohu SEG

#### "Comparison of Different Methods in Determining Current Carrying Capacity of Semiconductor Test Contacts"

Valts Treiberis - Xcerra Corporation

#### "Are New Temperature Test Strategies Needed? Meeting Performance and Cost Requirements of Today's Applications"

Andreas Nagy - Xcerra Corporation

#### "Extreme Temperature and High Current Testing Challenges of Automotive Devices"

Praveen kumar Ramamoorthy & Murad Hudda - Infineon Technologies

Dan Maccoux & Muhamad Izzat bin Roslee - JF Microtechnology

## Copyright Notice

The presentation(s)/paper(s) in this publication comprise the Proceedings of the 2015 BiTS Workshop. The content reflects the opinion of the authors and their respective companies. They are reproduced here as they were presented at the 2015 BiTS Workshop. This version of the papers may differ from the version that was distributed in hardcopy & softcopy form at the 2015 BiTS Workshop. The inclusion of the presentations/papers in this publication does not constitute an endorsement by BiTS Workshop or the workshop's sponsors.

There is NO copyright protection claimed on the presentation content by BiTS Workshop. However, each presentation is the work of the authors and their respective companies: as such, it is strongly encouraged that any use reflect proper acknowledgement to the appropriate source. Any questions regarding the use of any materials presented should be directed to the author(s) or their companies.

The BiTS logo and 'Burn-in & Test Strategies Workshop' are trademarks of BiTS Workshop. All rights reserved.

## Are New Temperature Test Strategies Needed? Meeting Performance and Cost Requirements of Today's Applications

**Andy Nagy**

Multitest Handler Group & Xcerra Test Cell Innovation

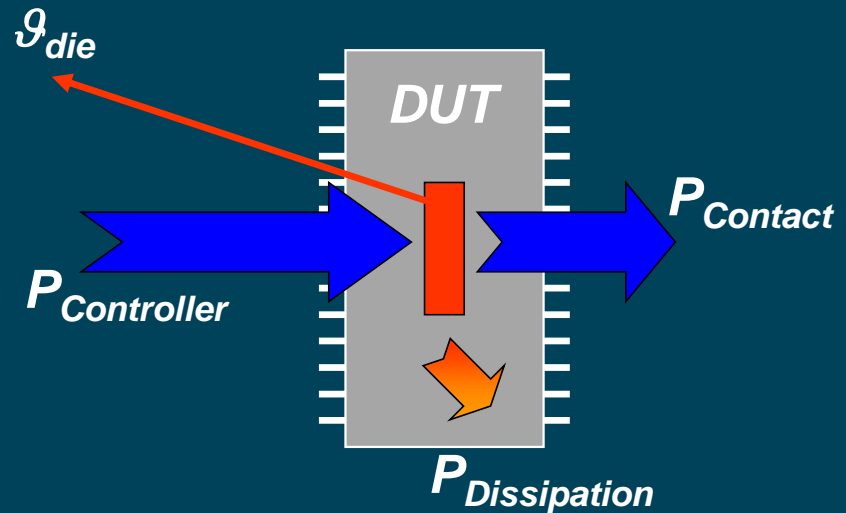


2015 BiTS Workshop  
March 15 - 18, 2015



# DUT Temperature Physics

Formula to maintain die temperature inside the DUT at set temperature (no drift after contacting):



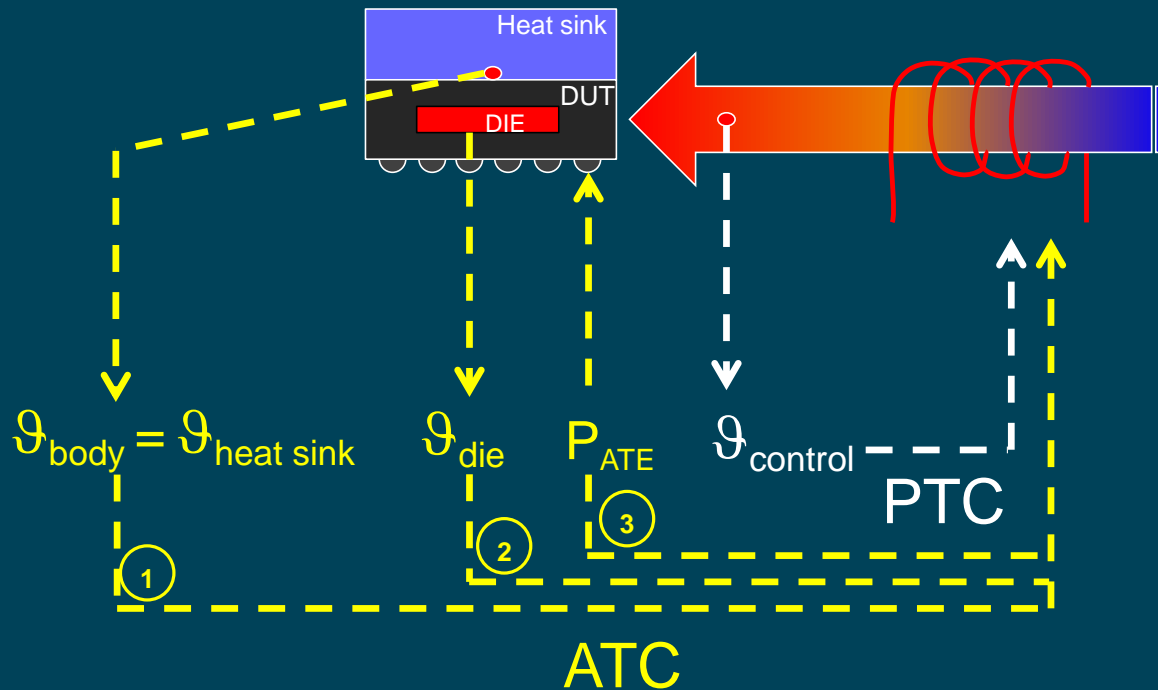
$$P_{controller} = P_{contact} - P_{Dissipation}$$

Energy transferred from handler into DUT

Energy loss through contactor

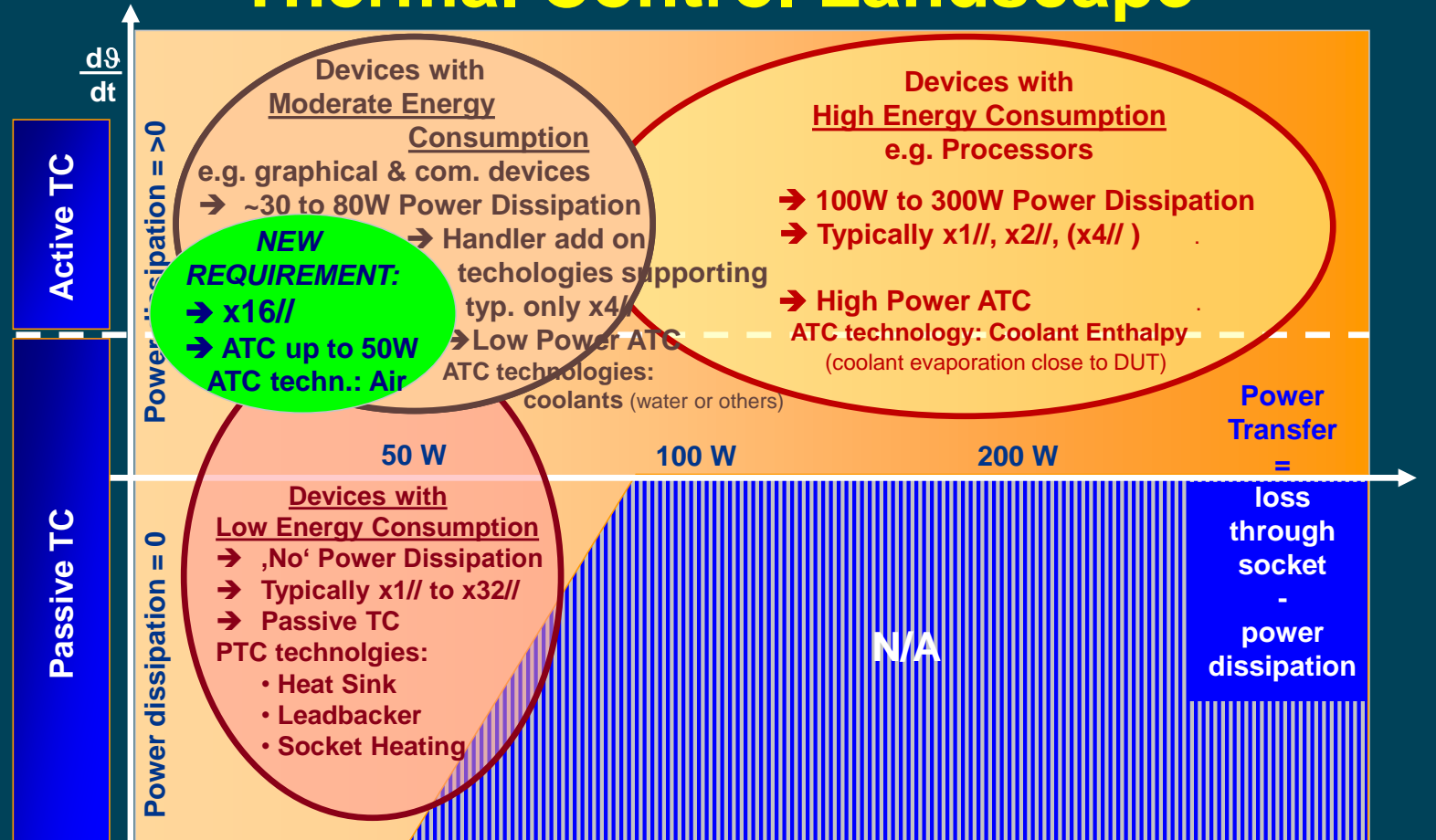
Energy removed by power dissipation from DUT

## Thermal Control Concepts



PTC = Passive Thermal Control  
 ATC = Active Thermal Control

## Thermal Control Landscape



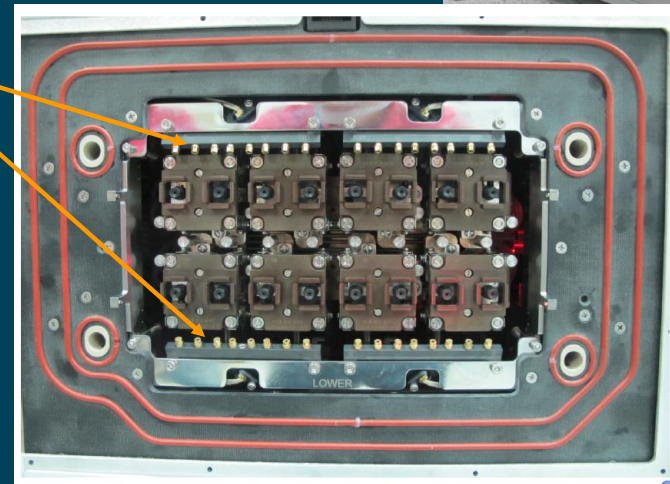


## Example for successful PTC

- **Key enabler:**
  - Site specific temperature control architecture
  - Temperature maintained at every state
  - Kit specific setup calibration
  - DUT with little power consumption
- **QFN 7x7 x16// Setup @ MT9510**

- Min. one air control nozzle for convective heating / cooling per plunger to maintain temperature of the DUT and the socket

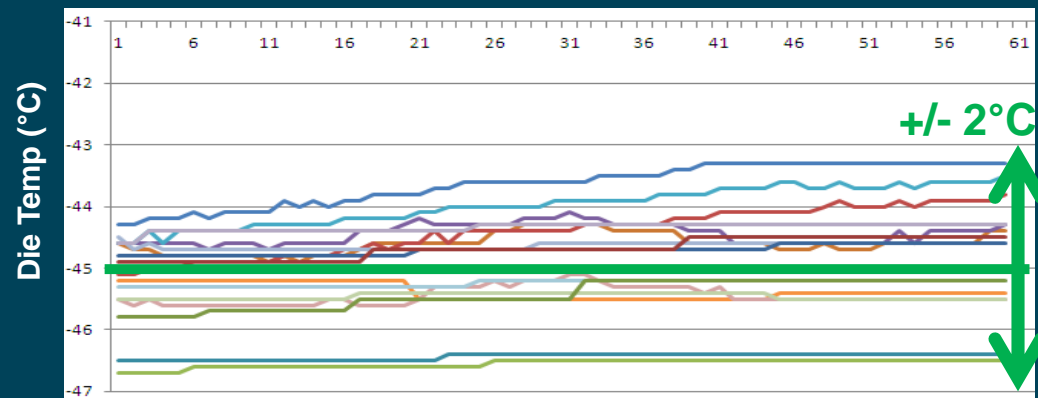
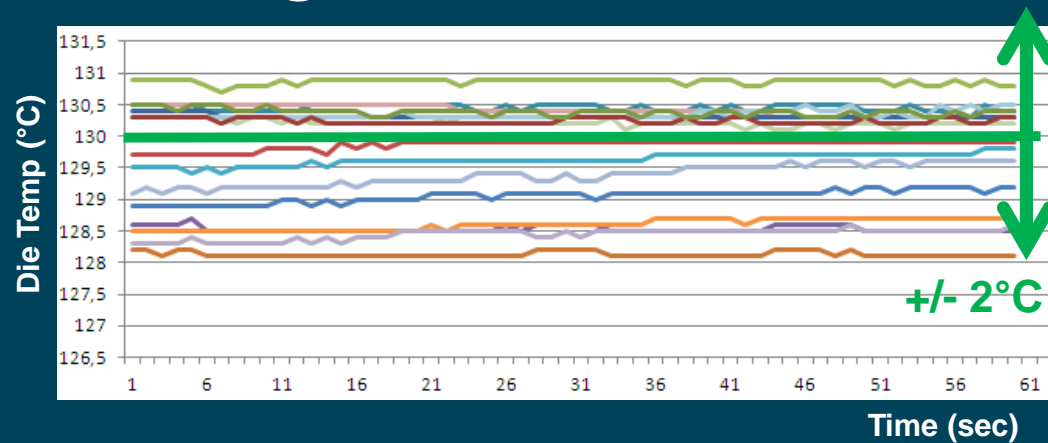
- If no device is under test, controlled air will keep socket and pogo pins on temperature





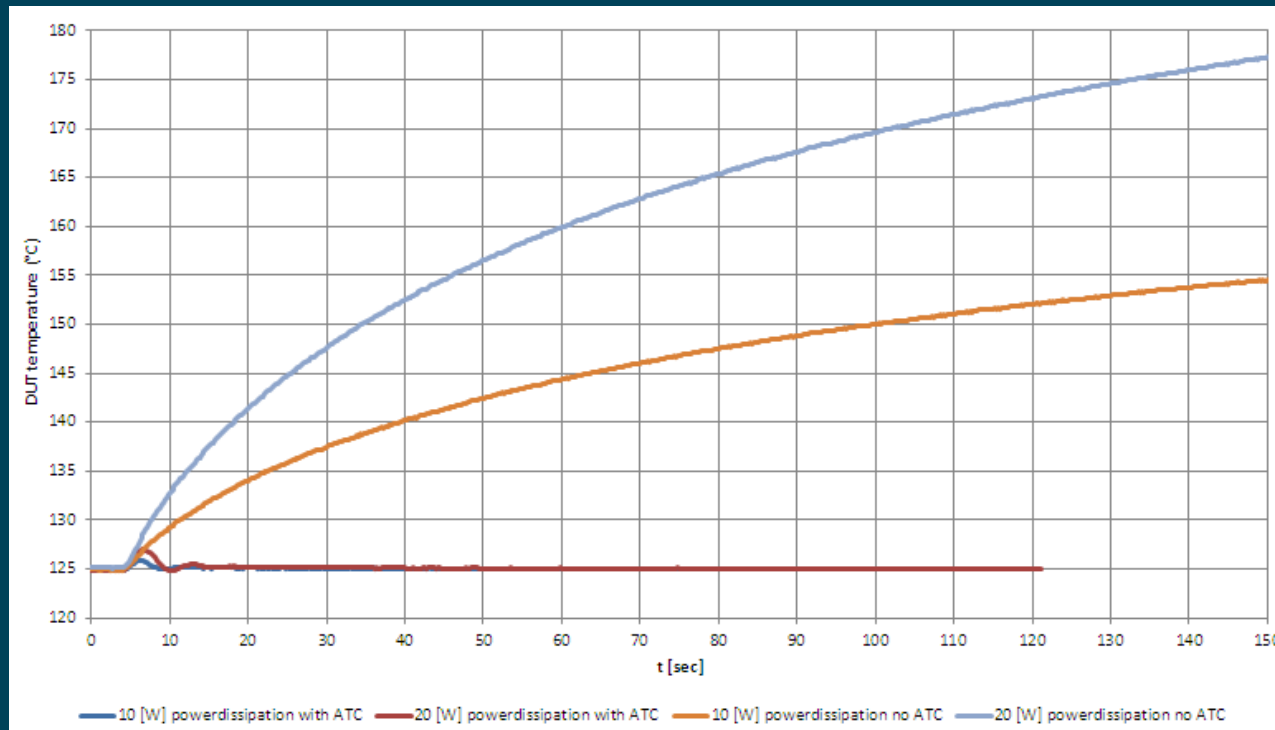
## Example for successful PTC

- MT9510 - QFN7x7 x16 sites @ -45°C & 135°C
- Test site stability during long test times within  $\pm 2^\circ\text{C}$  spec



# PTC Limitation vs ATC Capability

Power consumption at electric test will require power dissipation at longer test times to maintain DUT temperature



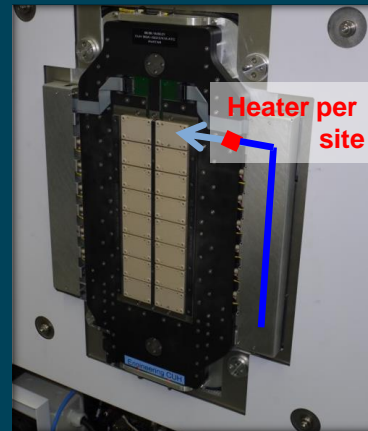
## Modular ATC Approach

- MT2168 P&P handler + ATC

MT2168 P&P



ATC CUH  
for x16//  
(Contact Unit Holder)

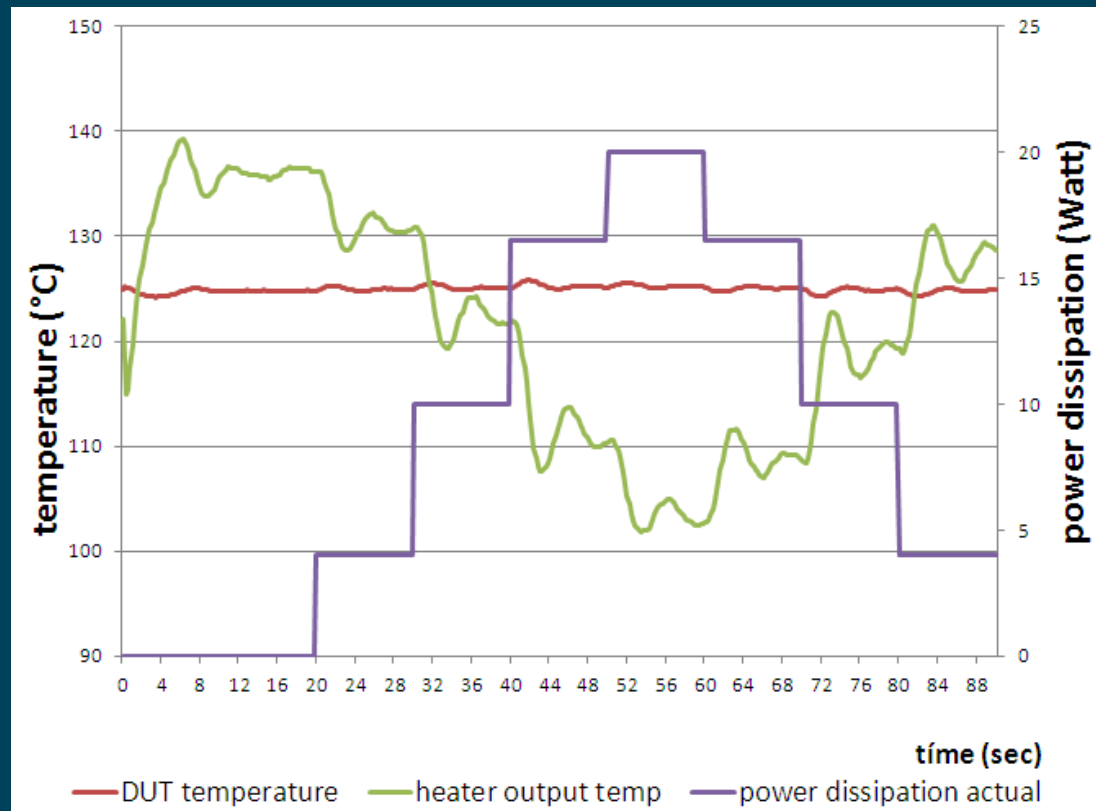


ATC Sensor routed via  
leadbacker to the socket



## ATC Example @ 125°C (w/o chiller)

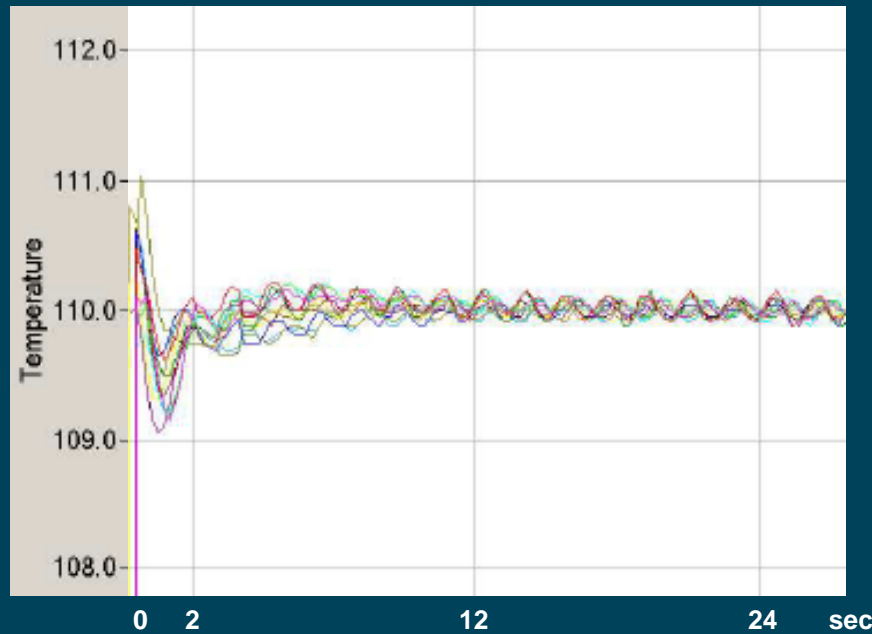
- ATC setup with mock-up device to emulate different power scenario



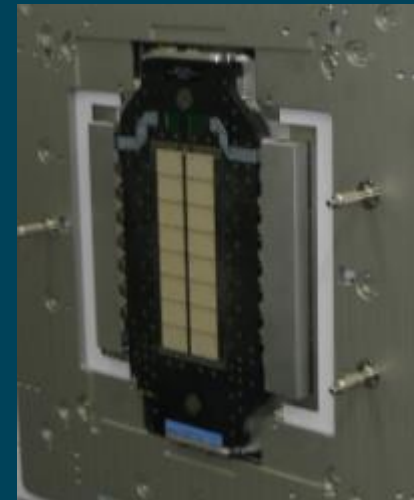
## Temp. Accuracy Example with ATC

Test-Case

MT2168 BGA745/12x12 12 sites



Up to  
16-site



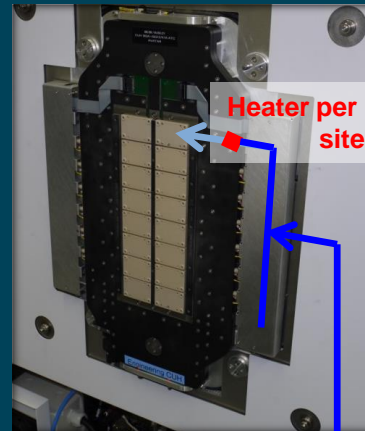
## Cold for Characterisation via ATC

- MT2168 Amb / AH handler + ATC + Chiller = **Tri-temp P&P handler for device characterisation**

MT2168 Amb / AH

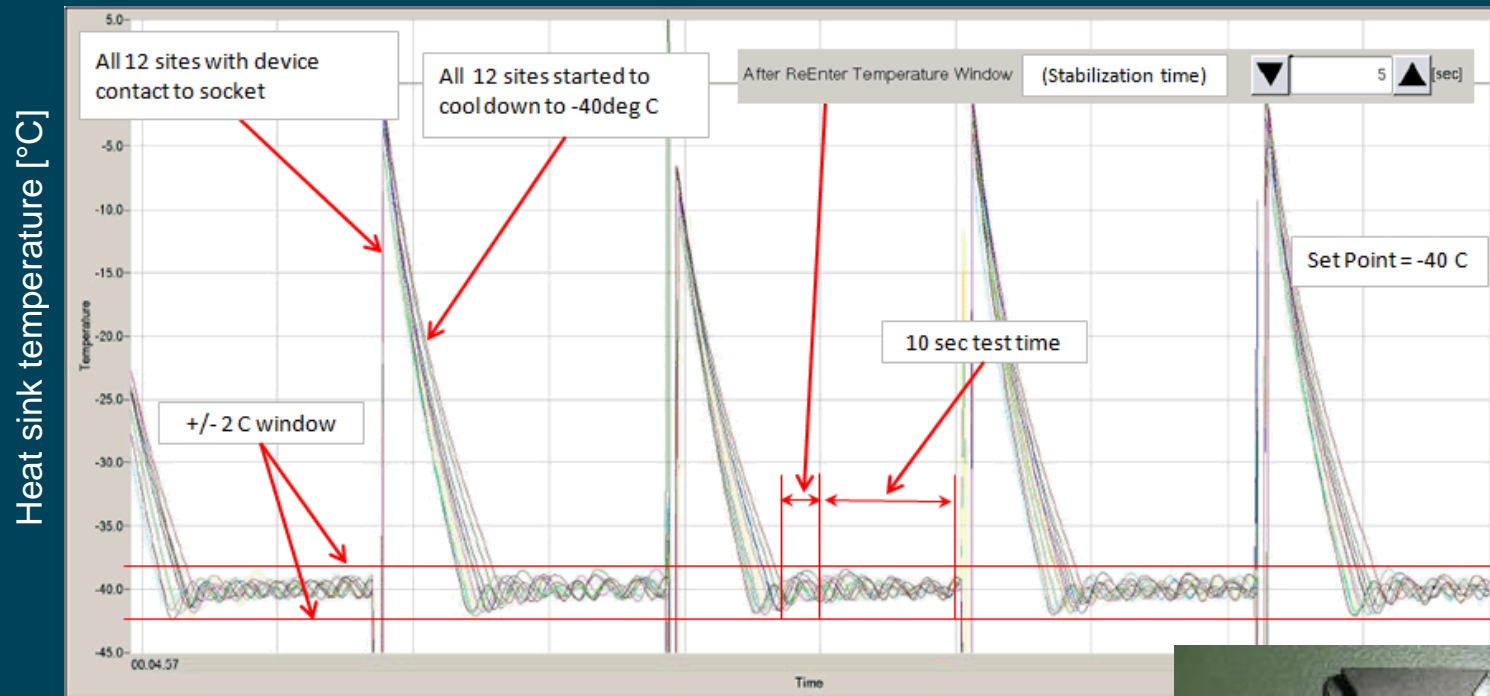


ATC CUH  
for up to x16//



External cold dry air  
(e.g. from any 3rd party chiller)

# MT2168 Cold for Characterisation



Time / 10sec per division

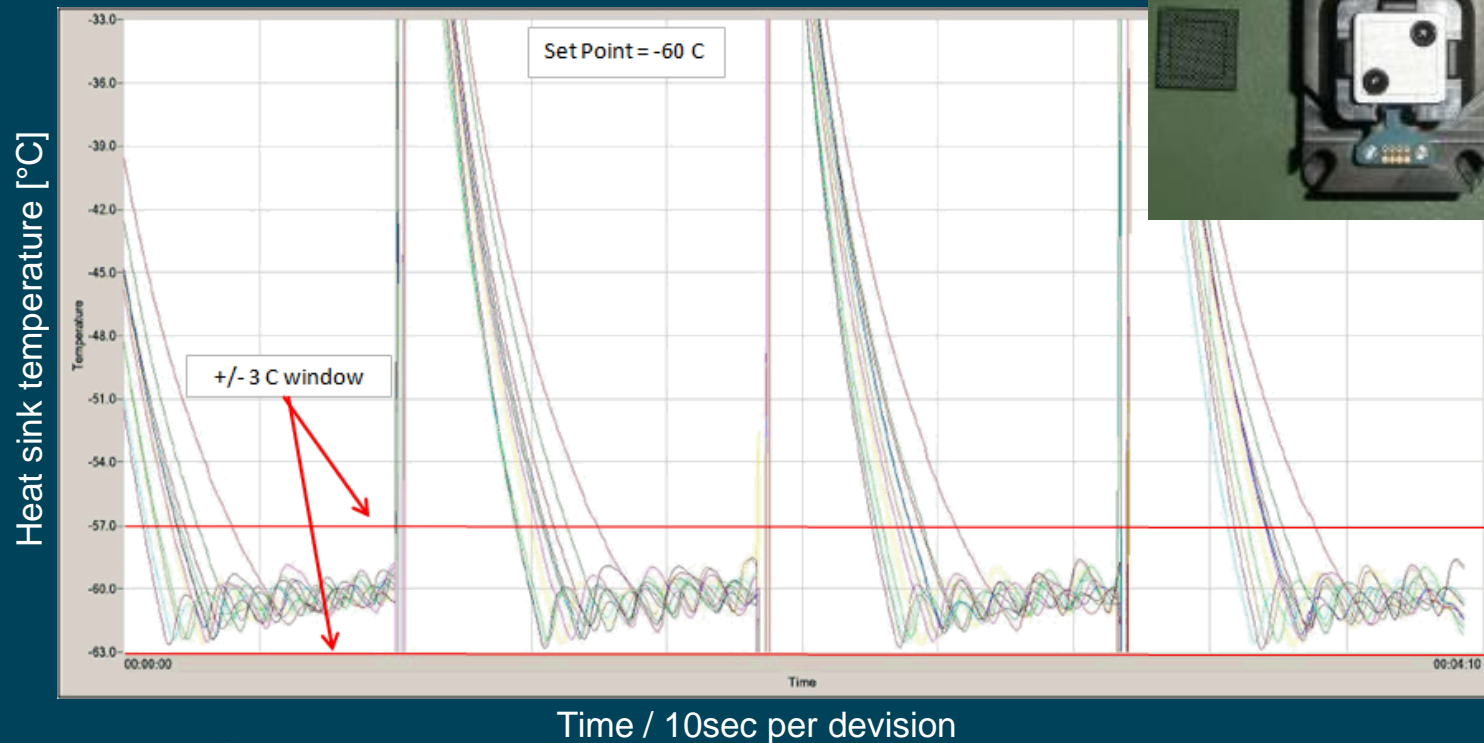
- MT2168 AH + ATC + External Chiller
- BGA745/12x12 @ -40 °C





# MT2168 Cold for Characterisation

- MT2168 AH + ATC + External Chiller
- BGA745/12x12 @ -60 °C



## Summary

New Temperature Test Strategy offered via

a) Modular x16// ATC up to 50W

b) Cold for Characterisation

### Benefits:

1. x16// ATC Test → Cost of Test ↓
2. Robust design → OEE ↑  
Air supply versus coolant  
→ Lower complexity & easier maintenance
3. Modular field upgradeable technology → ROI ↑
4. Cold for Characterisation → Cost of Test ↓  
= Tri-temp test at Amb. & AH Handlers  
= supporting cost requirement of AH markets  
that still require cold characterisation

