

PACKAGE TEST IS A DIRTY BUSINESS

Socket Cleaning Strategies to Reduce Cost of Test and Improve Overall Equipment Effectiveness (OEE)

by

Jerry Broz, Ph.D.

VP World Wide Applications International Test Solutions

G et down and dirty with Jerry Broz, Ph.D. of International Test Solutions, as he shares his strategies for socket cleaning, thereby reducing the cost of test and improving equipment effectiveness in his presentation titled, "Package Test is a Dirty Business!!!". In this broad tutorial, Broz will discuss various off-line and on-line socket cleaning practices, including high volume testing challenges and solutions for on-line cleaning to maintain high yield and throughput, reduce socket repairs, and control contactor damage.

ABSTRACT

uring assembled device testing with new socketing solutions, the connectors are clean and have stable resistance values. With continuous testing, contamination accumulated within the socket and materials are transferred to the contactor. Sources of resistive contamination are numerous and may include oxides, accumulated metals, residues from processing, or debris from the test environment. Over time, these adherent materials will form non-conductive "layers" that dramatically affect the electrical performance of the contactor. Experience has shown that over 70% of device yield fallout and re-screen can be attributed to electrical contact related issues.

To avoid multiple rescreen resulting in increased Cost of Test (COT), sockets have been historically maintained with various off-line cleaning practices combined with frequent pin replacement. Clearly off-line methods reduce throughput and increase tool downtime, so socket makers and pin designers have focused on robust technologies (e.g., tip geometries, platings, alloys, etc.).

In recent years, the major handler suppliers have developed tools with programmable auto clean functionalities capable of regular socket cleaning without substantial downtime. Such improvements in test infrastructure are critical for lowering COT and the recent developments support this trend.

In this broad tutorial, various off-line and on-line socket cleaning practices will be surveyed and discussed. High volume testing challenges and solutions for on-line cleaning to maintain high yield and throughput, reduce socket repairs, and control contactor damage will be covered. Key information regarding the auto clean functionalities from the major handler suppliers as well as recommendations for implementation will be presented and several case studies will be reviewed.

COPYRIGHT NOTICE

The paper(s) in this publication comprise the Proceedings of the 2013 BiTS Workshop. The content reflects the opinion of the authors and their respective companies. They are reproduced here as they were presented at the 2013 BiTS Workshop. This version of the papers may differ from the version that was distributed in hardcopy & softcopy form at the 2013 BiTS Workshop. The inclusion of the papers in this publication does not constitute an endorsement by BiTS Workshop, LLC or the workshop's sponsors.

There is NO copyright protection claimed on the presentation content by BiTS Workshop, LLC. (Occasionally a Tutorial and/or TechTalk may be copyrighted by the author). However, each presentation is the work of the authors and their respective companies: as such, it is strongly encouraged that any use reflect proper acknowledgement to the appropriate source. Any questions regarding the use of any materials presented should be directed to the author(s) or their companies.

The BiTS logo and 'Burn-in & Test Strategies Workshop' are trademarks of BiTS Workshop, LLC. All rights reserved.



Package Test is a Dirty Business

Cleaning Strategies to Reduce Cost of Test and Improve Overall Equipment Effectiveness (OEE)

Jerry Broz, Ph.D.

VP Applications
International Test Solutions, Inc.



2013 BiTS Workshop March 3 - 6, 2013



International Test Solutions (ITS) Innovation and Expertise Technological Leadership Cleaning solutions for front end, wafer ITS - Support | Sales | Application test, and package test **Market Share** #1 supplier of advanced cleaning **Core Strengths Unique Product Portfolio** Patented Technologies Engineered, advanced polymer materials **Global Presence** US, China, Korea, Taiwan, Japan, and Singapore **Customer Collaboration** History of performance and support 3/2013 Tutorial: Package Test is a Dirty Business

ITS - Who Are We?

- Global supplier of highly engineered cleaning materials for the wafer sort, package test, and front-end tools since 1997.
- Cost-effective cleaning solutions and technical services focused on process improvement solutions and cost savings.
- "Manufacturing Center" for with innovative manufacturing technologies and advanced polymer materials R&D.
- "Test Analysis Center" for performance characterization and cleaning process development.
- Global technical services, expertise, and product support.

3/2013

Tutorial: Package Test is a Dirty Business

3

Outline

- Fundamentals
 - Package Test is a "Dirty Business"
 - Contact Resistance (CRES) a very basic review
 - Offline and Online Methods of Probe Cleaning
- High Volume Testfloor Implementation
 - Auto Contact Cleaning (ACC) Utilization
 - Examples of Implementation
 - Approach to "Recipe" Development
- Summary

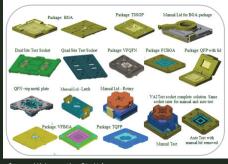
3/2013

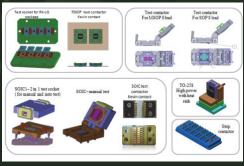
Tutorial: Package Test is a Dirty Business



Test Socket

- Test Socket provides a "real-world" interface between a DUT (device under test) and the tester (ATE)
- Test programs determine the pass / fail status of the DUT
- Upon test completion, the device binned, and so on ...





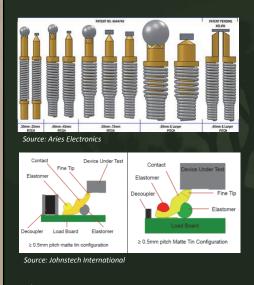
Source: VA Innovation Pte Ltd

3/2013

Tutorial: Package Test is a Dirty Business

5

Making Contact



- Various types of contactors are brought into physical contact with I/O's of a DUT.
- Test programs are run to determine the DUT pass / fail under the assumption of "perfect" contact.
- Multiple re-screen is performed to recover DUTs that failed during first pass testing.

3/2013

Tutorial: Package Test is a Dirty Business



Package Test is a "Dirty Business"

- Semiconductor packages carry adherent debris and other contaminants that affect electrical contact integrity.
- Debris / contaminants will be found on tip contact surfaces, around the pins, along guide plates, and across the socket bed.
- Contactors must physically touch the I/O's (pads, bumps, pillars, etc.) of the DUT for test programs to be executed.
- "Contact and mechanical motion" is CRITICAL to break surface oxide(s), but creates more debris and material transfer to contactors.

3/2013

Tutorial: Package Test is a Dirty Business

7

Many Sources of Contamination

- Material transfer from the device
- Localized material loss and pick-up
- Debris accumulation on contacts and across socket bed
- Intermetallic formation on the test pin contact area
- Oxidation (thick and thin non-conductive films)
- Mechanical wear and tip shape change over time
- Plating related issues (cracking, flaking, etc.)

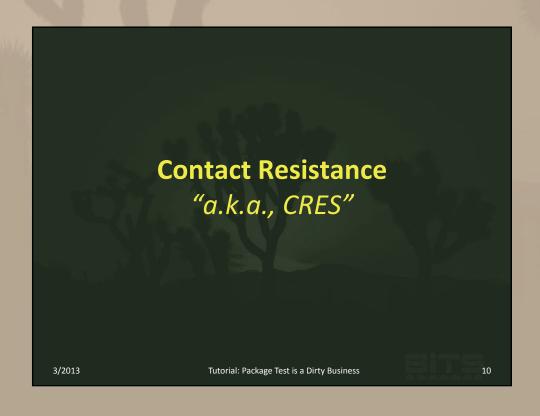
Contamination generates more contamination!

3/2013

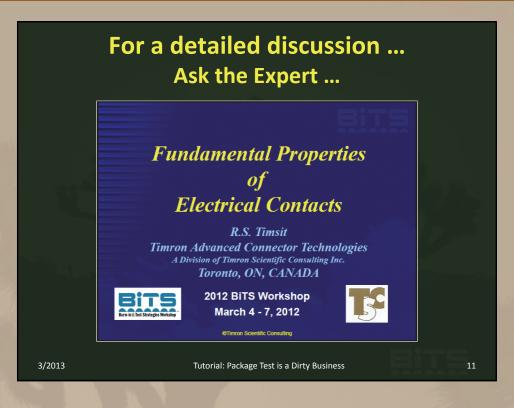
Tutorial: Package Test is a Dirty Business











Contact Resistance (CRES)

- "Contactors Touch the Device and the Current Flows"
- Contact Resistance (CRES) is the most CRITICAL parameter in all electrical testing
 - "Metal on Metal Contact" between a probe tip and the pads, bumps, pillars, etc.
 - Non-conductive films will build-up and interfere with the "Metal on Metal Contact"
 - Film resistance is affected by absorbed materials various oxides and compounds, and miscellaneous contaminants
 - Film resistance will eventually dominate contact reliability

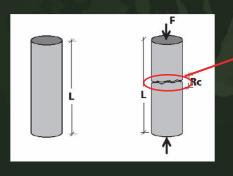
3/2013

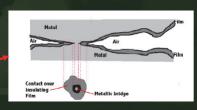
Tutorial: Package Test is a Dirty Business



Contact Resistance (CRES)

- Contact Resistance Fundamentals ...
 - CRES occurs between two bodies in contact
 - Creates losses in electrical and thermal systems





- Current flow is constricted to the inter-metallic contacts
- Localized joule heating

3/2013

Tutorial: Package Test is a Dirty Business

13

Classical CRES Definition

- Contact Resistance (CRES)
 - CRES stability (and instability) is entirely attributable to interfacial phenomena across contact areas (<u>Metallic Contact</u>) and with adherent contaminants (<u>Film Resistance</u>)

METALLIC CONTACT
$$C_{RES} = \frac{\left(\rho_{probe} + \rho_{pad}\right)}{4} \sqrt{\frac{\pi H}{P}} + \frac{\sigma_{film} H}{P}$$
R. Holm, 1967 FILM RESISTANCE

3/2013

Tutorial: Package Test is a Dirty Business

Major Parameters

- Surface Roughness
- Surface Hardness
- Electroplates for surface modification
- Inter-diffusion
- A-spot Temperature (localized Joule heating)
- Elevated Test Temperatures
- Signal Frequency ("Skin Effect")
- Small Contacts

3/2013

Tutorial: Package Test is a Dirty Business

15

Key Mechanisms

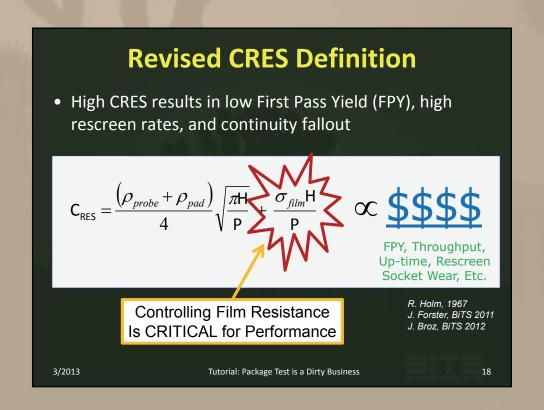
- Surface Insulating Films
 - Affect contact performance since these layers add to contact resistance
- Contact Degeneration
 - Oxidation, corrosion, fretting corrosion, intermetallic growth, differential thermal expansion, etc., eventually will limit connector life

3/2013

Tutorial: Package Test is a Dirty Business



Unstable Contact Affects Performance • Clear majority of yield fallout and re-screen problems can be attributed to contact related issues Handler Related Contact Related Interface Related B. Gibbs, BiTS 2006 Tutorial: Package Test is a Dirty Business 17









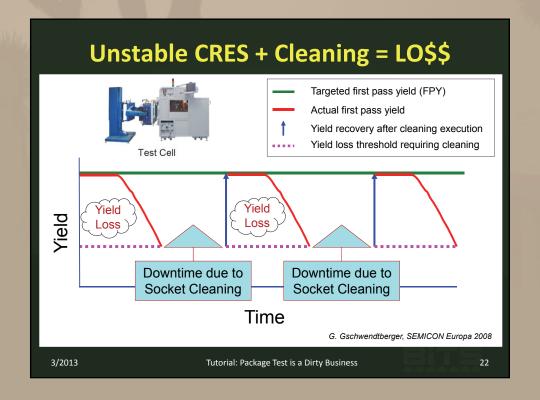


Film Resistance is Controlled with Contactor / Socket Cleaning

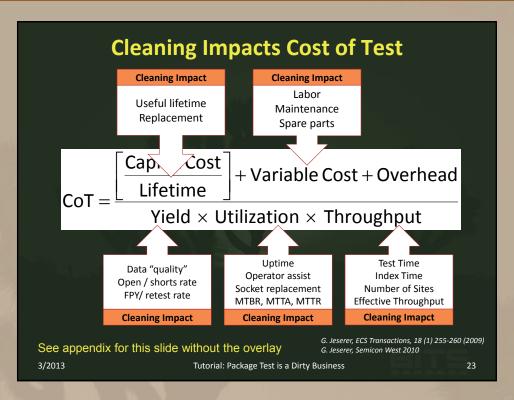
- Socket maintenance is critical to control CRES and maximize contactor electrical performance
- Off-line cleaning (idle state with potentially long downtime)
 - Pins in sockets and sockets in load-boards are replaced at added cost
 - Socket lifetime can be reduced due to cleaning related damage
 - Excessive cleaning can reduce test throughput without yield benefits
- On-line cleaning (consistent CRES control and limited downtime)
 - Socket and load boards remain docked (no idle state needed)
 - Debris and adherent materials are removed from socket in-situ
 - Consistent cleaning to maintain high FPY yields and without downtime

3/2013

Tutorial: Package Test is a Dirty Business









Offline Cleaning Methods

- Manual Methods
 - Abrasive cleaning
 - Dry brushing with various fiber / metallic brushes
 - Taping with "3M Scotch Mending Tape"
 - Rubber eraser ("Magic Eraser")
- Chemical Methods
 - Ultrasonic in various detergents + DI water
 - De-Ox methods
 - Replating / restoration
- Non-Contact Methods
 - CO2 Snow Blasting
 - Laser Ablation

3/2013

Tutorial: Package Test is a Dirty Business

25

Abrasive Lapping Method

- 3M Imperial Lapping films are used for plastic, metals, fiber optics connectors, metallographic, and other lapping and polishing applications ... <u>like sockets</u> (?)
- Entry of these lapping materials was most likely a "crossover" from probe card cleaning.



Source: 3M Micro-abrasives

3/2013

Tutorial: Package Test is a Dirty Business

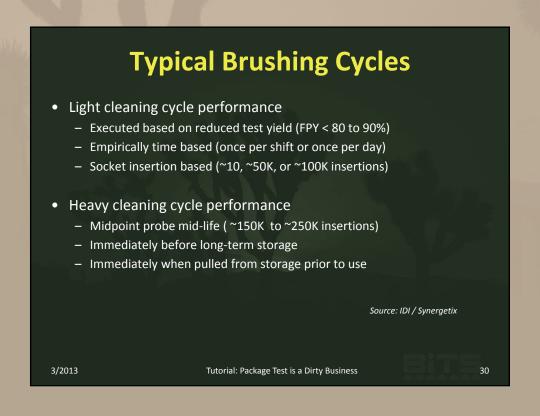










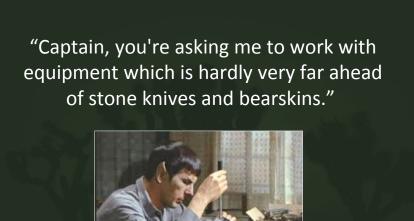












3/2013

Tutorial: Package Test is a Dirty Business

33

"3M Tape" for Rubber Sockets

- Contact elements are softly cleaned with "3M Magic Mending Tape" to remove adherent debris and process residuals.
- High adhesive, cellophane tapes could damage the contactors or leave residues and are not recommended
- · Daily cleaning is suggested
- Contactors will eventually become contaminated and need to be replaced.









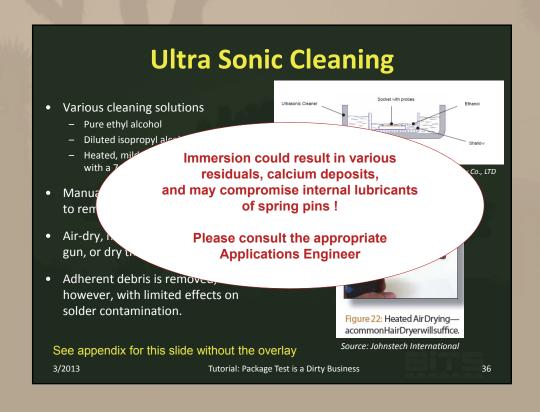
Source: Future Hitech Co., Ltd.

3/2013

Tutorial: Package Test is a Dirty Business

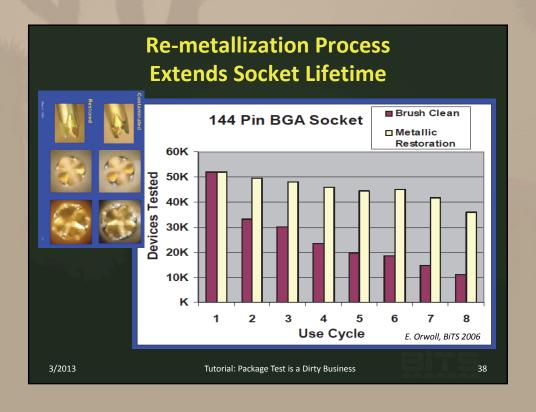


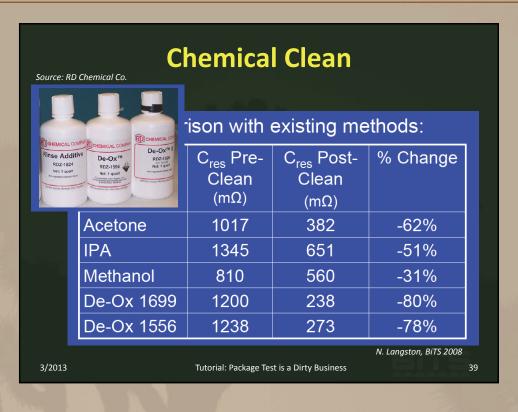


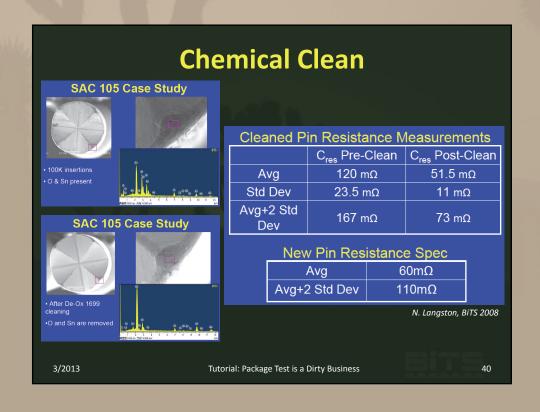






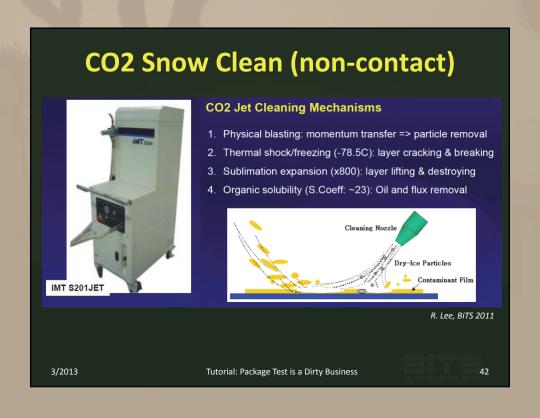


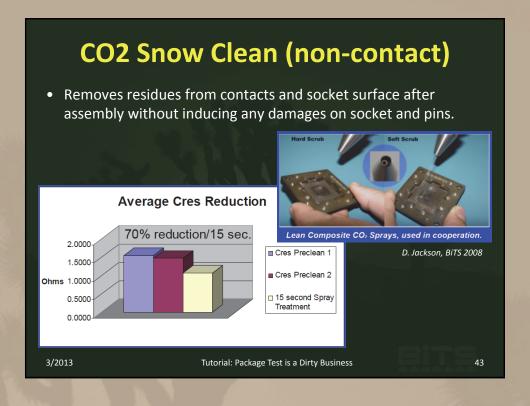




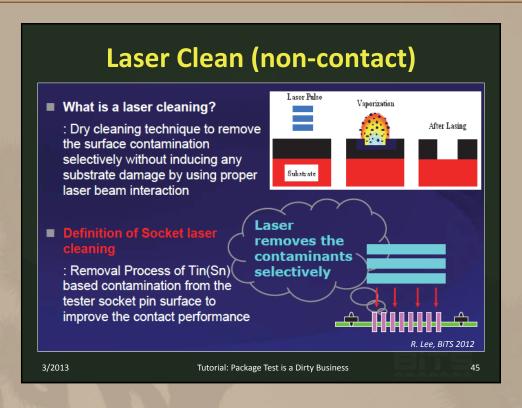






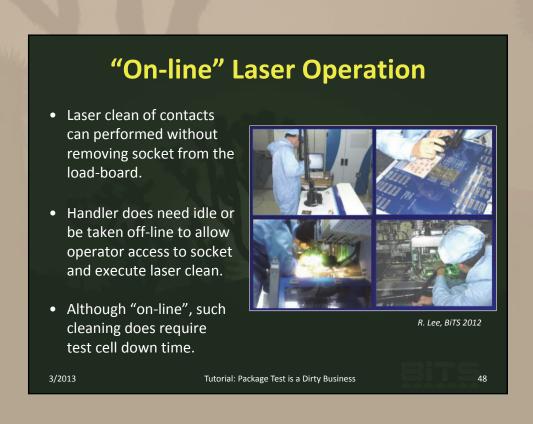




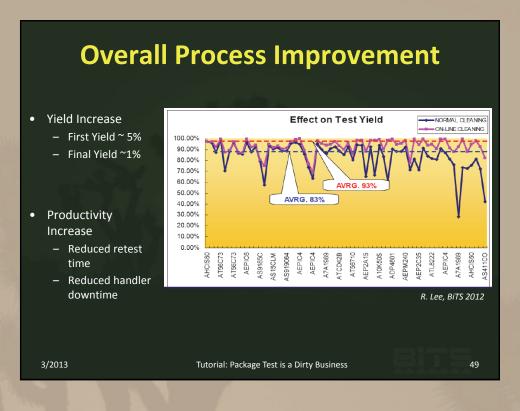


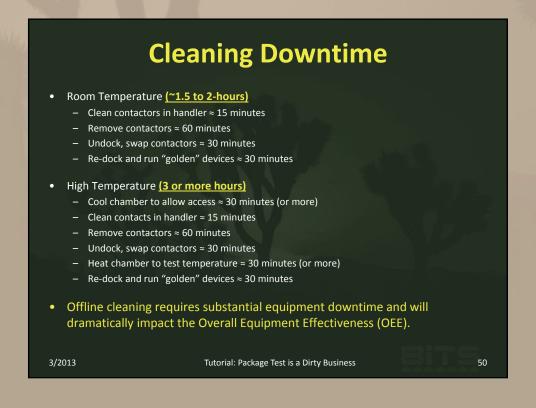


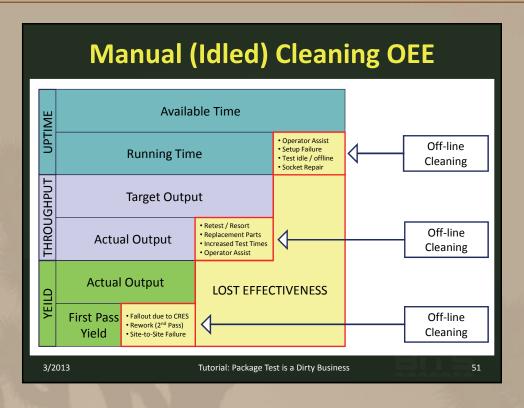


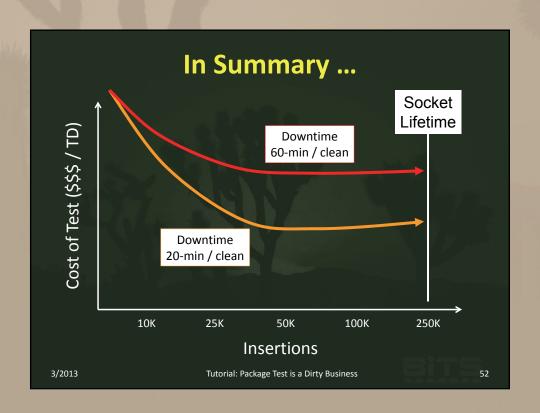


















- Overall equipment effectiveness (OEE)
 - Test-floor performance as defined by Uptime \times Throughput \times Yield
- Total effective equipment performance (TEEP)
 - OEE against calendar hours, i.e.: 24 hours per day, 365 days per year.





3/2013

Tutorial: Package Test is a Dirty Business

Understanding the Metrics

- OEE = Uptime × Throughput × Yield
 - "Uptime" = equipment running time vs. available time
 - For example, if a machine was available to run 16 hours but was only run for 12, then the "Availability" is 75 percent (12/16).
 - "Throughput" = running speed vs. maximum capability
 - For example, if a machine produced 70 pieces per hour but the capability of the machine is 100, then the "Performance" is 70 percent (70/100)
 - "Quality" = good parts produced vs. total number of parts tested
 - For example, if 100 parts are made and 90 of them are good, the "Quality" is 90 percent (90/100).

3/2013

Tutorial: Package Test is a Dirty Business

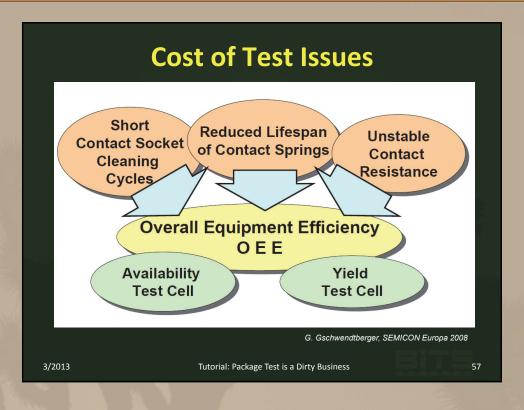
55

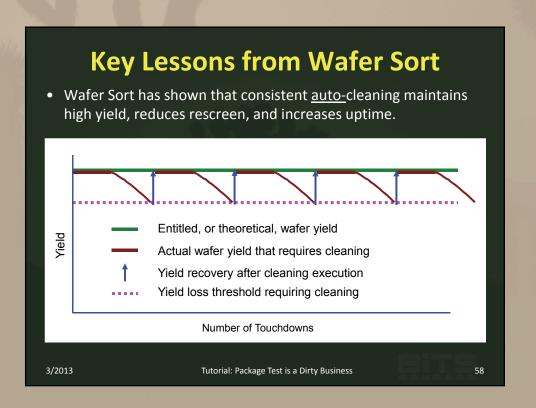
Improvement Opportunities

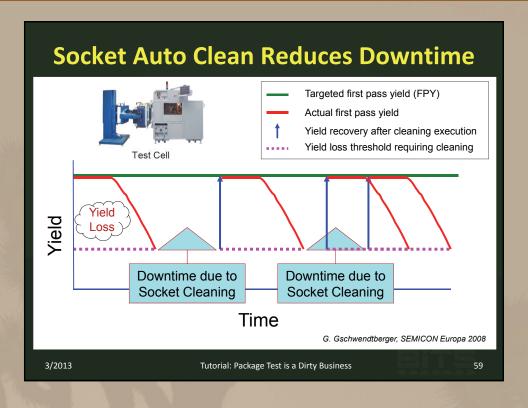
- Benefits for optimized OEE can always be found in one of the following "buckets"
 - Breakdown
 - Setup
 - Downtime
 - Speed loss
 - Small stops
 - Quality

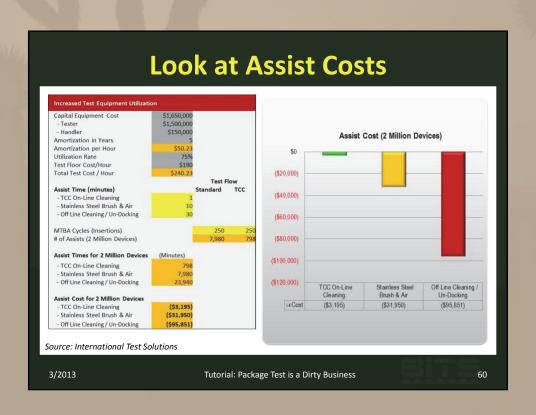
3/2013

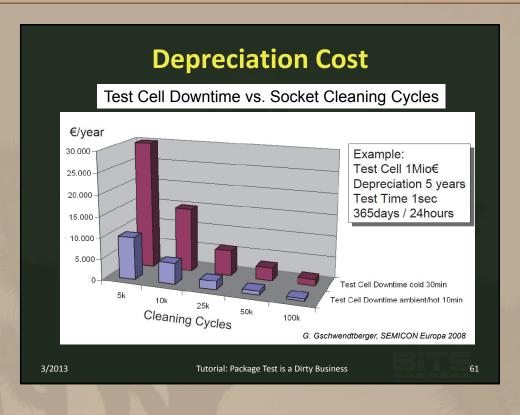
Tutorial: Package Test is a Dirty Business

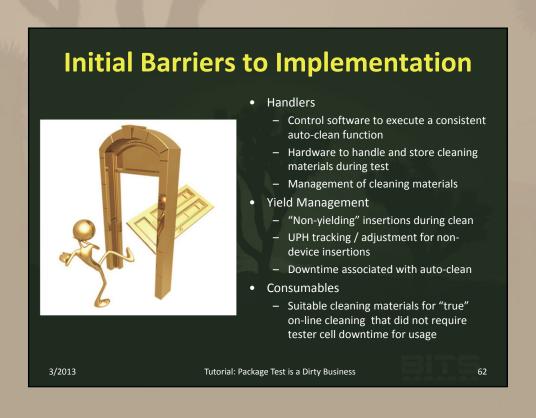










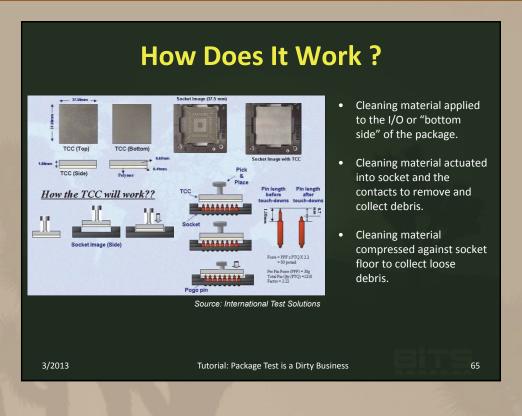


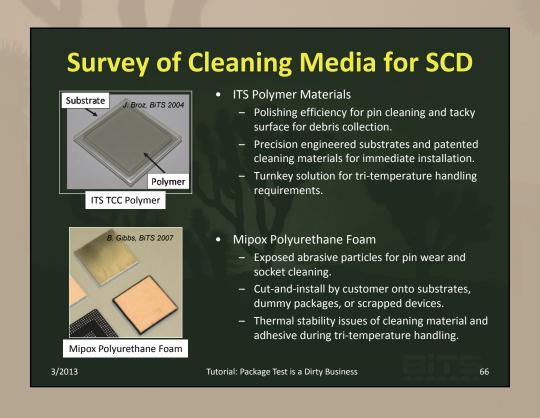












Survey of Cleaning Media for SCD



3M Lapping Film



- Abrasive media adapted from other industries
- Debris generation from dislodged particles and
- Removes probe tip material, changes tip shape, and exposes pin base metal
- Cut-and-install by customer onto substrates, dummy packages, or scrapped devices.



- 3M Sanding Sponge
 - Abrasive media adapted from other industries
 - Cut-and-install by customer onto substrates, dummy packages, or scrapped devices.

3/2013

Tutorial: Package Test is a Dirty Business

Cleaning Material Applied to Plunger



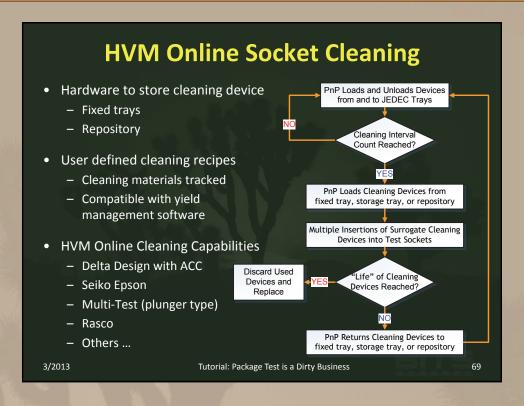


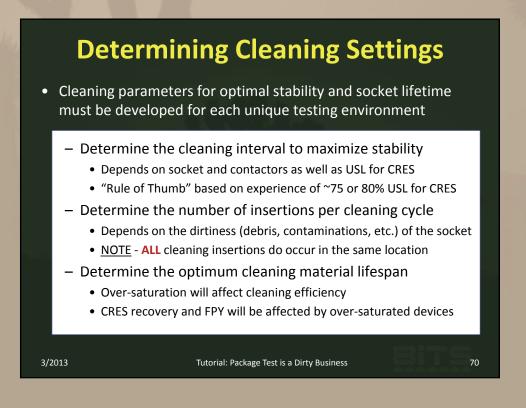
Courtesy of Multitest

- Multitest patented design for a cleaning lead-backer on plunger side.
- Available on MT9510 and MT9510XP
 - simple solution to integrate
 - conversion kit part only
 - no risk of mixing (cleaning devices / production lot)
 - individual setting of cleaning parameter
 - setup is part of device recipe

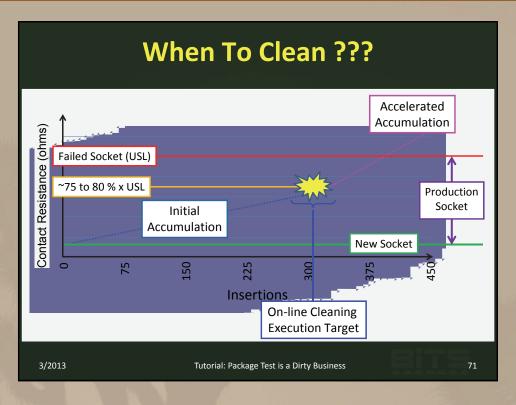
3/2013

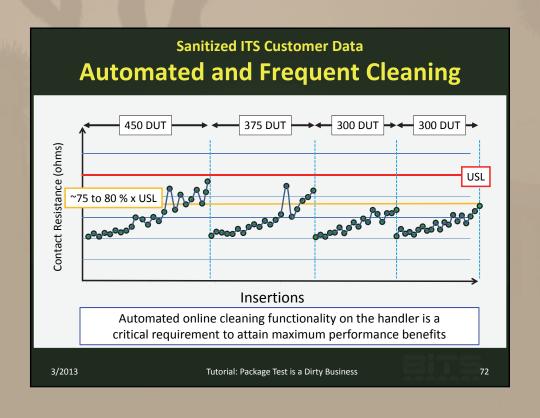
Tutorial: Package Test is a Dirty Business

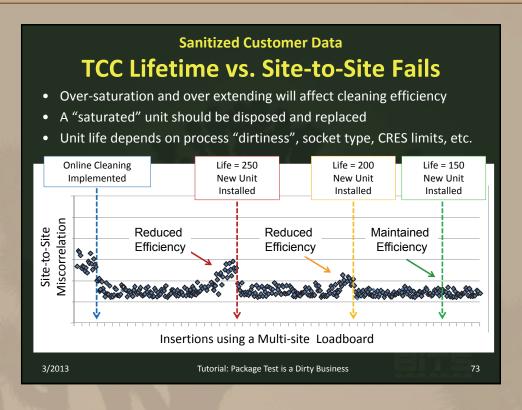


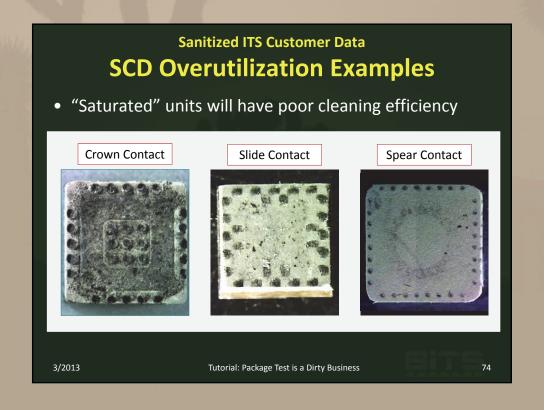














Online Socket Cleaning Implementation a.k.a. Auto Contact Cleaning (ACC)

3/2013

Tutorial: Package Test is a Dirty Business

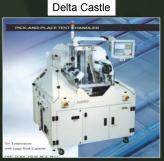
75

Delta Design PnP Handlers

- Delta Design worked with Texas Instruments on automated contact cleaning (ACC) for "true" online cleaning function.
- Byron Gibbs (TI) and Kevin McNamara (Delta) "Auto Contact Cleaning Engineering Study Applied To Package Test", BiTS 2007



Delta Edge



Source: Delta Design

3/2013

Tutorial: Package Test is a Dirty Business



MATRIX Handler



23 Customer Sites ~ 60% Tri-Temp **Multiple X32 Customers**



- 10,000 UPH to 16,000 UPH
- **Auto Contactor Cleaning**
- Temp range: -55C to +175C
- · Jedec tray based packages sized 3×3mm up to 51×51mm
- Productivity:
 - Continuous Load/Unload
 - Auto Re-Probe and Re-test
 - Lot Cascading
- Various V-Core options
- Modular Architecture

Source: Delta Design

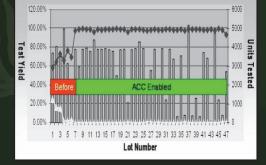
3/2013 Tutorial: Package Test is a Dirty Business

Cleaning Efficiency

- Proprietary "Auto-Contactor Cleaning" reduces retest
- Predictive maintenance to avoid downtime (Available in 2013)
 - Monitor machine performance
 - Identify corrective actions during assist



Before Cleaning



3/2013

Cleaning 75% of loose debris removed

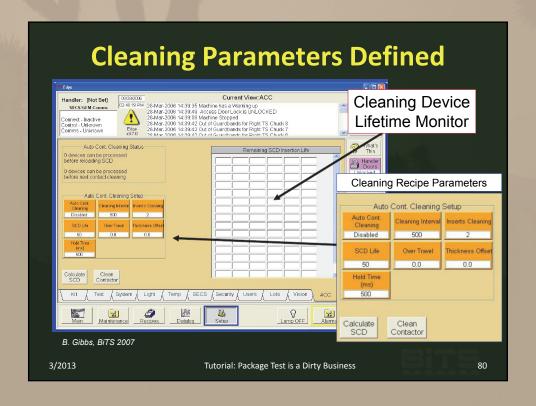
After

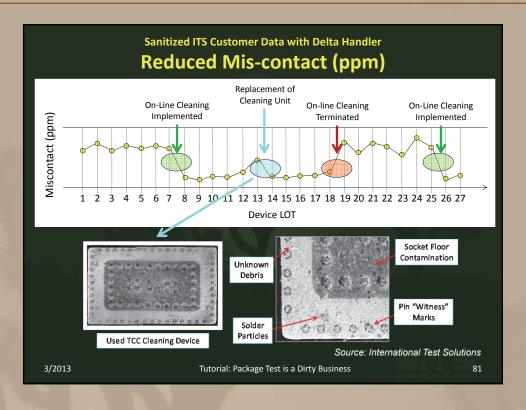
Tutorial: Package Test is a Dirty Business

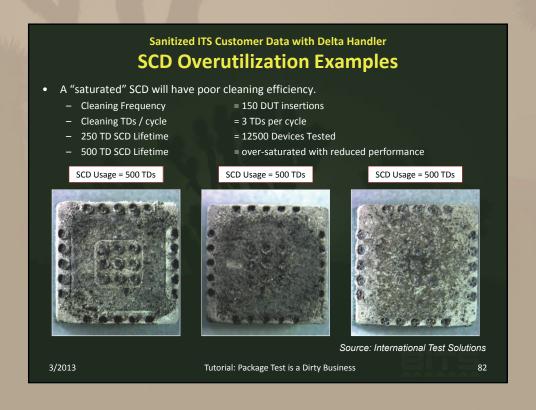
Source: Delta Design











Advantest

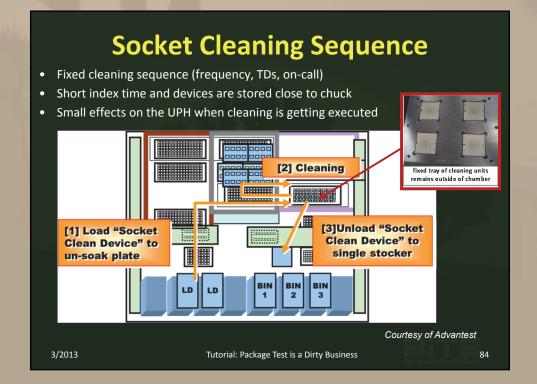


- High-throughput device handler for volume testing of BGA, CSP, QFP, etc., package types.
- 16-device parallel test capable of high throughput of 18,500 UPH
- 3 seconds test time or less for reduced Cost of Test
- Wide temperature range -40C to +125C
- Soft Touch Handling mechanism for a high-accuracy test environment.

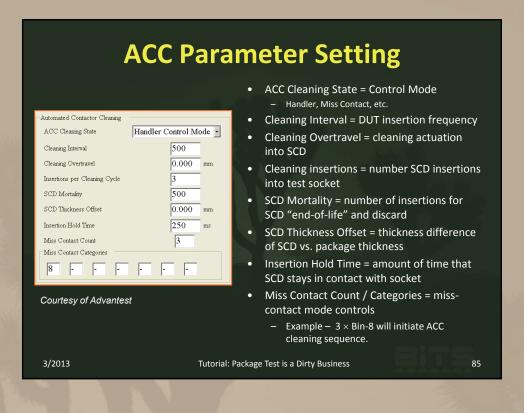
Courtesy of Advantest

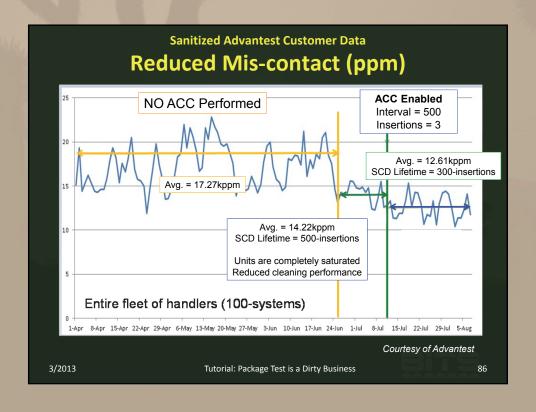
3/2013

Tutorial: Package Test is a Dirty Business

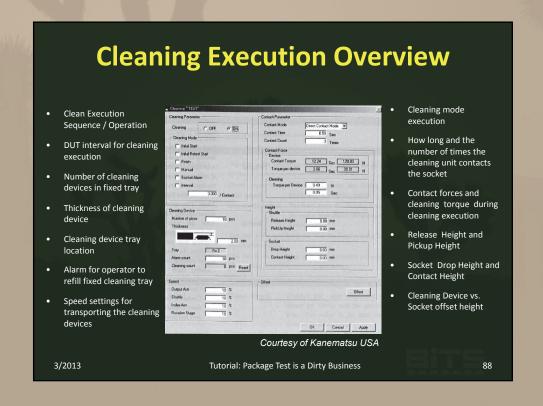




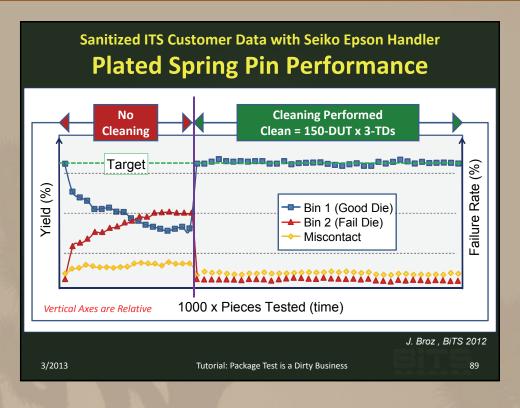


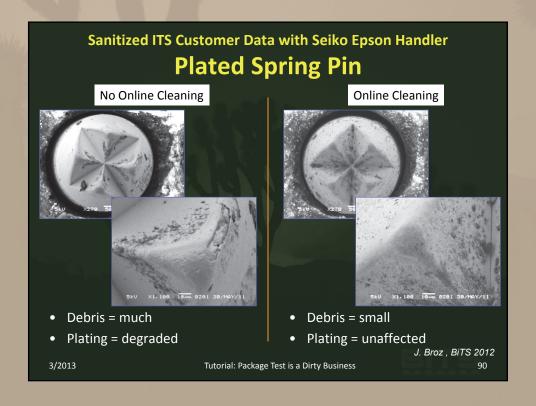




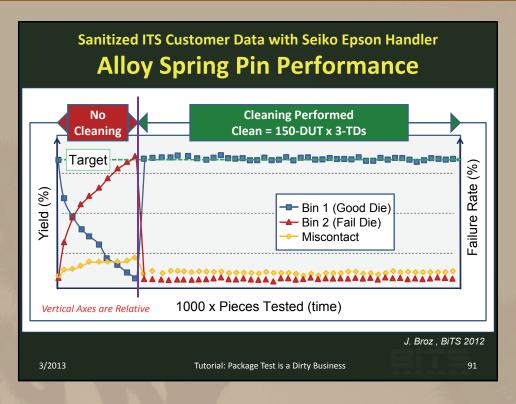
















MultiTest PnP Handler

- X8 contact site (MT9510) and X16 (MT9510XP)
- Tri-temp capable from -55 to +175°C
- Throughput up to 5,300 UPH
- Packages from 3×3mm to 70×70mm
- Installed MT9510 base > 600 systems



- Fully automated concept which does not require opening, undocking, or down-time during execution.
- User can define preventative cleaning execution to perform the cleaning before yield loss occurs without significant throughput effects
- No modifications to handler base unit and hardware can be installed into existing equipment.

Courtesy of Multitest

3/2013

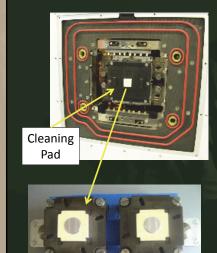
MT9510

MT9510XP

Tutorial: Package Test is a Dirty Business

93

Auto Cleaning Implementation



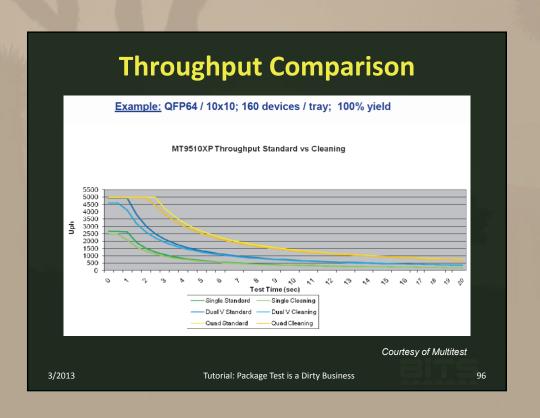
- Cleaning plunger is installed onto one of the 4 rotary plunger sides.
- Cleaning pad is affixed onto the plunger head so there is no risk of sorting the cleaning devices into an output BIN.
- User programs the cleaning recipe for each device as part of the setup process.

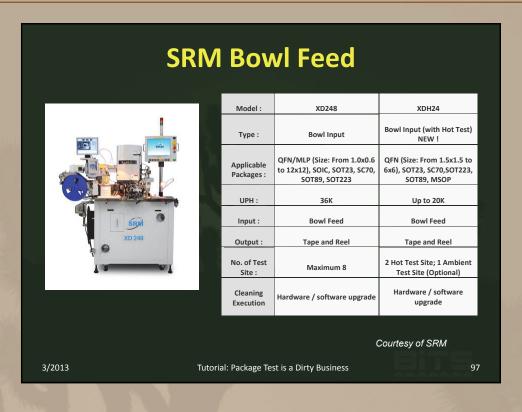
Courtesy of Multitest

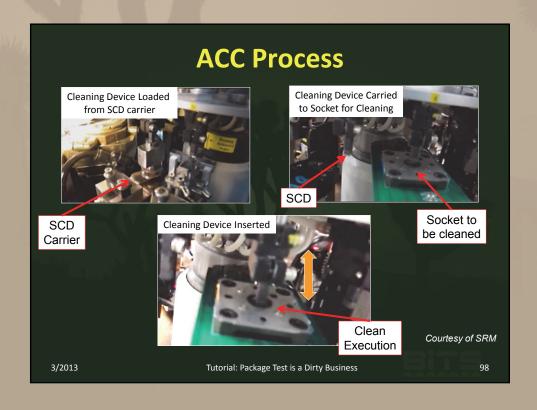
3/2013

Tutorial: Package Test is a Dirty Business







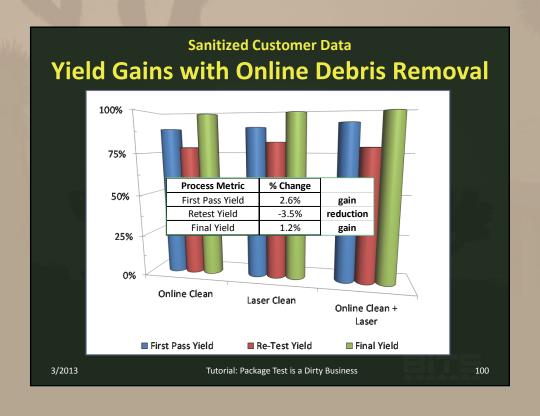


Laser + Online Cleaning

- Problem Overview
 - Periodic laser cleaning of sockets has been shown effective for socket performance recovery and contactor maintenance
 - Handlers must be idled (although socket is not removed) to implement manual laser cleaning (~10-min to 30-min)
 - Debris accumulation from packages does create contact issues
- Objectives for Process Improvement
 - Implement regular online cleaning to reduce debris buildup
 - Supplement laser cleaning to further improve yield metrics.
 - Extend the interval between laser cleaning operations.

3/2013

Tutorial: Package Test is a Dirty Business



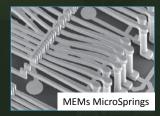


Auto Cleaning as a Technology Enabler

- Historically, socket and pin maker have focused on technologies that are robust and do not require frequent cleaning (i.e., minimal handler downtime).
- Auto-Cleaning is an enabler for "new pin technologies" which can be engineered with critical performance properties (metalluargy, tip shape, scrub characteristics, etc.) to optimize test practices.
- Lessons from wafer sort ... cost of test reduction requirements + effective online clean enabled proliferation of MEMS technologies.







3/2013

Tutorial: Package Test is a Dirty Business

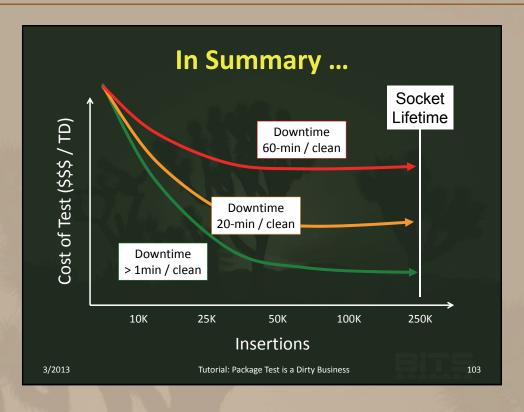
101

In Summary / Conclusion ...

- Offline socket cleaning is a "tried and true" method for maintaining sockets; however, handler downtime affects HVM production and increased CoT.
 - Non-optimized cleaning processes compromise test results, reduce test hardware life, affect throughput, and affect equipment up-time.
- Automated handler driven cleaning clearly improves first-pass yield, reduces rescreening rates, and improves equipment utilization.
 - Development of customized online cleaning devices can provide the high volume manufacturer a substantial competitive advantage.
- Users expect and demand processes that increase overall performance metrics to maximize OEE and reduce the overall cost of test.
 - With auto-cleaning, an HVM test-floor with 20 testers can increase utilization by 5% and basically have a "free" test cell for reduced cost of test.

3/2013

Tutorial: Package Test is a Dirty Business





Acknowledgements ...

- BiTS Workshop Chair (Fred Taber) and Program Committee
- ITS WW Applications Team ...
 - Bret Humphrey (TCC Product Manager)
- ITS Technical Partners ... THANKS!
 - Customers and technologists that must unfortunately remain "nameless".
- Alphabetical contributors ...
 - Advantest: Brad Emberger and David Komma
 - Delta Design: Cristina Schafer and Roger Hopkins
 - IMT Co. Ltd.: Rocky Lee and Nick Gullet (SemiConsultants)
 - Multitest: Barbara Loferer and Valts Treiberg
 - Seiko Epson: William Mowry (Kanamatsu USA)
 - Texas Instruments: Byron Gibbs
 - T.I.P.S. GmBH: Dr. Rainer Gaggl
 - SRM: China service team

3/2013

Tutorial: Package Test is a Dirty Business

105

Learn about Wafer Sort

- IEEE SW Test Workshop 2013
 - http://www.swtest.org
 - Abstract submission is open!
 - San Diego, CA, for June 9 to 12



 IEEE / CPMT sponsored probe technology forum where attendees come to learn about recent developments in wafer sort industry, exchange ideas, and get some answers to crtical technical challenges.

3/2013

Tutorial: Package Test is a Dirty Business

About the Author



Jerry Broz, Ph.D. VP World Wide Applications International Test Solutions Reno, NV 89502

Jerry Broz, Ph.D., has been the Applications Engineering Team Leader and VP of Applications at International Test Solutions since 2003. Dr. Broz is responsible for the ITS branch office teams located in Taiwan, Korea, Japan, China, and Singapore that are focused on optimal online cleaning solutions for wafer sort and package test. Previously, Dr. Broz was a Member of Technical Staff with the Worldwide Probe Development Team at Texas Instruments, Inc. He has authored numerous publications and presentations in the areas of wafer level test, package test, and IC packaging. Dr. Broz holds a number of US and International patents as well as several pending patent applications related to wafer sort, package test, and front-end processes. Dr. Broz earned a Ph.D. in Mechanical Engineering from the University of Colorado at Boulder and has over 20 years of experience in various high volume manufacturing and applied research environments.

Dr. Broz is the General Chair for IEEE SW Test Workshop and a Sr. Member of the IEEE as well as an IEEE Golden Core member. The SW Test web site http://www.swtest.org is an on-line repository for many probe technology presentations.

3/2013

Tutorial: Package Test is a Dirty Business





