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TEST-IN-TRAY PERSPECTIVE by Dr. Thomas Di Stefano President Centipede Systems, Inc.

ABSTRACT

Advances in Test-in-Tray technology have potential to improve greatly the productivity of burn-in And test for semiconductor electronics. With the growing complexity of IC devices, back end test operations consume an ever increasing portion of manufacturing cost. This trend cannot continue. Testin-tray (TnT) enables full "lights-out" automation through all back-end processes where individual parts are not handled, manually or robotically, until pack and ship. The major wafer fabs have implemented full lights out automation years ago using the FOUP as a standard wafer carrier. A standard tray carrier serves the same role for back end processes. Automation equipment can be standardized around TnT for efficiency and a minimum of custom fixturing.

A recently announced FlexFrame carrier overcomes problems that have hindered testing in tray or strip format. Although strip testing greatly increases test throughput where applicable, the method is limited to certain specific devices that can be tested in strip format. Further, dimensional stability, part placement, and cost of complex strip handlers have stunted adoption. The FlexFrame carrier overcomes these problems by holding devices in a dimensionally stable tray that is CTE matched to its mating contactor socket. Now, the placement and number of devices in the tray may be matched to ATE test capacity. The FlexFrame carrier allows easy alignment and insertion/extraction from the mating contactor socket, greatly simplifying the process and reducing the cost of automation.

Test-in-Tray methods are applicable to a full range of semiconductor devices from WLP and TSV chips to complex BGA packages and MEMS sensors. TnT has the full potential to revolutionize the test industry – and the change is needed now. Open standards are essential to support a great opportunity for all.

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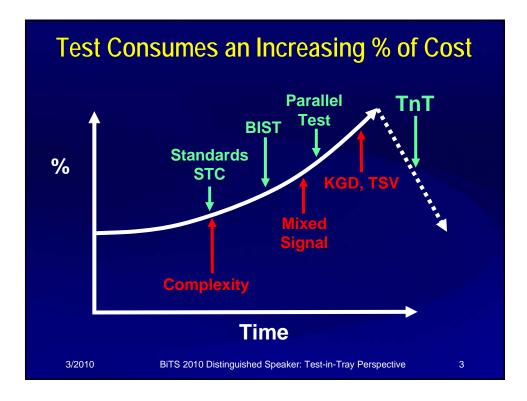
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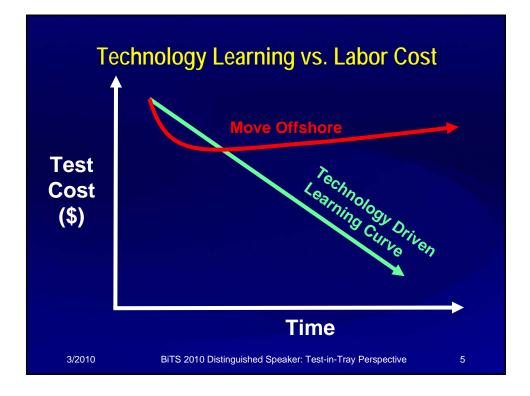






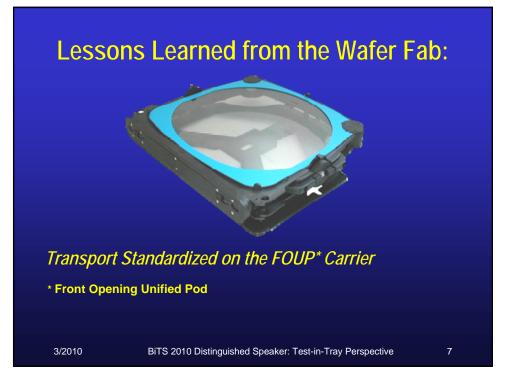


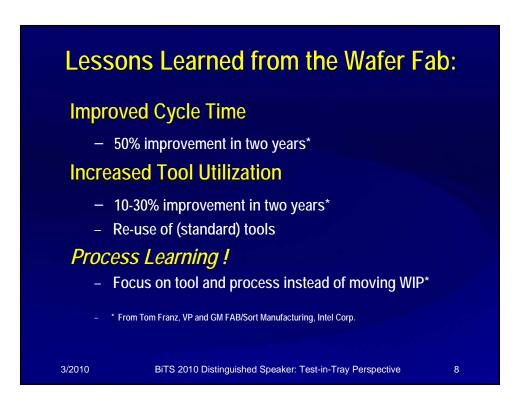










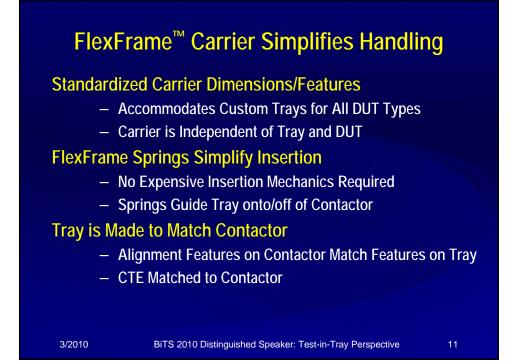






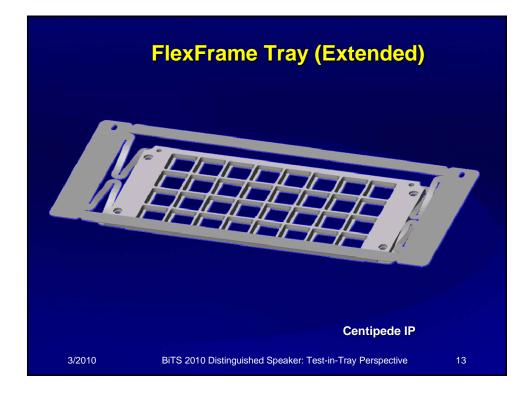


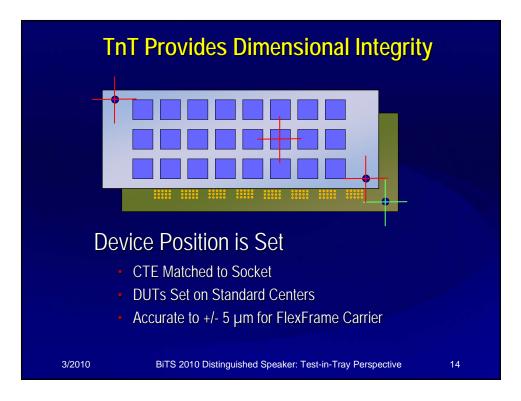




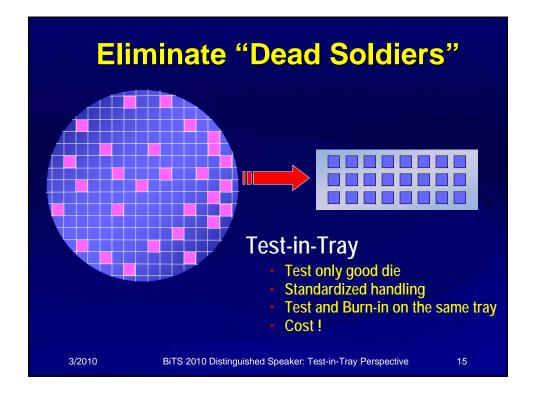


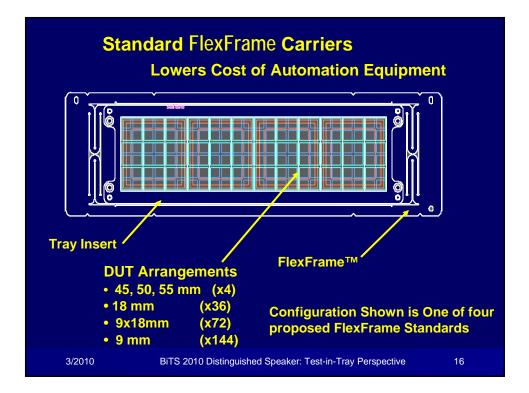




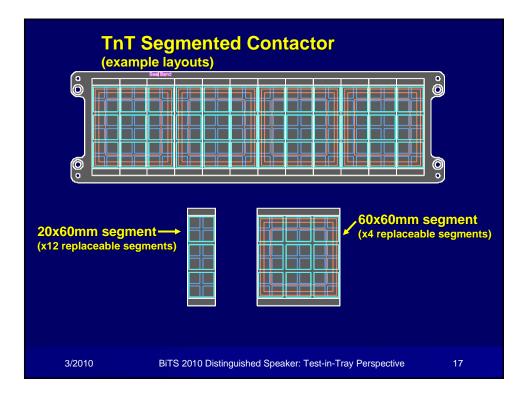


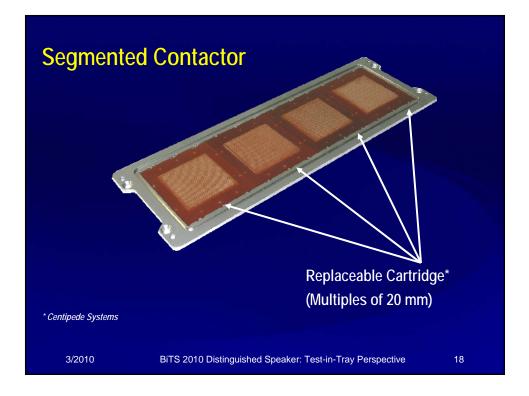




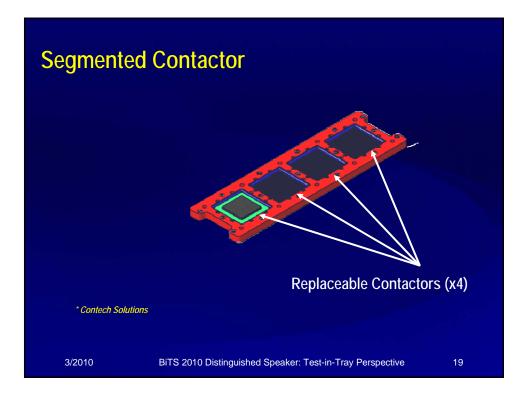


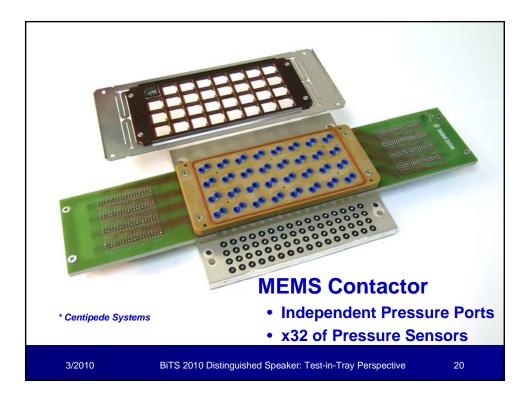














BGA to 60mm x 60mm QFN, QFP,TSOP, ... CSP, WLCSP, ... MEMS – All Types WLP TSV Flip-chip

- Stacked dice
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