



ARCHIVE 2008

SOCKETS: ON THE FLOOR, IN THE LAB

"Contactor Selection Criteria Overview for RF Component Testing" James Migliaccio, Ph.D RF Microdevices

"Design Optimized, Manufacturing Limited -A 250W Thermal Solution" Trevor Moody, Kevin Hanson, Rick Davis Antares Advanced Test Technologies

"Test Socket Tracking: From Cradle to Grave" Angelo Giaimo IBM Corporation

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Sockets: On the Floor, In the Lab

RFMD

Contactor Selection Criteria Overview for RF Component Testing

2008 Burn-in and Test Socket Workshop March 9 - 12, 2008



James Migliaccio, Ph.D. RFMD







Customer View of Contactor Supplier

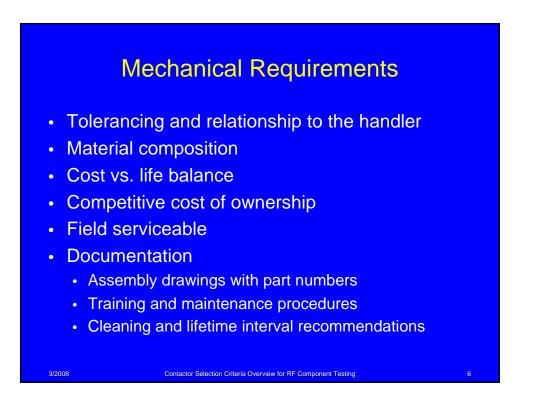






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RFMD PA TEST
 Tried/used many different contactors for RF test:
Spring Probes
Sliders
Rockers
Interposers
Particle Interconnect
Fibrous Gold Balls
Cantilever
 Most consist of a plastic body holding small metal pieces in place
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Sockets: On the Floor, In the Lab

Electrical Requirements

- Typical DUT has low pin count
- Mix of RF & DC pins
- Current requirement can exceed 2A on a pin
- May need to have external components close to the DUT
- Minimal ground inductance preferred
- PCB Real-estate concerns
- RF performance
- ESD

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Other Considerations

Contactor Selection Criteria Overview for RF Component Testing

- Acquisition Costs
- Existing Relationship
- Anything New and Innovative
- Unique DUT or close relative of existing product
- Custom or standard package?
- Part pad composition
- Accelerated mechanical life testing
- NDA

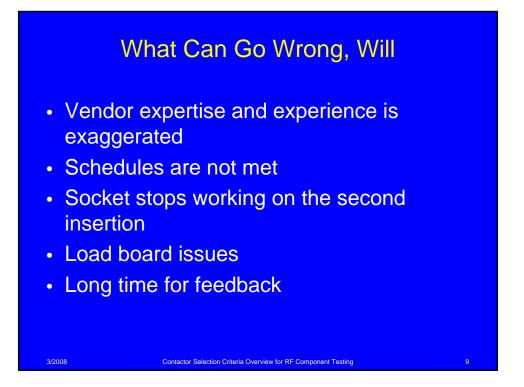
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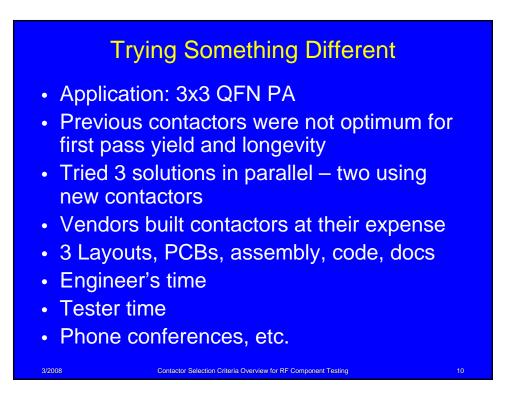
- Changing design is very painful
- Cres is not an important data point. We measure RF performance directly and use an SPC system to determine performance.
- Will go to production

Contactor Selection Criteria Overview for RF Component Testing



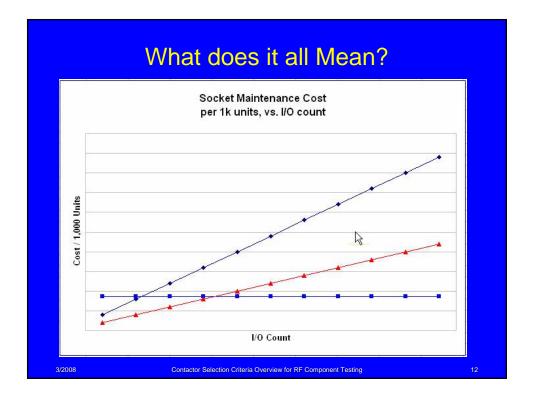
Sockets: On the Floor, In the Lab







The Big One -	Cost
 Initial development cost Sockets, load boards Time Production Cost Initial Replacements, spares, training Down time - yield Re-use Know the alternative – price & Service/Quality/Reliability are equalizers 	
- 3/2008 Contactor Selection Criteria Overview for RF Com	ponent Testing 11





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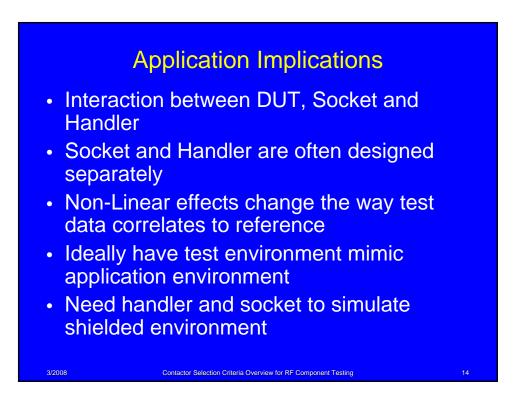
Sockets: On the Floor, In the Lab

From Socket to Application

- Socket maintenance cost is a function of contactor lifetime and repair cost
- This chart ignores the cost of tester down time, labor, spares and first pass yield loss
- Although lifetime cost is a major factor, performance is king
- Not all performance variation is associated with the socket

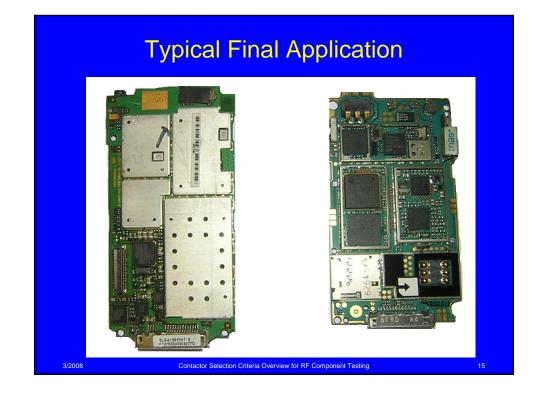
Contactor Selection Criteria Overview for RF Component Testing

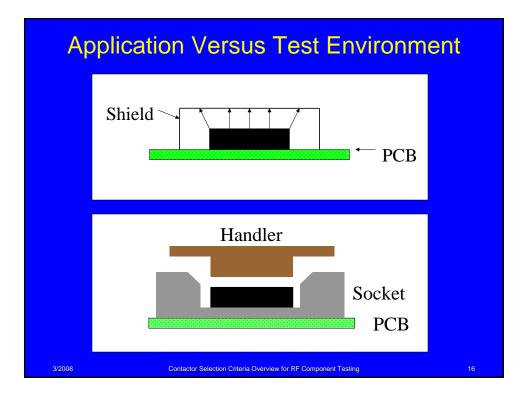
Final application can change everything





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Design Optimized, Manufacturing Limited – A 250W Thermal Solution

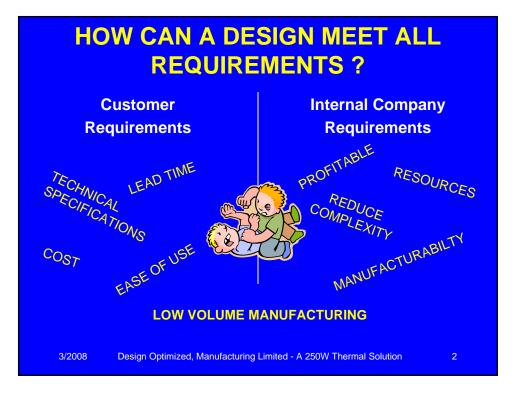
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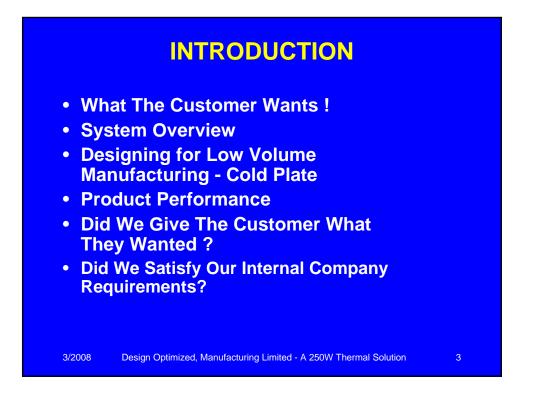
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Advanced Test Technologies





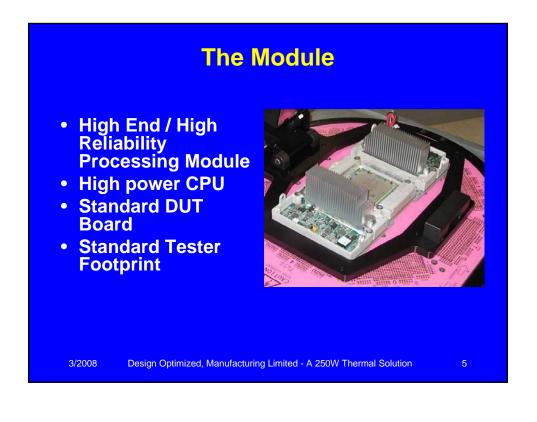
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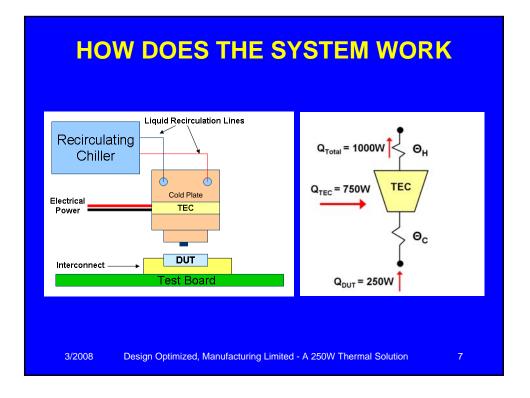


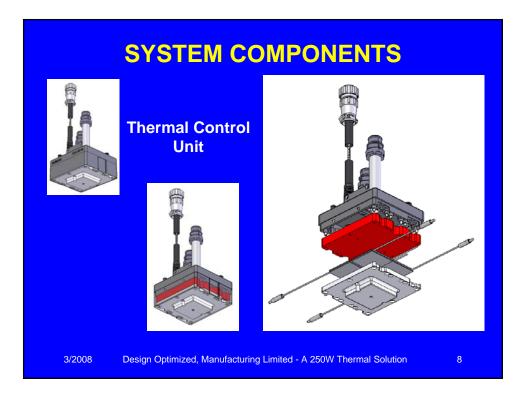
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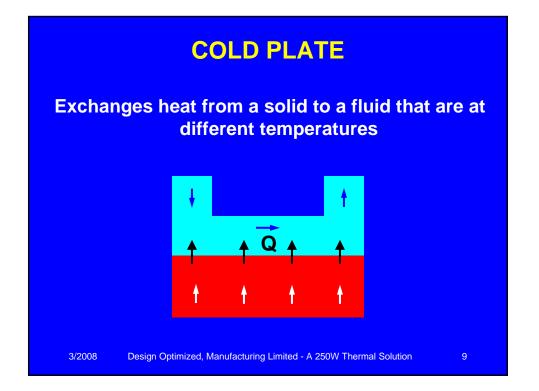


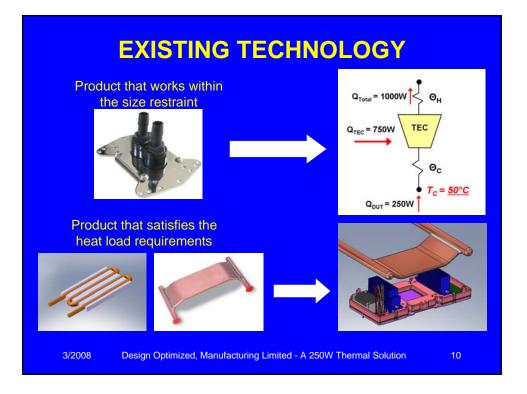






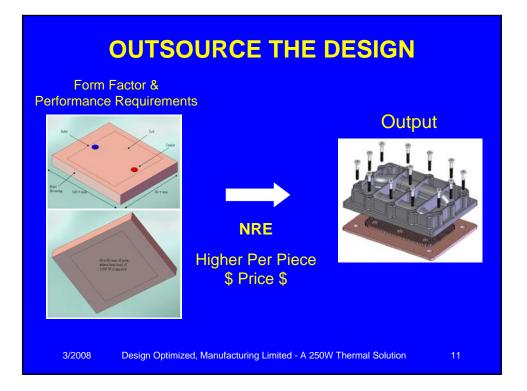
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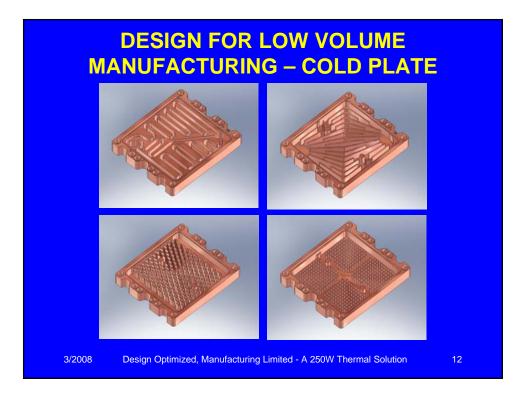






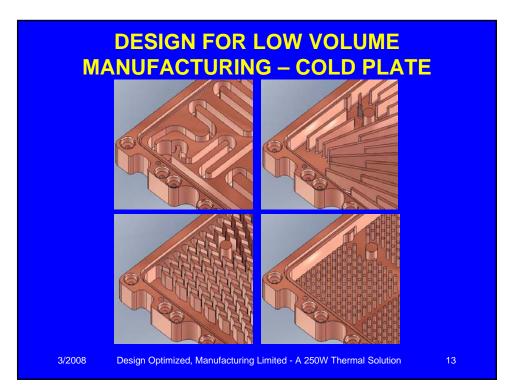
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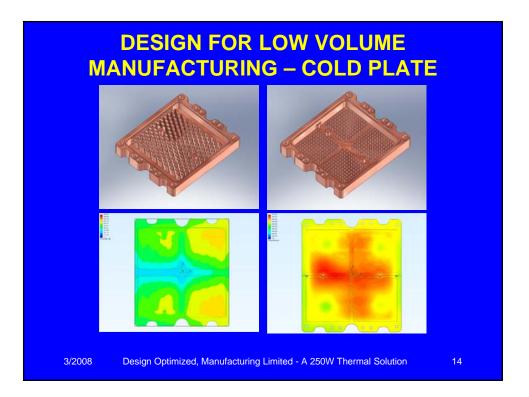






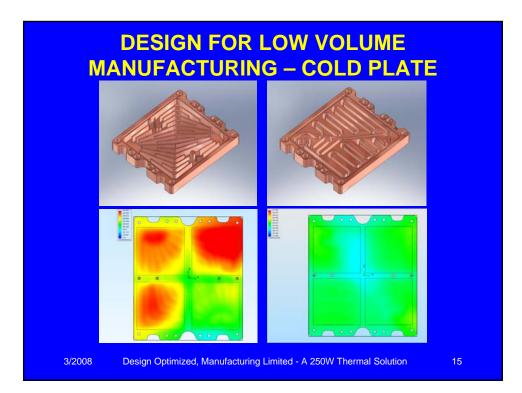
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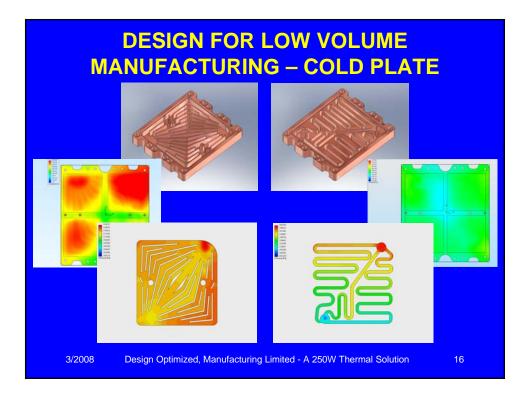






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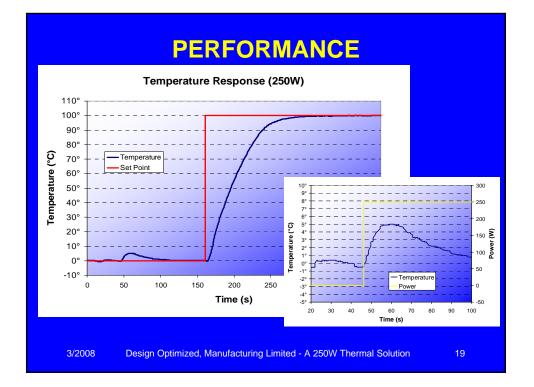
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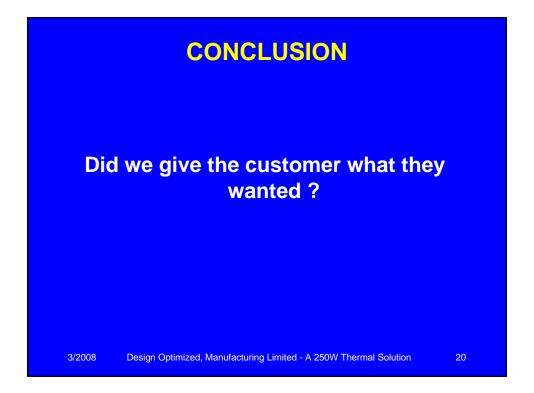
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Sockets: On the Floor, In the Lab







Sockets: On the Floor, In the Lab

Test Socket Tracking: From Cradle to Grave

2008 Burn-in and Test Socket Workshop March 9 - 12, 2008



Angelo Giaimo IBM Corporation



HOW DO YOU KNOW ?

- In today's dynamic test mfg environment:
 - How do you know that the Front End Hardware that you just put on the tester is good?
 - How do you know that you won't be wasting precious tester time to figure it out?
 - Can you afford more Testers?
 - Want to lower the cost of test?

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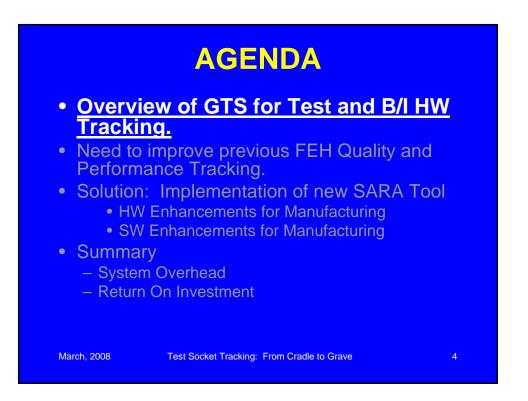
Test Socket Tracking: From Cradle to Grave

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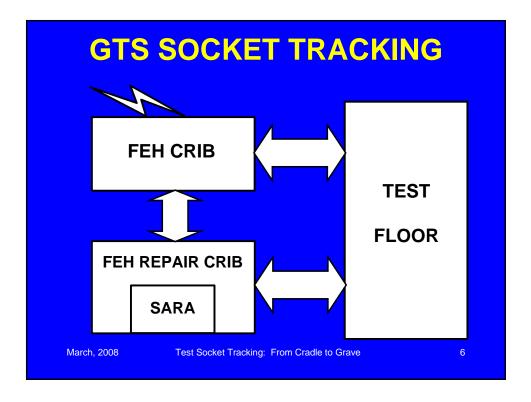














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SARA: BACKGROUND

- SARA = <u>Socket Analog Resistance Analyzer</u>
- Metrology Tool used to make accurate mass measurements of Socket and Probe Assemblies.
 - Architected for 2,209 usable I/O's (47X47 Array)
 - Pseudo-4 Point Measurements (BiTS 2000 Paper)
- Originally designed as an Engineering Tool.
 - Lab/Development Environment
 - Used for the development, evaluation and test of Test and B/I Sockets.
- HW and SW upgrades for MFG use.

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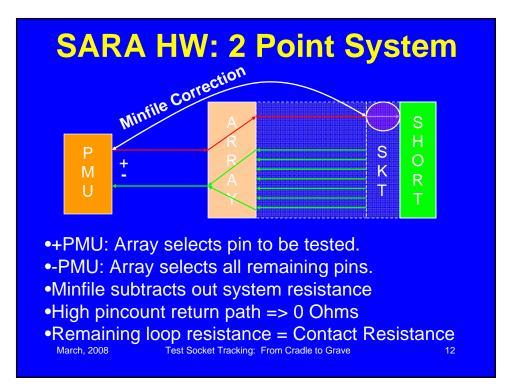
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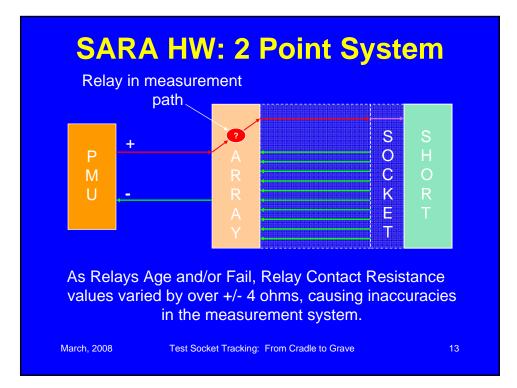


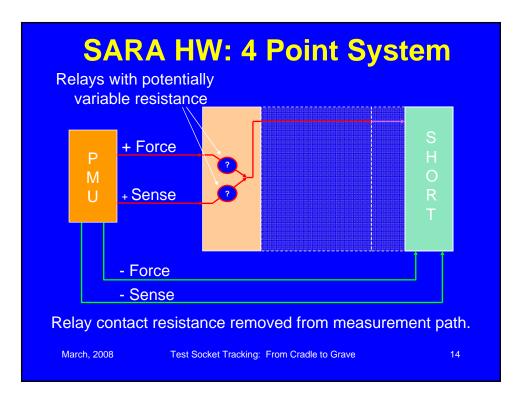
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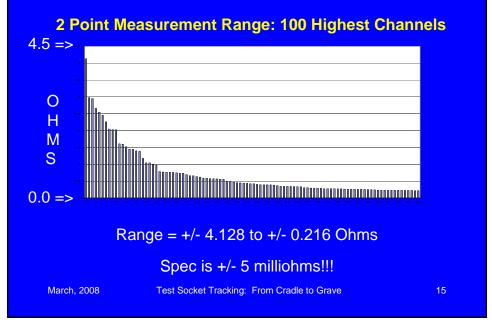


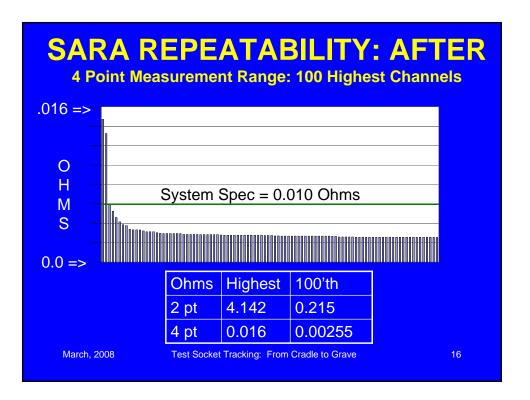






SARA REPEATABILITY: BEFORE





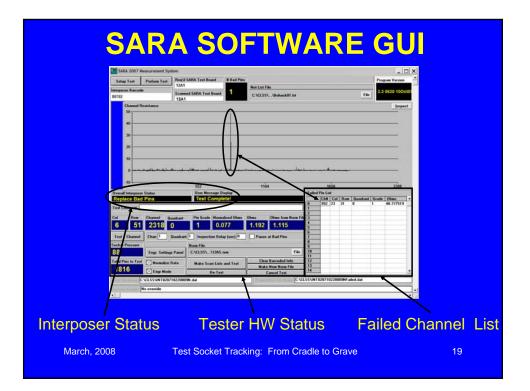


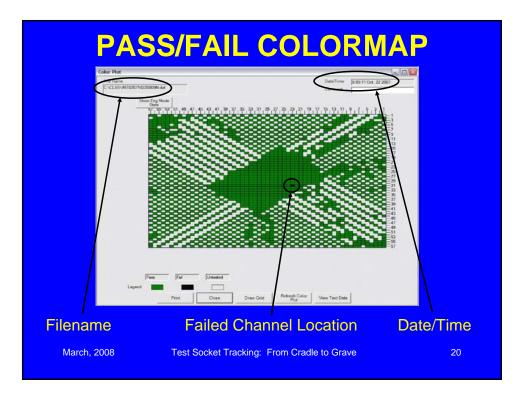






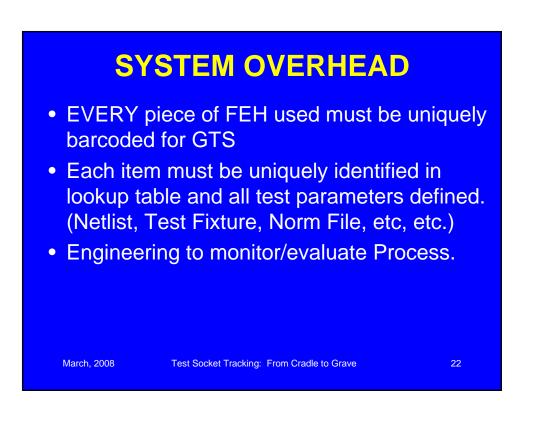
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RETURN ON INVESTMENT

- Tester Utilization Savings
- Yield Loss reduction due to defective FEH
- Yield Loss reduction due to downbinning.
- Reduced Manufacturing Operator Labor
- Reduced Test Floor Maintenance Labor

Test Socket Tracking: From Cradle to Grave

